STPP0100, STPP0101

Laser pico projection specific image-processor

Data brief – preliminary data

Features

- Supports TTL/CMOS video input
  - 24-bit RGB/YUV raster video input up to 80 MHz pixel clock
  - ITU-R BT.656 input
- Supports MIPI DSI video input
  - Up to four lanes, up to 800 Mb/s per lane
- Supports video formats up to 720p (1280 x 720) in 16/9 format or XGA (1024 x 768) in 4/3 format
- Integrated video processor
  - Image scaler
  - Color space conversion
  - Image color adjustment
  - Geometrical compensation
- ARM® Cortex™ M3 integrated MCU
- 100% brightness uniformity support
- Integrated mirror control
- Integrated laser driver interface
- Hardware laser safety mechanism
- Specific interface for Opto Electronic Integrated Circuits (OEIC)
- Dual DACs for vertical ST MEMS (Micro-electro mechanical systems) mirror control
- Four input ADC used for mirror control
- Peripherals: UART, JTAG, SPI, GPIO, I2C master, I2C slave
- Low power operation
  - Less than 375 mW @ 720p
- Low power MIPI sleep mode
- Package: TFBGA (8 x 8 mm, 242 balls, 0.4 mm pitch)

Applications

Consumer applications: Phones, tablets, PMP integrating HD or non-HD laser based pico projector, standalone HD laser pico-projector, digital camera (still and video), user interfaces

Automotive applications: Cars head up display, navigation systems, and instrumentation displays

Professional applications: Video conferencing systems, medical, emergency interfaces.

This is preliminary information on a new product now in development or undergoing evaluation. Details are subject to change without notice. For further information contact your local STMicroelectronics sales office.
1 Description

The STPP0100 and STPP0101 chips are innovative, integrated video processor and application controllers designed for laser pico-projection systems and HD laser pico-projection systems. They support video inputs of up to XGA (1024 x 768) in 4/3 format or 720p HD (1280 x 720) in 16/9 format.

The STPP0100 and STPP0101 ICs support a MIPI DSI video interface. They allow seamless integration into mobile equipment such as phones, tablets, digital still camera or video camera, gaming consoles, and MP3 players amongst others. The MIPI DSI interface supports DPI mode, four lanes 800 Mb/sec bit rate to support up to 720p video or picture resolution. It also supports TTL/CMOS input for standalone pico-projector type applications.

The STPP0100 and STPP0101 chip series takes a video input stream, renders it to modulate the lasers with the correct color/timing, outputs the results on a laser diode driver interface, and generates the ST MEMS mirror driver commands.
2 Benefits

The STPP0100 and STPP0101 ICs include a feature called dynamic white balance to compensate for temperature induced color shift in the tri-laser projector. These SoCs enable the HD pico-projection to have close to 100% brightness uniformity.

Also, available is an integrated set of communication interfaces to interconnect with the rest of the application. The communication interfaces count as 1x SPI bootable, I2C (1 slave and 1 master), UART, and GPIO. In addition to the communication interfaces, STPP0100 and STPP0101 includes a selectable quad input 10-bit ADC for additional control of the application.

Other benefits include:
- Seamless integration in mobile devices
- Fully dedicated and integrated SoC
- Integrated eye safety mechanism
- HD video input support
- Arm Cortex M3
- Low power in mission mode (<375 mW typical for 720p)
- Low power MIPI standby mode (<650 µW)
3 Main features

- Resolution 4/3 up to XGA, 16/9 up to 720p @ 60 FPS. Any lower resolution is supported natively—no scaling is required
- Video input support
  - up to XGA 60 Hz max in 4/3 format
  - up to 720p 60 Hz max in 16/9 format
  - 24 bit RGB/YUV raster scan video up to 80 MHz pixel clock
  - MIPI DSI
  - ITU-R BT656
- MIPI DSI/DPI and parallel video input supporting ITU-R BT.656 with configurable color depth up to 24 bits
- Supporting MEMS mirrors
- Integrated mirror/lasers Analog Front End (AFE)
- Integrated ARM Cortex-M3 microcontroller and peripherals for autonomous operation
- Package TFBGA- 8 x 8 mm, 242 balls, 0.4 mm pitch
- Low power (375 mW typical for 720p)
- Deep sleep (no data retention) (<650 µW)

3.1 ARM Cortex-M3 microcontroller subsystem

- ARM Cortex-M3 standard core with bootstrap ROM
- Software loaded from external SEEP or application processor by high speed SPI
- IIC / SPI control interface
- GPIO user interface

3.2 Video pre-processing

- Color space/video format conversion
- Color inversion/complementary color transform

3.3 Laser modulation

- Geometrical interpolation engine supporting MEMS mirrors ensures full cancellation of any barrel or pin cushion distortion effects
- Digital keystone correction
- Configurable brightness uniformity compensation, up to 100% uniformity
- Color temperature (white balance) control
- Laser linearity compensation
- Brightness/contrast and gamma corrections
3.4 **MEMS mirror interface**
- Supporting MEMS resonance mode horizontal mirror, and linear mode vertical mirror
- Mirror control interface including:
  - Dual channel DAC with differential output
  - Quad input DAC for mirror control feedback
- Vertical mirror drive LUT
- Horizontal mirror pulse generator

3.5 **Laser interface**
- Support of RGB direct laser diodes
- Lasers are controlled by software running on ARM MCU
- Integrated AFE with differential input to interface with external optical power detector

3.6 **Eye safety monitor**
- Built-in mirror movement monitoring
- External proximity detection support
- System watchdog monitor

3.7 **Additional digital interfaces**
- Multiple configurations supported
- Dedicated laser driver interface up to 150 MHz
- 2 x I²C
- SPI + 1 proprietary SPI
- UART
- GPIO (8 bits)
- JTAG
4 Application overview

Figure 1. STPP0100 and STPP0101 application overview
5 Ordering information

Table 1. Order codes

<table>
<thead>
<tr>
<th>Part number</th>
<th>Description</th>
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<tr>
<td>STPP0101-AA (Generic)</td>
<td>TFBGA 8 x 8 mm</td>
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<tr>
<td>STPP0100-AA (Reserved)</td>
<td>TFBGA 8 x 8 mm</td>
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5.1 ECOPACK®

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK® packages, depending on their level of environmental compliance. ECOPACK® specifications, grade definitions and product status are available at: www.st.com. ECOPACK® is an ST trademark.
## 6 Revision history

Table 2. Document revision history

<table>
<thead>
<tr>
<th>Date</th>
<th>Revision</th>
<th>Changes</th>
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<tbody>
<tr>
<td>21-Nov-2012</td>
<td>1</td>
<td>Initial release.</td>
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