

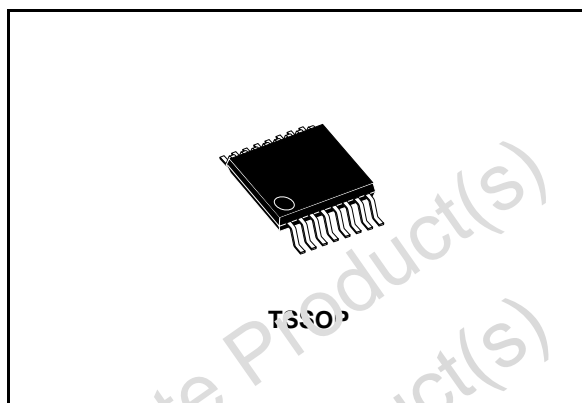
## High speed differential line drivers

### Feature summary

- Meets or exceeds the requirements of ansi TIA/EIA-644 standard
- Low voltage differential signaling with typical output voltage of 350mV and a 100Ω load
- Typical output voltage rise and fall times of 750ps (400Mbps)
- Typical propagation delay times of 1.7ns
- Operates from a single 3.3V supply
- Power dissipation 25mW typical per driver at 200MHz
- Driver at high impedance when disabled or with  $V_{CC} = 0V$
- Pin compatible with the MC3487 and SN65LVD3487
- Low voltage TTL (LVTTTL) logic input levels

### Description

The STLVDS3487 is a quad differential line drivers that implements the electrical characteristics of low voltage differential signaling (LVDS). This signaling technique lowers the output voltage levels of 5V differential standard levels (such as TIA/EIA-4225) to reduce the power, increase the switching speeds and allows



operations with a 3.3V supply rail. Any of the four current mode drivers will deliver a minimum differential output voltage magnitude of 247mV into a 100Ω load when enabled.

The intended application of this device and signalling technique is for point-to-point baseband data transmission over controlled impedance media approximately 100Ω. The transmission media may be printed circuit board traces, backplanes or cables. The ultimate rate and distance of data transfer is dependent upon the attenuation characteristics of the media and noise coupling to the environment.

The STLVDS3487 is characterized for operation from -40°C to 85°C.

### Order code

Part number	Temperature Range	Package	Comments
STLVDS3487BTR	-40 to 85 °C	TSSOP16 (Tape & Reel)	2500 parts per reel

---

## Contents

1	Pin configuration .....	3
2	Logic diagram .....	4
3	Maximum ratings .....	5
4	Electrical characteristics .....	6
5	Test circuit .....	8
6	Typical performance characteristics .....	11
7	Package mechanical data .....	12
8	Revision history .....	15

# 1 Pin configuration

Figure 1. Pin connections

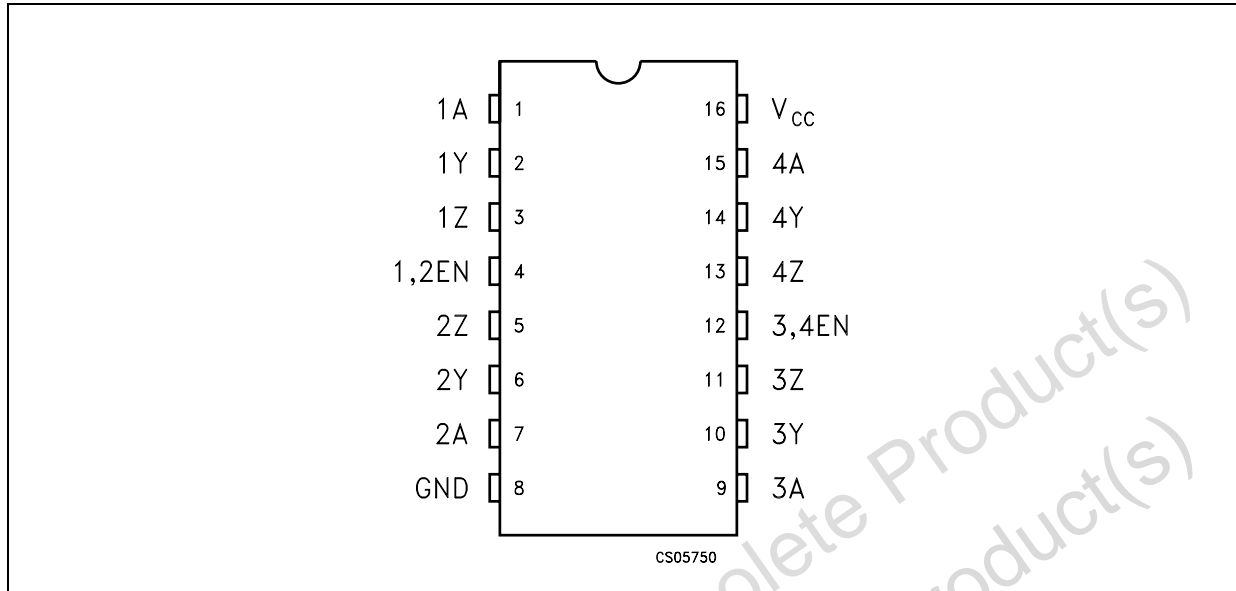


Table 1. Pin description

Pin n°	Symbol	Name and function
1, 7, 9, 15	1A to 4A	Driver inputs
2, 6, 10, 14	1Y to 4Y	Driver outputs
3, 5, 11, 13	1Z to 4Z	Driver outputs
4	1EN, 2EN	Drivers 1 and 2 enable
12	3EN, 4EN	Drivers 3 and 4 enable
8	GND	Ground
16	V <sub>CC</sub>	Supply voltage

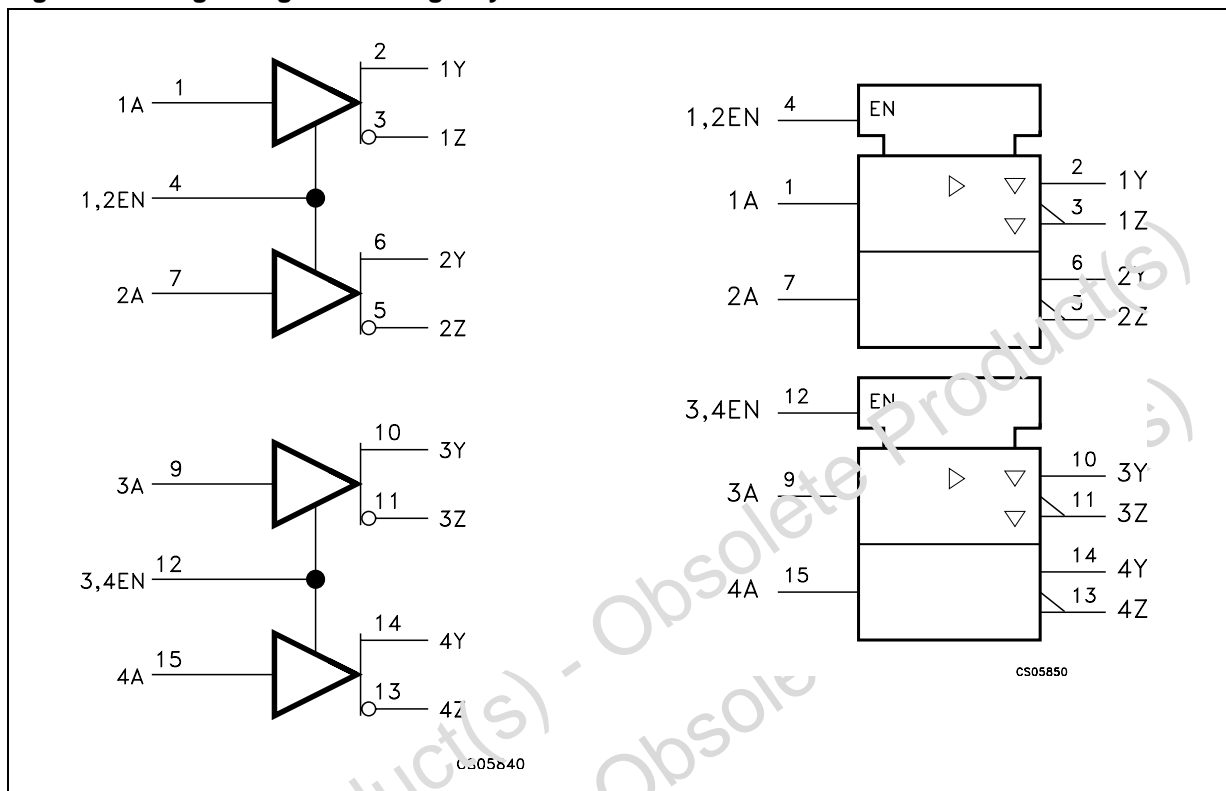
Table 2. Truth table

Input	Enables	Outputs	
		Y	Z
A	EN		
H	H	H	L
L	H	L	H
X	L	Z	Z
OPEN	H	L	H

L=Low level, H=High Level, X=Don't care, Z= High Impedance

## 2 Logic diagram

Figure 2. Logic diagram and logic symbol



### 3 Maximum ratings

**Table 3. Absolute maximum ratings**

Symbol	Parameter	Value	Unit
$V_{CC}$	Supply voltage (Note 1)	-0.5 to 4.6	V
$V_I$	DC Input voltage	-0.5 to ( $V_{CC} + 0.5$ )	V
$T_{stg}$	Storage temperature range	-65 to +150	°C

Note: Absolute Maximum Ratings are those values beyond which damage to the device may occur. Functional operation under these condition is not implied.

Note: 1 All voltages except differential I/O bus voltage, are with respect to the network ground terminal.

**Table 4. Recommended operating conditions**

Symbol	Parameter	Min.	Typ.	Max.	Unit
$V_{CC}$	Supply voltage	3	3.3	3.6	V
$V_{IH}$	High level input voltage	2			V
$V_{IL}$	Low level input voltage			0.8	V
$T_A$	Operating temperature range	-40		85	°C

## 4 Electrical characteristics

**Table 5. Electrical characteristics**

(Over recommended operating conditions unless otherwise noted. All typical values are at  $T_A = 25^\circ\text{C}$ , and  $V_{CC} = 3.3\text{V}$ ).

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
$V_{OD}$	Differential output voltage	$R_L = 100\Omega$ , Fig. 2	247	350	454	mV
$\Delta V_{OD}$	Change in differential output voltage between logic state		-50		50	mV
$\Delta V_{OC(SS)}$	Change in steady-state common mode output voltage between logic state	Fig. 3	1.125	1.2	1.375	V
$V_{OC(SS)}$	Steady-state common mode output voltage	Fig. 3	-50		50	mV
$V_{OC(PP)}$	Peak to peak common mode output voltage			80	150	mV
$I_{CC}$	Supply current	$V_{IN} = 0.8\text{V}$ or $2\text{V}$ , Enabled, No load		11.5	20	mA
		$V_{IN} = 0.8\text{V}$ or $2\text{V}$ , Enabled, $R_L = 100\Omega$		25	35	mA
		$V_{IN} = 0$ or $V_{CC}$ , Disabled		0.3	1	mA
$I_{IH}$	High level input current	$V_{IH} = 2\text{V}$		4	20	$\mu\text{A}$
$I_{IL}$	Low level input current	$V_{IL} = 0.8\text{V}$		0.6	10	$\mu\text{A}$
$I_{SC}$	Short circuit output current	$V_{O(1)}$ or $V_{O(2)} = 0\text{V}$		6.1	-24	mA
		$V_{OD} = 0$			$\pm 12$	mA
$I_{OZ}$	High impedance output current	$V_O = 0$ or $2.4\text{V}$			$\pm 1$	$\mu\text{A}$
$I_{OFF}$	Power OFF output current	$V_{CC} = 0$ , $V_O = 2.4\text{V}$			$\pm 1$	$\mu\text{A}$
$C_{IN}$	Input capacitance			3		pF

**Table 6. Switching characteristics**

(Over recommended operating conditions unless otherwise noted. All typical values are at  $T_A = 25^\circ\text{C}$ , and  $V_{CC} = 3.3\text{V}$ ).

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
$t_{PLH}$	Propagation Delay Time, Low to High Output	$R_L = 100\Omega, C_L = 10\text{pF}$ Fig. 2	0.5	1.4	2	ns
$t_{PHL}$	Propagation Delay Time, High to Low Output		1	1.7	2.5	ns
$t_r$	Differential Output Signal Rise Time		0.4	0.5	0.6	ns
$t_f$	Differential Output Signal Fall Time		0.4	0.5	0.6	ns
$t_{sk(P)}$	Pulse Skew ( $ t_{THL} - t_{TLH} $ )			0.3	0.6	ns
$t_{sk(O)}$	Channel to Channel Output Skew (note1)			0	0.3	ns
$t_{PZH}$	Propagation Delay Time, High Impedance to High Level Output	Fig. 4		5.4	15	ns
$t_{PZL}$	Propagation Delay Time, High Impedance to Low Level Output			2.5	15	ns
$t_{PHZ}$	Propagation Delay Time, High Level to High Impedance Output			8.1	15	ns
$t_{PLZ}$	Propagation Delay Time, Low Level to High Impedance Output			7.3	15	ns

Note: 1  $t_{sk(O)}$  is the maximum delay time difference between drivers on the same device.

2 RS-232 IN to TTL-CMOS OUT (from 50% to 50%)

## 5 Test circuit

Figure 3. Voltage and current definitions

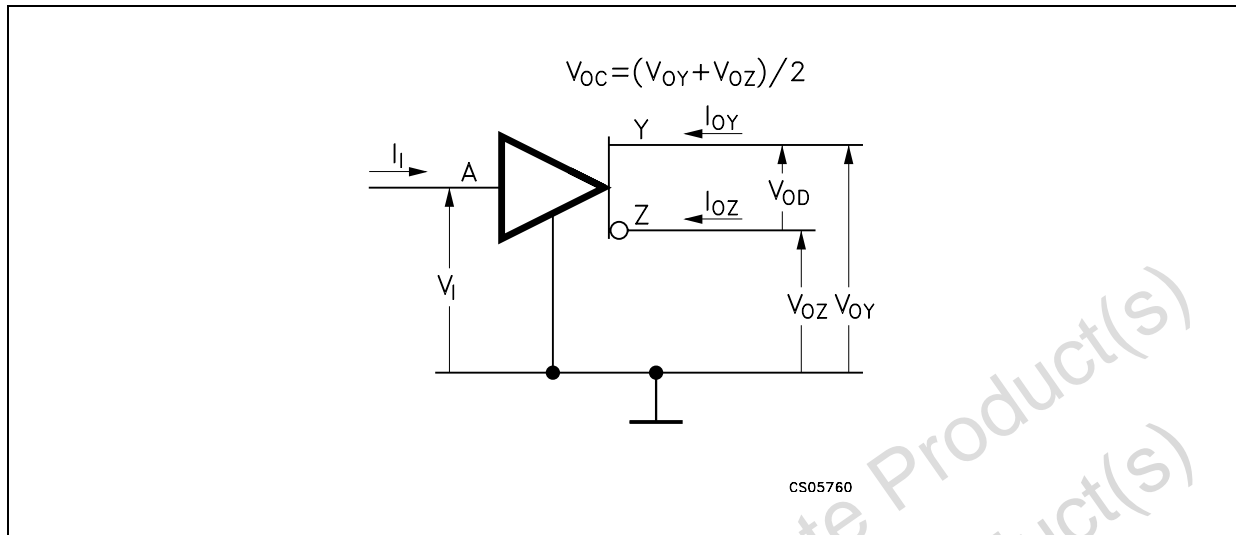
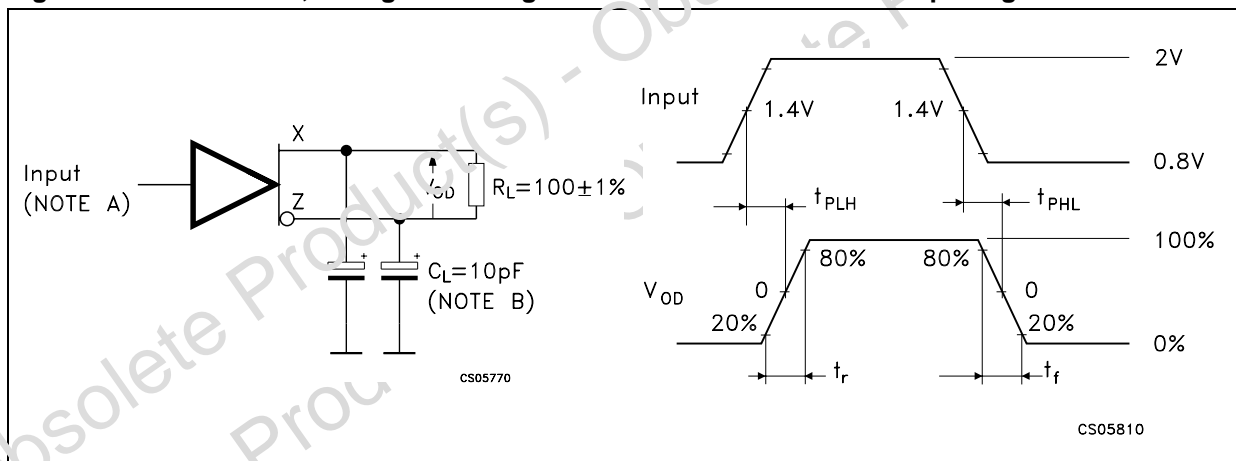


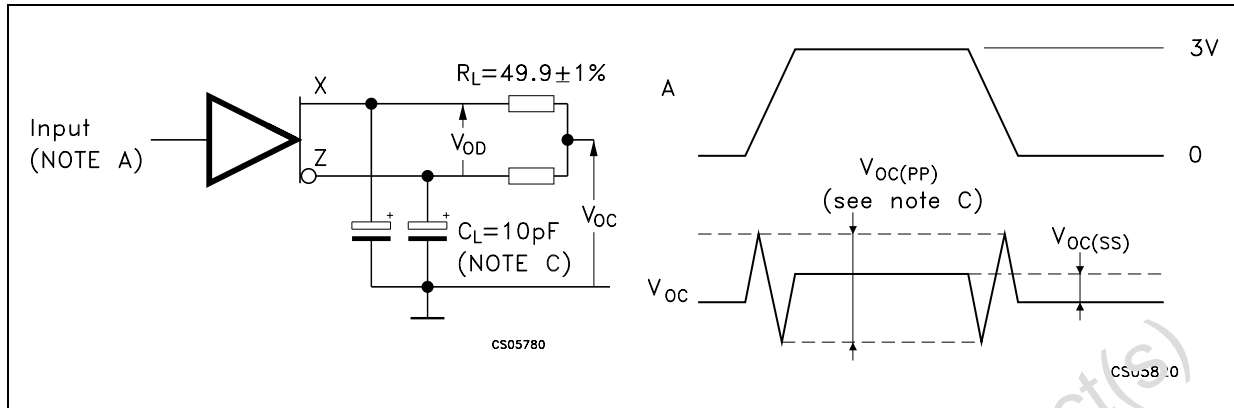
Figure 4. Test circuit, timing and voltage definitions for differential output signal



Note A: All input pulse are supplied by a generator having the following characteristics:  
 $t_r$  or  $t_f \leq 1\text{ns}$ , pulse repetition rate (PRR) = 50Mpps, pulse width =  $10 \pm 0.2\text{ns}$ .

Note B:  $C_L$  includes instrumentation and fixture capacitance within 6mm of the D.U.T

Figure 5. Test circuit and definitions for the driver common mode output voltage

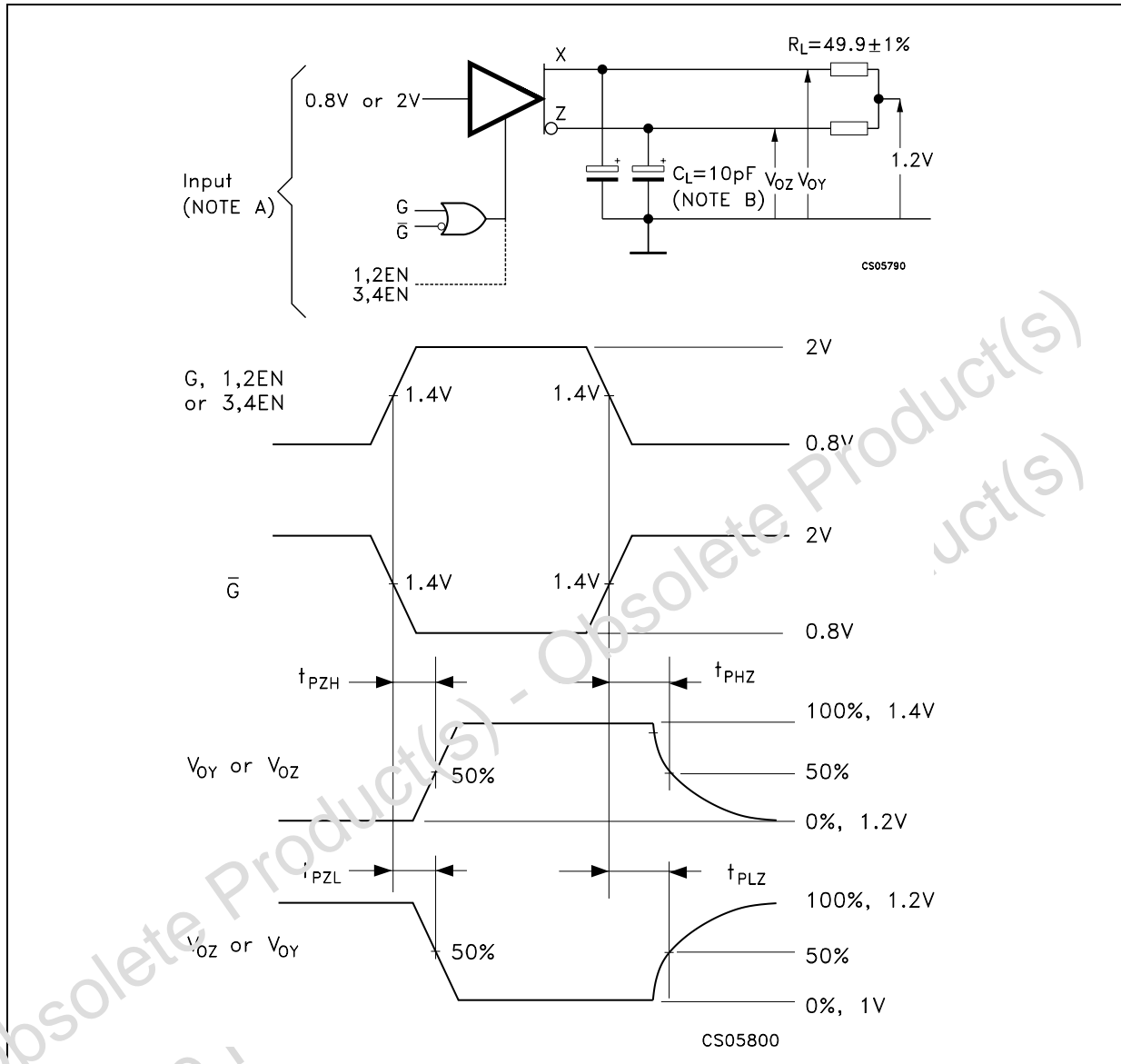


Note A: All input pulse are supplied by a generator having the following characteristics:  $t_r$  or  $t_f \leq 1ns$ , pulse repetition rate (PRR) = 50Mpps, pulse width =  $10 \pm 0.2ns$ .

Note B:  $C_L$  includes instrumentation and fixture capacitance within 6mm of the D.U.T

Note C: The measurement of  $V_{OC(PP)}$  is made on test equipment with a -3dB bandwidth of at least 300MHz.

Figure 6. Enable and disable time test circuit and waveform



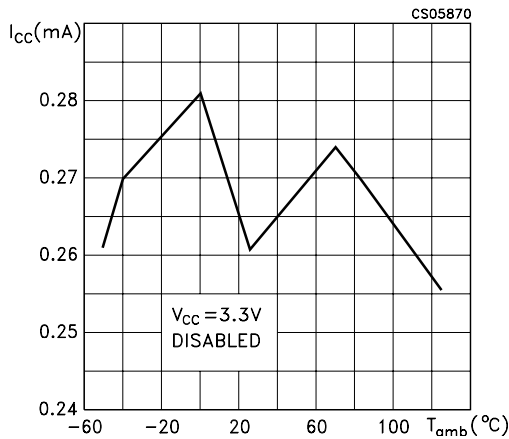
Note A: All input pulse are supplied by a generator having the following characteristics:  $t_r$  or  $t_f \leq 1\text{ns}$ , pulse repetition rate (PRR) = 0.5Mpps, pulse width =  $500 \pm 10\text{ms}$ .

Note B:  $C_L$  includes instrumentation and fixture capacitance within 6mm of the D.U.T

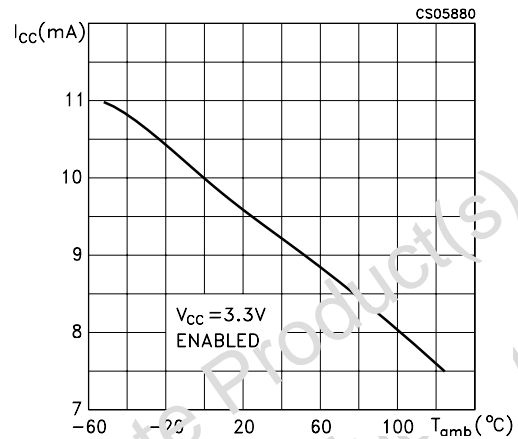
## 6 Typical performance characteristics

(unless otherwise specified at  $T_J = 25^\circ\text{C}$ )

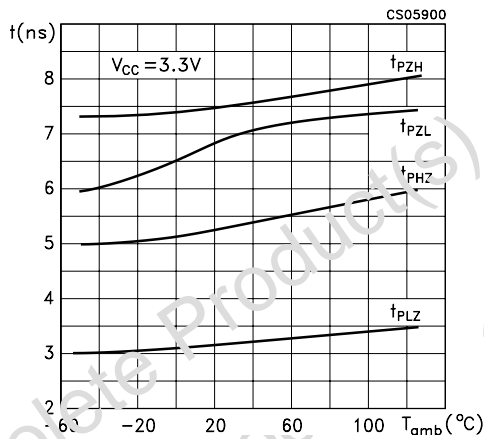
**Figure 7. Supply current vs temperature**



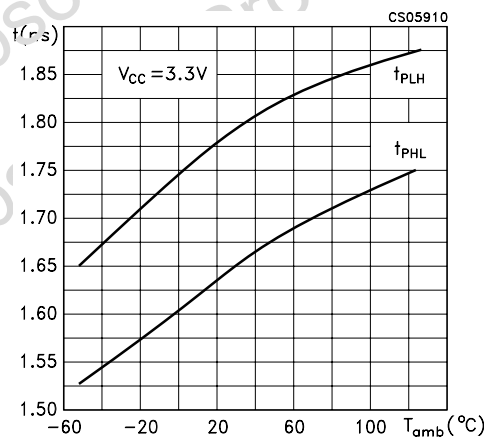
**Figure 8. Supply Current vs Temperature**



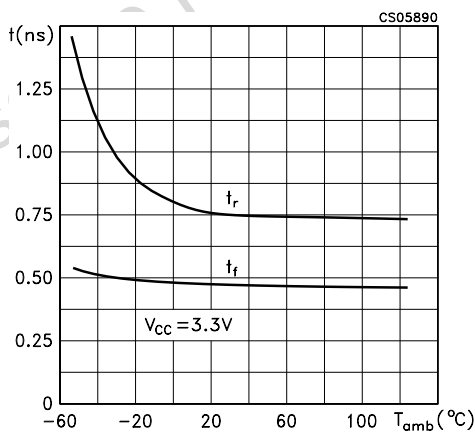
**Figure 9. Propagation delay enable to output**



**Figure 10. Propagation delay time**



**Figure 11. Differential output signal**



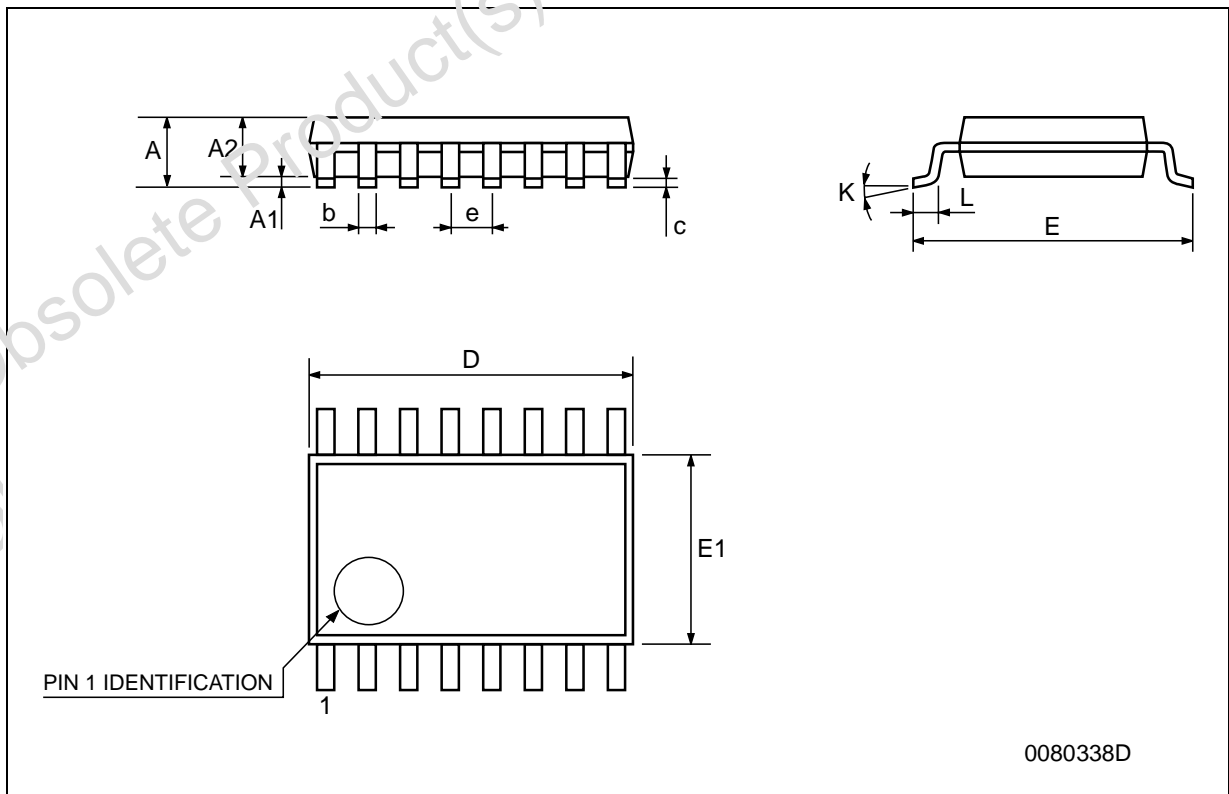
## 7 Package mechanical data

In order to meet environmental requirements, ST offers these devices in ECOPACK® packages. These packages have a Lead-free second level interconnect. The category of second Level Interconnect is marked on the package and on the inner box label, in compliance with JEDEC Standard JESD97. The maximum ratings related to soldering conditions are also marked on the inner box label. ECOPACK is an ST trademark. ECOPACK specifications are available at: [www.st.com](http://www.st.com).

Obsolete Product(s) - Obsolete Product(s)  
Obsolete Product(s) - Obsolete Product(s)

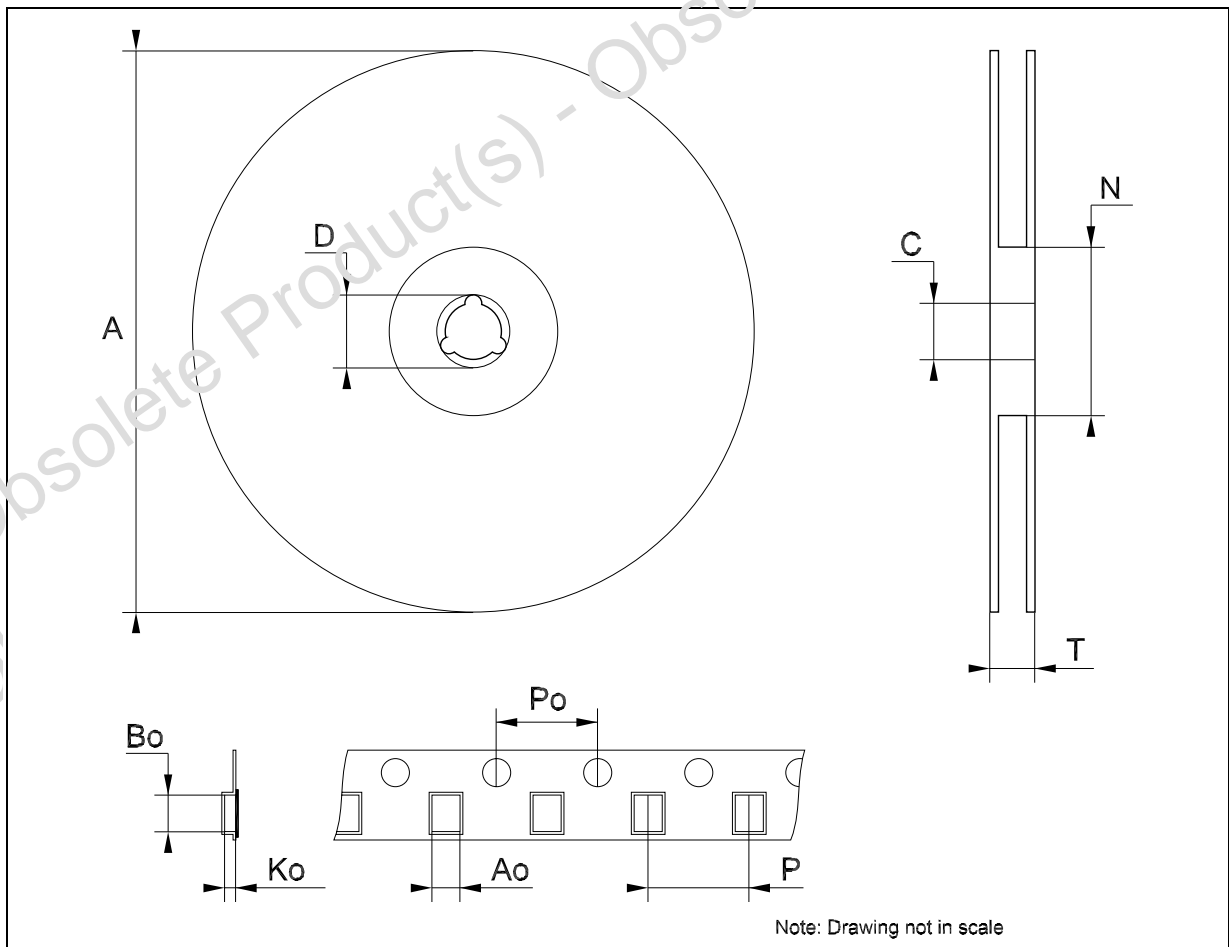
**TSSOP16 MECHANICAL DATA**

DIM.	mm.			inch		
	MIN.	TYP	MAX.	MIN.	TYP.	MAX.
A			1.2			0.047
A1	0.05		0.15	0.002	0.004	0.006
A2	0.8	1	1.05	0.031	0.039	0.041
b	0.19		0.30	0.007		0.012
c	0.09		0.20	0.004		0.0079
D	4.9	5	5.1	0.193	0.197	0.201
E	6.2	6.4	6.6	0.244	0.252	0.260
E1	4.3	4.4	4.48	0.169	0.173	0.176
e		0.65 BSC			0.0256 BSC	
K	0°		3°	0°		8°
L	0.45	0.60	0.75	0.018	0.024	0.030



**Tape & Reel TSSOP16 MECHANICAL DATA**

DIM.	mm.			inch		
	MIN.	TYP	MAX.	MIN.	TYP.	MAX.
A			330			12.992
C	12.8		13.2	0.504		0.519
D	20.2			0.795		
N	60			2.362		
T			22.4			0.892
Ao	6.7		6.9	0.264		0.272
Bo	5.3		5.5	0.209		0.217
Ko	1.6		1.8	0.063		0.071
Po	3.9		4.1	0.153		0.161
P	7.9		8.1	0.311		0.319



## 8 Revision history

Table 7. Revision history

Date	Revision	Changes
06-Apr-2006	2	Order codes has been updated and new template.

Obsolete Product(s) - Obsolete Product(s)  
Obsolete Product(s) - Obsolete Product(s)

**Please Read Carefully:**

Information in this document is provided solely in connection with ST products. STMicroelectronics NV and its subsidiaries ("ST") reserve the right to make changes, corrections, modifications or improvements, to this document, and the products and services described herein at any time, without notice.

All ST products are sold pursuant to ST's terms and conditions of sale.

Purchasers are solely responsible for the choice, selection and use of the ST products and services described herein, and ST assumes no liability whatsoever relating to the choice, selection or use of the ST products and services described herein.

No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted under this document. If any part of this document refers to any third party products or services it shall not be deemed a license grant by ST for the use of such third party products or services, or any intellectual property contained therein or considered as a warranty covering the use in any manner whatsoever of such third party products or services or any intellectual property contained therein.

**UNLESS OTHERWISE SET FORTH IN ST'S TERMS AND CONDITIONS OF SALE ST DISCLAIMS ANY EXPRESS OR IMPLIED WARRANTY WITH RESPECT TO THE USE AND/OR SALE OF ST PRODUCTS INCLUDING WITHOUT LIMITATION IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE (AND THEIR EQUIVALENTS UNDER THE LAWS OF ANY JURISDICTION), OR INFRINGEMENT OF ANY PATENT, COPYRIGHT OR OTHER INTELLECTUAL PROPERTY RIGHT.**

**UNLESS EXPRESSLY APPROVED IN WRITING BY AN AUTHORIZED REPRESENTATIVE OF ST, ST PRODUCTS ARE NOT DESIGNED, AUTHORIZED OR WARRANTED FOR USE IN MILITARY, AIR CRAFT, SPACE, LIFE SAVING, OR LIFE SUSTAINING APPLICATIONS, NOR IN PRODUCTS OR SYSTEMS, WHERE FAILURE OR MALFUNCTION MAY RESULT IN PERSONAL INJURY, DEATH, OR SEVERE PROPERTY OR ENVIRONMENTAL DAMAGE.**

Resale of ST products with provisions different from the statements and/or technical features set forth in this document shall immediately void any warranty granted by ST for the ST product or service described herein and shall not create or extend in any manner whatsoever, any liability of ST.

ST and the ST logo are trademarks or registered trademarks of ST in various countries.

Information in this document supersedes and replaces all information previously supplied.

The ST logo is a registered trademark of STMicroelectronics. All other names are the property of their respective owners.

© 2006 STMicroelectronics - All rights reserved

STMicroelectronics group of companies

Australia - Belgium - Brazil - Canada - China - Czech Republic - Finland - France - Germany - Hong Kong - India - Israel - Italy - Japan - Malaysia - Malta - Morocco - Singapore - Spain - Sweden - Switzerland - United Kingdom - United States of America

[www.st.com](http://www.st.com)