About this release note

This Release note contains information about the latest version of the ST-LINK/V2 firmware. It can be used to identify the version of the firmware currently running and, if necessary, to upgrade the firmware through the USB port.

Table 1 summarizes the tools covered by this document.

<table>
<thead>
<tr>
<th>Type</th>
<th>Part number</th>
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</thead>
<tbody>
<tr>
<td>Development tools</td>
<td>ST-LINK/V2</td>
</tr>
</tbody>
</table>

Table 1. Applicable tools
1 Host PC system requirements

PC and compatibles running with:
- Windows XP, Vista®, Windows Seven and Windows 8 32-bit operating systems
- Windows Vista®, Windows Seven and Windows 8 64-bit operating systems.

The application requires an ST-Link/V2 dedicated USB driver to be installed. In case the driver was not installed by the used toolset, it can be found on the ST-LINK/V2 page on the company website at www.st.com.

2 Changes in version 2.21.4

On ST-Link/V2, upgrade firmware to V2J21S4.

Corrections:
- Set NRST pin high, when exiting the firmware update mode.

3 Information about major previous release

3.1 Summary of changes in version 2.20.4

On ST-Link/V2, upgrade firmware to V2J20S4.

Corrections:
- Fixed regression appeared in V2J19 in SWV trace management.
- Fixed potential lockup condition in SWV trace management.

3.2 Summary of changes in version 2.19.4

On ST-Link/V2, upgrade firmware to V2J19S4.

New features:
- Fixed the issue about the RAM parity check error with some microcontrollers when reading misaligned RAM contents.
- Enhanced the SWV data flow (loosing data at 1024-bytes boundaries more rarely).
- Enhanced the target TNRST signal management for low-powered targets.
3.3 **Summary of changes in version 2.16.4**

- Added command for FPU registers access (STM32F3xxx and STM32F4xxx)
- Firmware compatible for boards with isolation
- Added command for board identification
- Added possibility of hot plug JTAG connection (without target hard reset)
- Fixed the limitation about power not activated in the debug domain during JTAG initialization sequence

4 **Customer support**

For more information or help concerning ST-LINK/V2, please contact the nearest sales office. For a complete list of ST offices and distributors, please refer to [www.st.com](http://www.st.com).
## Revision history

### Table 2. Document revision history

<table>
<thead>
<tr>
<th>Date</th>
<th>Revision</th>
<th>Changes</th>
</tr>
</thead>
<tbody>
<tr>
<td>16-Oct-2012</td>
<td>1</td>
<td>Initial release.</td>
</tr>
<tr>
<td>24-Feb-2014</td>
<td>2</td>
<td>Added content for new version 2.19.4</td>
</tr>
<tr>
<td>18-Apr-2014</td>
<td>3</td>
<td>Added content for new version 2.20.4</td>
</tr>
<tr>
<td>02-June-2014</td>
<td>4</td>
<td>Added content for new version 2.21.4</td>
</tr>
</tbody>
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