



PRODUCT/PROCESS CHANGE NOTIFICATION

PCN DSG/03/346

Discretes & Standard ICs Group Products in DFN, QFN & PowerFLAT: Lead-Free Component Connections

PCN DSG/03/346		
Product Family /Commercial Product	Products in DFN, QFN & PowerFLAT packages	
Type Of Change	Package assembly material change	
Reason For Change	Lead-Free market requirements and ROHS	
Description of the change	ECOPACK specification for Lead-Free connections	
Forecasted date of change	02-Jan-2004	
Forecasted date of samples for customer	01-Oct-2003	
Forecasted date for STMicroelectronics change qualification report availability	01-Oct-2003	
Marking to identify changed product	Date code + letter "E" marking whenever	
Description of qualification program	See Attached Qualification Plan	
Product Line(s) and/or Part Number(s)	See Attached List	
Manufacturing Location(s)		
Estimated Date of first shipment	09-Jan-2004	
Division Product Manager	C. NOPPER	Date: Oct.14 ,03
Division Q.A. Manager	A. BESSON	Date: Oct.14 ,03



Customer Acknowledgement of Receipt		PCN DSG/03/346
Please sign and return to STMicroelectronics Sales Office		
<input type="checkbox"/> Qualification Plan Denied <input type="checkbox"/> Qualification Plan Approved <input type="checkbox"/> Change Denied <input type="checkbox"/> Change Approved Remark	Name:	
	Title:	
	Company:	
	Date:	
	Signature:	
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**PRODUCT/PROCESS
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DSG – Discretes & Standard ICs Group
Products in DFN, QFN & PowerFLAT™ packages:
Lead-Free Component Connections



WHY THIS CHANGE?

The purpose of this change is to meet the **LEAD-FREE** requirements of the market and the ROHS (Restriction of the use of certain Hazardous Substances in electrical and electronic equipment) directive.

LEAD-FREE components are defined by STMicroelectronics as **ECOPACK®** components and the implementation of the ECOPACK specification includes the suppression of Lead (Pb) metal in the alloys used for the components lead finish.

Besides the change of lead finish, a change in materials (frame, glue or moulding compound) may occur in several cases in order to meet the higher soldering temperature constraints required for lead-free soldering using in particular IPC/JEDEC STD-020B standard as reference. Should it be the case, the description of such a change in materials is given below.

WHAT IS THE CHANGE?

The connections coating used for our components in DFN, QFN and PowerFLAT meeting the ECOPACK specification will be **Tin (Sn)** post assembly plating.

ECOPACK components can be assembled with both **current SnPb** and **SnAgCu lead-free** PCB assembly processes.

Besides the change of lead finish, **no change in materials** will be made for these components in DFN, QFN and PowerFLAT to meet the ECOPACK specification.

Moisture sensitivity testing of test vehicle devices indicates **no negative impact** to moisture performance with a peak reflow temperature of 250°C according to IPC/JEDEC JSTD-020B.

No negative impact to moisture performance was detected with the additional reflow profile with peak temperature of **260°C** introduced by ST, Philips and Infineon for testing MSL of small devices.

Except for contact finishing, **same test and assembly processes** will continue to be implemented, with no impact on the mechanical, thermal and electrical parameters of the products with reference to the product datasheet.

The identification of ECOPACK products will be achieved through **specific labelling** on component boxes. There will be no change in the product code, in the packing modes and the standard delivery quantities.

HOW AND WHEN?

Qualification and test results:

The **reliability tests plan** supporting the qualification program for the announced change is provided in the appendix 1 attached to this document. The **reliability test report** of the qualification program is available on request.

Sampling:

Samples of the devices used as **test vehicle** are available on request according to the list provided in the appendix 2 of the present document.



Change implementation schedule:

The **production start** and **first shipments** will be implemented as indicated in the roadmap table below, depending on our work in progress and materials availability. All given dates are subject to variation depending on our test results and assembly capabilities.

Marking and traceability:

Whenever possible, the letter "**E**" will be added in the marking pattern beside the ST logo on the package body. The traceability for the ECOPACK devices will also be ensured by the **date code**, by the "**ECOPACK**" **indication** on the boxes labelling and by the **Q.A. number**.

Roadmap table:

Package	Assembly Locations	Pb-free coating material	Samples Availability	Production Start	1st Shipments (*)
DFN 3 x 3 6L	Malasya	Tin	See appendix 2	Wk 01-2004	Wk 02-2004
DFN 5 x 6 8L	Malasya				
PowerFlat 3 x 3 6L	Malasya				
PowerFlat 6 x 5 8L	Malasya				
PowerFlat 5 x 5 14L	Malasya				
QFN 2 x 2 8L	Malaysia				
QFN 5 x 5 14L	Malaysia				
QFN 3 x 3 16L	Malaysia				
QFN 4x4 24L	Korea				

(*) Delivery of current product versions will continue while stocks last.

Appendix 1: Reliability tests for qualification program

Appendix 2: List of available sample parts by package.





Products in DFN, QFN & PowerFLAT packages: Lead-Free Component Connections

Reliability & Lead-free compatibility tests plan for QUALIFICATION PROGRAM

LEAD-FREE COMPATIBILITY TESTS				
TEST	CONDITIONS	DURATION	NBR OF LOTS	SAMPLE SIZE
SOLDERABILITY IN SnPb BATH	Dry Aging 155°C – Dipping 220°C Wet Aging 100°C – Dipping 220°C Wet Aging 100°C – Dipping 260°C	16Hrs - 3s 8Hrs - 3s 8Hrs - 3s	3	22 pcs / lot
SOLDERABILITY IN SnAgCu BATH	Dry Aging 155°C – Dipping 245°C Wet Aging 100°C – Dipping 245°C Wet Aging 100°C – Dipping 260°C	16Hrs - 3s 8Hrs - 3s 8Hrs - 3s	3	22 pcs / lot
Whiskers inspection reject criteria: no whisker with length > 50µm	- At to - After 500 Hrs (85°C 85%RH) - After 2, 4 and 6 months at 55°C - After thermal cycling (-35 / +125°C – 500 cycles)	-	1	10 pcs / lot



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List of available samples

Samples available	Package
EMIF08-2005QCF, STG3699QTR	QFN 3 x 3 16L

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