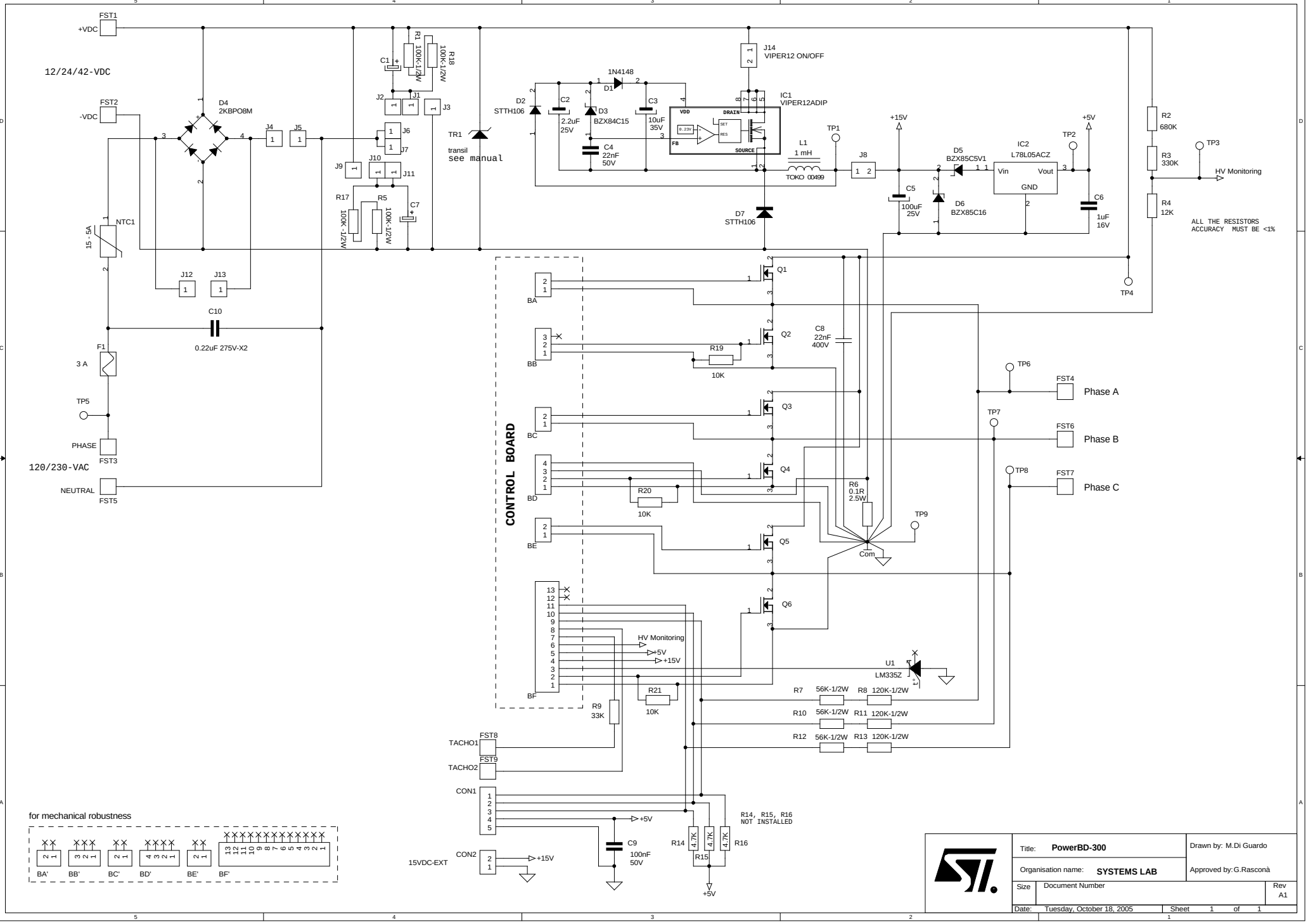
FST1
+VDC
12/24/42-VDC
FST2
-VDC
D4
2KBPO8M
J4
J5
C1
R1
100K-1/2W
R18
100K-1/2W
J2
J1
J3
J6
J7
J9
J10
J11
R17
100K-1/2W
R5
100K-1/2W
C7
TR1
transil
see manual
NTC1
15 - 5A
J12
J13
C10
0.22uF 275V-X2
F1
3 A
TP5
PHASE
FST3
120/230-VAC
NEUTRAL
FST5
D2
STTH106
C2
2.2uF
25V
1N4148
D1
D3
BZX84C15
C3
10uF
35V
C4
22nF
50V
VDD
DRAIN
SET
RES
FB
SOURCE
J14
VIPER12 ON/OFF
IC1
VIPER12ADIP
L1
1 mH
TOKO 00499
TP1
J8
D7
STTH106
+15V
C5
100uF
25V
D5
BZX85C5V1
D6
BZX85C16
IC2
L78L05ACZ
Vin
Vout
GND
TP2
+5V
C6
1uF
16V
R2
680K
R3
330K
R4
12K
TP3
HV Monitoring
ALL THE RESISTORS
ACCURACY MUST BE <1%
TP4
CONTROL BOARD
BA
BB
BC
BD
BE
BF
Q1
Q2
Q3
Q4
Q5
Q6
R19
10K
C8
22nF
400V
R20
10K
R6
0.1R
2.5W
TP9
Com
TP6
FST4
Phase A
TP7
FST6
Phase B
TP8
FST7
Phase C
HV Monitoring
+5V
+15V
U1
LM335Z
R21
10K
R9
33K
R7
56K-1/2W
R8
120K-1/2W
R10
56K-1/2W
R11
120K-1/2W
R12
56K-1/2W
R13
120K-1/2W
FST8
TACHO1
FST9
TACHO2
CON1
CON2
15VDC-EXT
+15V
C9
100nF
50V
+5V
R14, R15, R16
NOT INSTALLED
R14
4.7K
R15
4.7K
R16
4.7K
+5V
for mechanical robustness
BA'
BB'
BC'
BD'
BE'
BF'
Title: PowerBD-300
Organisation name: SYSTEMS LAB
Drawn by: M.Di Guardo
Approved by: G.Rasconà
Size
Document Number
Rev
A1
Date: Tuesday, October 18, 2005
Sheet 1 of 1