

Low voltage high performance NPN power transistor

Features

- Very low collector-emitter saturation voltage
- High current gain characteristic
- Fast switching speed
- Miniature SOT-23 plastic package ECOPACK[®]2 grade for surface mounting circuits

Applications

- Strobe and LED drives
- Motor and relay drives
- DC-DC converters



This device is an NPN transistor manufactured using low voltage planar technology with a double-metal process.

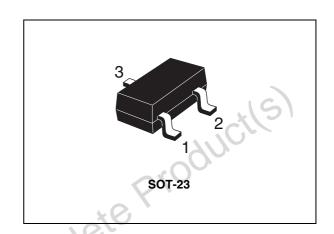


Figure 1. Internal schematic diagram

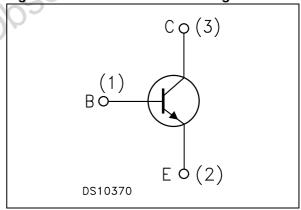


Table 1. Device summary

Order code	Marking	Package	Packing
3STR1630	1630	SOT-23	Tape and reel

Electrical ratings 3STR1630

Electrical ratings 1

Table 2. **Absolute maximum ratings**

Symbol	Parameter	Value	Unit	
V _{CES}	Collector-emitter voltage (V _{BE} = 0)	30	V	
V _{CEO}	Collector-emitter voltage (I _B = 0)	30	V	
V _{EBO}	Emitter-base voltage (I _C = 0)	5	V	
I _C	Collector current 6			
I _{CM}	Collector peak current (t _P < 5 ms)			
P _{TOT}	Total dissipation at T _{amb} = 25 °C	0.5	W	
T _{STG}	Storage temperature	-65 to 150	°C	
T _J	Max. operating junction temperature		°C	
Table 3. Thermal data				
Symbol	Parameter	Value	Unit	

Table 3. Thermal data

Symbol	Parameter	Value	Unit
R _{thJA} ⁽¹⁾	Thermal resistance junction-ambient max	250	°C/W

obsolete Product(s) 1. Device mounted on PCB area of 1 cm².

Electrical characteristics 2

 T_{case} = 25 °C unless otherwise specified.

Table 4. **Electrical characteristics**

Symbol	Parameter	Test co	onditions	Min.	Тур.	Max.	Unit
IODO	Collector cut-off current (I _E = 0)	V _{CB} = 30 V				0.1	μΑ
1 1	Emitter cut-off current (I _C = 0)	V _{EB} = 4 V				0.1	μA
V _{(BR)CBO}	Collector-base breakdown voltage (I _E = 0)	I _C = 100 μA		30	(316	V
V _{(BR)CEO} (1)	Collector-emitter breakdown voltage (I _B = 0)	I _C = 10 mA	Ç	30	0.0		V
V ()	Emitter-base breakdown voltage ($I_C = 0$)	I _E = 100 μA	eje,	5			٧
	Collector-emitter saturation voltage	$I_{C} = 1 A$ $I_{C} = 2 A$ $I_{C} = 5 A$	$I_B = 100 \text{ mA}$ $I_B = 40 \text{ mA}$ $I_B = 500 \text{ mA}$		60 140 240	90 190 300	mV mV mV
Vp=/\ \''	Base-emitter saturation voltage	$I_C = 2 A$ $I_C = 5 A$	$I_B = 40 \text{ mA}$ $I_B = 500 \text{ mA}$		830 1000	1100	mV mV
h _{FE} ⁽¹⁾	DC current gain	$I_C = 50 \text{ mA}$ $I_C = 0.5 \text{ A}$ $I_C = 2 \text{ A}$ $I_C = 5 \text{ A}$	$V_{CE} = 2 V$ $V_{CE} = 2 V$	210 180 170	260 90	560	
f _t	Transition frequency	I _C = 0.1 A	V _{CE} = 10 V		100		MHz
ССВО	Collector-base capacitance (I _E = 0)	V _{CB} = 40 V,	f = 1 MHz		15		pF
	Resistive load	I _C = 2.5 A	V _{CC} = 12 V 25 mA		90		ns

^{1.} Pulse test: pulse duration ≤300 µs, duty cycle ≤2%

Electrical characteristics 3STR1630

2.1 Electrical characteristics (curves)

Figure 2. DC current gain ($V_{CE}=1 V$) Figure 3. DC current gain ($V_{CE}=2 V$)

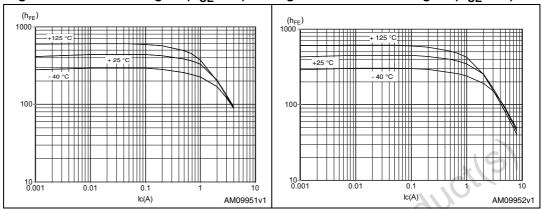


Figure 4. Collector-emitter saturation Figure 5. Collector-emitter saturation voltage (V_{CEsat} @ h_{FE} =10) voltage (V_{CEsat} @ h_{FE} =50)

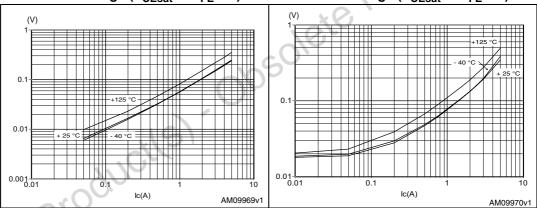
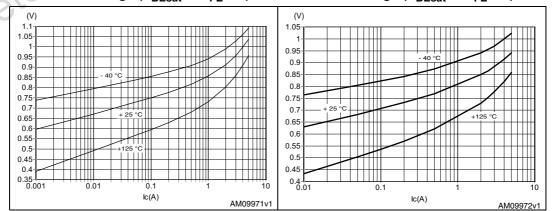


Figure 6. Base-emitter saturation Figure voltage (V_{BEsat} @ h_{FE}=10)

Figure 7. Base-emitter saturation voltage (V_{BEsat} @ h_{FE}=50)



Resistive load switching time Figure 9. Resistive load switching time Figure 8. (OFF)

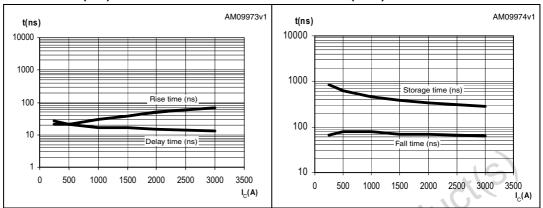


Figure 10. **Capacitance curves**

V_R reverse voltage (V)

C, capacitance (pF)

Obsolete P

Figure 11. Output curve AM09976v1 10000 lc(A) Tj=25°C F=1MHz 1000 Ib=60 mA 100 10 Ccb

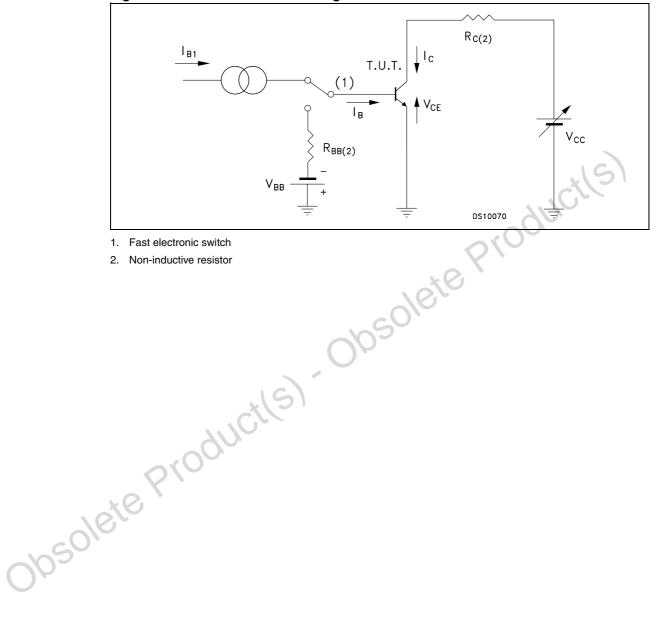
AM09975v1

0.5

Vce(V)

2.2 **Test circuits**

Figure 12. Resistive load switching test circuit



3 Package mechanical data

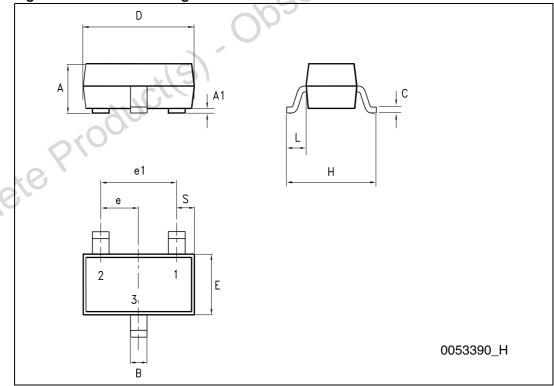
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Obsolete Product(s). Obsolete Product(s)

Table 5. SOT-23 mechanical data

Dim.		mm.	
Dilli.	Min.	Тур.	Max.
А	0.89		1.4
A1	0		0.1
В	0.3		0.51
С	0.085		0.18
D	2.75		3.04
е	0.85		1.05
e1	1.7		2.1
E	1.2		1.6
Н	2.1	. (2.75
L		0.6	
S	0.35	*6	0.65

Figure 13. SOT-23 drawings



3STR1630 Revision history

4 Revision history

Table 6. Document revision history

	Date	Revision	Changes
	02-Nov-2009	1	Initial release
	17-Jan-2011	2	Removed "Preliminary data" text from coverpage header.
	15-Jun-2011	3	Curves inserted Modified: <i>Table 4</i>
Obsole	tepro	ducil	Modified: Table 4 Modified: Table 4

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