The STA020D is a monolithic CMOS device which encodes and transmits audio data according to the AES/EBU, IEC 958, S/PDIF, & EIAJ CP-340 interface standards. It supports 96kHz sample rate operation.

The STA020D accepts audio and digital data which is then multiplexed, encoded and driven onto a cable. The audio serial port is double buffered and capable of supporting a wide variety of formats. The STA020D multiplexes the channel, user, and validity data directly from serial input pins with dedicated input pins for the most important channel status bits.

Block Diagram:

- AUDIO SERIAL PORT
- REGISTERS
- MUX
- DIFFERENTIAL
- DEDICATED CHANNEL STATUS BUS
- CBL
- TRNPT
- M0 M1 M2 VD+ GND MCK RST
- SCK FSYNC CLATA
- C U V

Ordering Number: STA020D