Table 1. Signal names

<table>
<thead>
<tr>
<th>Signal</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>OSCI</td>
<td>Oscillator input</td>
</tr>
<tr>
<td>OSCO</td>
<td>Oscillator output</td>
</tr>
<tr>
<td>FT/OUT</td>
<td>Frequency test / output driver (open drain)</td>
</tr>
<tr>
<td>SDA</td>
<td>Serial data address input / output</td>
</tr>
<tr>
<td>SCL</td>
<td>Serial clock</td>
</tr>
<tr>
<td>VBAT</td>
<td>Battery supply voltage</td>
</tr>
<tr>
<td>VCC</td>
<td>Supply voltage</td>
</tr>
<tr>
<td>VSS</td>
<td>Ground</td>
</tr>
</tbody>
</table>

Figure 2. 8-pin SOIC connections

Figure 3. Block diagram

- OSCILLATOR (32.768 kHz)
- OSCO
- FT/OUT
- VCC
- VSS
- VBAT
- SCL
- SDA

1 Hz

- SECONDS
- MINUTES
- CENTURY/HOURS
- DAY
- DATE
- MONTH
- YEAR
- CONTROL

RAM (56 x 8)

- OSCI
- OSCO
- FT/OUT
- VCC
- VSS
- VBAT
- SCL
- SDA

- VOLTAGE SENSE and SWITCH CIRCUITRY
- CONTROL LOGIC
- ADDRESS REGISTER
- SERIAL BUS INTERFACE