Figure 1. Block diagram

Figure 2. Configuration diagram (top view)

Table 2. Suggested connections for unused and not connected pins

<table>
<thead>
<tr>
<th>Connection/pin Status</th>
<th>N.C. Output</th>
<th>Input</th>
</tr>
</thead>
<tbody>
<tr>
<td>Floating</td>
<td>X</td>
<td>X</td>
</tr>
<tr>
<td>To ground</td>
<td>X</td>
<td>Through 10 K Ω resistor</td>
</tr>
</tbody>
</table>

UNDERVOLTAGE

OVERTEMPERATURE

CURRENT LIMITER

ON STATE OPENLOAD DETECTION

OFF STATE OPENLOAD AND OUTPUT SHORTED TO VCC

VCC

GND

INPUT

STATUS

LOGIC

DRIVER

Power CLAMP

OVERVOLTAGE DETECTION

UNDERRVOLTAGE DETECTION

VCC CLAMP

VCC

5

4

3

2

1

PPAK / P2PAK PENTAWATT