1 Block diagram and pin description

Figure 1. Block diagram

Table 2. Pin definition and functions

<table>
<thead>
<tr>
<th>Pin Symbol</th>
<th>Function</th>
</tr>
</thead>
<tbody>
<tr>
<td>1, 18, 19, 36</td>
<td>GND</td>
</tr>
</tbody>
</table>

Ground: reference potential.

Important:
For the capability of driving the full current at the outputs all pins of GND must be externally connected!

2, 35 | OUT11 |

Highside driver output 11.
The output is built by a highside switch and is intended for resistive loads, therefore the internal reverse diode from GND to the output is missing. For ESD reason a diode to GND is present, but the energy which can be dissipated is limited. The highside driver is a power DMOS transistor with an internal parasitic reverse diode from the output to VS (bulk-drain-diode). The output is over-current protected.

Important:
for the capability of driving the full current at the outputs both pins of OUT11 must be externally connected!

Driver Interface & Diagnostic

ST SPI

Charge Pump

All components to be placed together as close as possible