Figure 1. Block diagram

Table 1. Pin functions

<table>
<thead>
<tr>
<th>Name</th>
<th>Function</th>
</tr>
</thead>
<tbody>
<tr>
<td>VCC</td>
<td>Battery connection</td>
</tr>
<tr>
<td>OUT</td>
<td>Power output (1)</td>
</tr>
<tr>
<td>GND</td>
<td>Ground connection</td>
</tr>
<tr>
<td>IN</td>
<td>Voltage controlled input pin with hysteresis, CMOS compatible. Controls output switch state</td>
</tr>
<tr>
<td>CS</td>
<td>Analog CS pin, delivers a current proportional to the load current</td>
</tr>
<tr>
<td>CS_DIS</td>
<td>Active high CMOS compatible pin, to disable the CS pin</td>
</tr>
</tbody>
</table>

1. Pins 1 and 7 must be externally tied together.

Control & Diagnostic

- Power Clamp
- V_{ON} Limitation
- Current Limitation
- Over temp.
- OFF State Open load
- \( V_{SENSEH} \)
- Current Sense
- OVERLOAD PROTECTION (ACTIVE POWER LIMITATION)

Signal Clamp

Reverse Battery Protection

Logics

Driver

Driver & Control

In

CS_DIS

CS

Logic

VCC

Out

GND