

JOËL HARTMANN
Executive Vice President
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STMicroelectronics

Joël Hartmann is Executive Vice President of STMicroelectronics, Digital Front-End Manufacturing and Technology, and has held this position since February 2012. He manages ST's manufacturing operations in Crolles and Rousset, France, Technology and Design Platforms for the Company's digital products.

From 1979 to 2000, Hartmann worked at CEA-Leti, France-based applied-research center. In 2000, he joined STMicroelectronics as Director of the Crolles2 Alliance, the semiconductor manufacturing R&D initiative of STMicroelectronics, NXP, and Freescale Semiconductor. In 2008, Hartmann was promoted to Group Vice President and Director of Advanced CMOS Logic & Derivative Technologies. From 2010 to 2012, he had additional responsibilities as a co-leader of the Semiconductor Research and Development Center in Fishkill, NY, within the IBM ISDA Technology Alliance for advanced CMOS process development.

Hartmann sits on the Board of the SOI Industry Consortium Initiative and is a Member of the IEEE Electron Device Society. In 2017, he became a member of the French "Académie des Technologies". Hartmann has filed 15 patents on semiconductor technology and devices and authored 10 publications in this field to date.

Joël Hartmann was born in Toulon, France, in 1955. He graduated from the Ecole Nationale Supérieure de Physique de Grenoble with a degree in Physics.

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