Functional safety packages
STM32 MCUs and MPUs
STM8 MCUs
“If only
I could speed up the design time
of safety-certified systems

This is where we come in
Free safety packages for STM32
and STM8 with an ecosystem of
ST Authorized Partners
With its **Functional Safety Packages** based on robust built-in MCU/MPU safety features, ST provides a comprehensive set of certified software libraries and documentation for manufacturers to significantly reduce the development efforts, time and cost to achieve functional safety standard certifications.

- **SIL Functional Safety Package**
  for industrial IEC 61508 (STM32)

- **ASIL Functional Safety Package**
  for automotive ISO 26262 (STM8A)

- **Class B Functional Safety Package**
  for household electrical appliances
  IEC 60335-1/60730-1 (STM32 & STM8)
STM32 built-in safety features

- Dual watchdogs: Independent watchdog and system window watchdog
- Backup clock circuitry with clock security system (CSS)
- Supply monitoring (POR, BOR, PVD)
- I/O function locking
- PWM critical register protections with write-once registers (except on STM32L0/L1)
- Memory protection unit (MPU) with 8 or 16 regions to ensure data integrity from invalid behavior (except on STM32F0)
- Built-in safety features in Cortex-M cores (dual stack pointer, fault exceptions, debug module)

<table>
<thead>
<tr>
<th>Other features</th>
<th>F0</th>
<th>F1</th>
<th>G0</th>
<th>F3</th>
<th>G4</th>
<th>F2/F4</th>
<th>F7</th>
<th>H7</th>
<th>L0/L1</th>
<th>L4/L4+</th>
<th>L5</th>
<th>U5</th>
<th>WB</th>
<th>WL</th>
<th>MP1</th>
</tr>
</thead>
<tbody>
<tr>
<td>Nb of Hardware CRC unit</td>
<td>1</td>
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<tr>
<td>Programmable polynomial in CRC unit</td>
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<td>Multiple Flash memory protection levels</td>
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<td>PWM stop on core lockup</td>
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<tr>
<td>Parity bit for SRAM memory (1bit/byte)</td>
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<tr>
<td>ECC (SECDED) for SRAM</td>
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</tbody>
</table>

(1) Depending on part number
SIL Functional Safety Package
Reduce time and cost to build STM32-based systems certified to IEC 61508 industrial safety standard.
ST provides a complete, certified offering to:
- Lower project costs
- Reduce design complexity
- Ease SIL certification assessment
SIL functional safety for STM32 safety documentation

Safety manuals: detailed list of safety requirements (conditions of use) and examples to guide STM32 users to achieve safety integrity level certification in compliance with IEC 61508.

Available at STM32 series level for free download on www.st.com/x-cube-stl

FMEA: detailed list of MCU/MPU failure modes and related mitigation measures adopted

FMEDA: static snapshot reporting IEC 61508 failure rates, computed at both MCU/MPU and basic function detail levels.

Available on demand at STM32 series level (*)(**) on www.st.com/x-cube-stl

(*) submitted to NDA
(**) FMEDA snapshot is generated for a specific set of part numbers
SIL functional safety package for STM32 X-CUBE-STL self-test libraries

- Software-based diagnostic suite designed to detect random hardware failures in safety-critical STM32 core components (CPU + SRAM + Flash memory)
- Diagnostic coverage verified by state-of-the-art ST proprietary fault injection methodology
- Application independent: can be potentially used in any end customer application
- Compiler independent: delivered as object code
- Certified by TÜV Rheinland
- IEC 61508 SC3 compliant
- Provided with safety manual and user guide

Available on demand at STM32 series level
www.st.com/x-cube-stl

(1) The original certificate and the updated list of certificated software versions can be downloaded from TÜV Rheinland websites: www.fsproducts.com, www.certipedia.com
(2) submitted to NDA
ST builds functional safety solutions for its STM32 Arm® Cortex®-M microcontroller family, including detailed and accurate safety analyses supported by verification activities based on state-of-the-art fault injection methods.

STM32 Design Database

Proprietary state-of-the-art fault injection methods

Certified STM32 Self-test Library X-CUBE-STL

IEC 61508-compliant software development

IEC 61508-compliant safety analysis

STM32 Safety Documentation
Achieve SIL2/SIL3 with STM32

<table>
<thead>
<tr>
<th>SIL2</th>
<th>Achievable with single STM32 (1oo1 architecture)</th>
</tr>
</thead>
<tbody>
<tr>
<td>SIL3</td>
<td>Achievable with two STM32 (1oo2 architecture)</td>
</tr>
</tbody>
</table>

1oo1: 1 out of 1 MCU (no redundancy)

1oo2: 1 out of 2 MCUs (1 redundant system)
STM32 Safety Concepts

STM32 MCU single Cortex-M core

Refer to STM32F0, F1, F2, F3, F4, F7, H7 single core, G0, G4, L0, L1, L4/L4+, L5, U5 safety manuals for details
TÜV Rheinland single core certificate

STM32 MCU dual Cortex-M core

Refer to STM32H7 dual-core and STM32WL5x dual-core safety manuals for details
TÜV Rheinland dual core certificate

STM32MP1 MPU dual Cortex-A7 and Cortex-M4

Refer to STM32MP1 safety manual for details
TÜV Rheinland dual core certificate
STM32 MCU dual Cortex-M core Safety Concept

2 possible schemes for acquisition, execution and transfer of result

**Individual scheme**
Each CPU implement a specific safety function, no collaboration

**Collaborative scheme**
The 2 CPUs collaborate for the implementation of the same safety function

PEi = input processing element
PEC = computation processing element
PEo = input processing element
SF(s) = on or multiple safety Functions

More details in UM2840 STM32H7 dual-core safety manual and UM2814 STM32WL5x dual-core safety manual
STM32MP1 MPU dual Cortex-A7 and Cortex-M4 Safety Concept

Safety function implementation confined in Cortex-M4 real-time side

- **Non-Safe Partition**
  - **Cortex-A7**
    - up to 800 MHz
  - Hardware and software-based separation

- **Safe Partition**
  - **Cortex-M4**
    - 209 MHz
  - Execution of self-test library (X-CUBE-STL for STM32MP1)
  - dedicated RAM and peripherals

The coexistence with non-safety related software on Cortex-A7 (e.g. Linux) is possible

More details in UM2714 STM32MP1 Series safety manual
ASIL Functional Safety Package
Reduce time and cost to build STM8A-based systems certified to ISO 26262 automotive functional safety standard.
STM8A-SafeASIL

Safety documentation

Safety manual: Detailed list of safety requirements and examples to support STM8AF and STM8AL use in applications that need to fulfill functional safety requirements as defined by automotive safety integrity level ASIL B of ISO 26262.

Available for STM8AF and STM8AL series for free download on www.st.com/stm8safety

FMEA: detailed list of MCU failure modes and related mitigation measures adopted

FMEDA: static snapshot reporting ISO 26262 failure rates, computed at both MCU / basic function detail levels.

Available on demand for STM8AF and STM8AL (*)

Ask your local ST contact.

(*) submitted to NDA
full list of detailed safety requirements enabling STM8AF and STM8AL users to realize, in the framework of their ISO26262-compliant software development process, the software Self-test Library required by STM8AF or STM8AL Safety Manual to support application up to ASIL B.

The quality of the specification document allows its direct use in a development process compliant to ISO26262-6 requirements.

The specification includes the evidences and rationales behind the generation of the safety requirements for the completeness of end-user safety case.

Application independent: can be used in potentially any end-user application.

on demand for STM8AF and STM8AL series(*)
Ask your local ST contact

(*) submitted to NDA
ClassB functional safety package for STM32 and STM8 MCUs

Reduce time and cost to build STM32 & STM8 based systems certified to IEC 60335-1 and 60730-1 household electrical appliance safety standards.

- **Certified** ST self-test libraries
- **Optimized** code based on STM32CubeHAL
- **Safety manuals** (guidelines and examples)
- For STM32: Support of IAR™ EWARM, Keil® MDK-ARM, and STM32CubeIDE
- **Worldwide standards coverage** (IEC, UL, and CSA)
# ClassB functional safety package for STM32 and STM8 MCUs

<table>
<thead>
<tr>
<th>Package name</th>
<th>X-CUBE-CLASSB</th>
<th>STM8-SafeClassB</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>STM32 Series covered</strong></td>
<td><strong>V2.2.0</strong> - STM32F0, F1, F3, F2, F4, F7, STM32L0, L1, L4 <strong>V2.3.0</strong> - STM32G0, G4, WB, H7 single core <strong>V2.4.0</strong> - STM32L5 <strong>V3.0.0, 3.0.1</strong> - STM32H7 dual core</td>
<td><strong>STM8AF</strong> <strong>STM8AL</strong> <strong>STM8L</strong> <strong>STM8S</strong></td>
</tr>
<tr>
<td><strong>Self-test libraries based on</strong></td>
<td><img src="image" alt="STM32CubeHAL" /></td>
<td>Optimized direct access to STM8 registers</td>
</tr>
<tr>
<td><strong>Supported development environments</strong></td>
<td>IAR Embedded Workbench®, ARM KEIL®, STM32CubeIDE</td>
<td>IAR Embedded Workbench®, Cosmic®</td>
</tr>
<tr>
<td><strong>Certification</strong></td>
<td><strong>UL@2016-2021</strong></td>
<td><strong>UL &amp; VDE@2018</strong></td>
</tr>
<tr>
<td><strong>IEC 60335-1 and 60730-1 international standards coverage</strong></td>
<td><img src="image" alt="IEC, UL and CSA" /></td>
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</tr>
<tr>
<td><strong>Safety manual (guidelines)</strong></td>
<td><strong>AN4435</strong></td>
<td><strong>AN3181</strong></td>
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</tbody>
</table>

## Certification
- **UL@2016-2021**
- **UL & VDE@2018**

## Standards
- IEC 60335-1 and 60730-1
- UL, VDE, CSA

## Development Environments
- IAR Embedded Workbench®, ARM KEIL®, STM32CubeIDE
- IAR Embedded Workbench®, Cosmic®
Guidelines and examples for STM32 and STM8 users to achieve Class B certification in compliance with IEC 60335-1 and 60730-1.
Functional Safety Packages summary
# Functional Safety Packages for STM32 & STM8 MCUs

<table>
<thead>
<tr>
<th>MCU support</th>
<th>STM32</th>
<th>STM8A</th>
<th>STM32</th>
<th>STM8</th>
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<tbody>
<tr>
<td>Achievable safety standards</td>
<td>IEC 61508</td>
<td>ISO 26262</td>
<td>IEC, UL, CSA 60335-1, 60730-1</td>
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<tr>
<td>Certification</td>
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<tr>
<td>Package content</td>
<td>• Safety Documentation</td>
<td>• Safety Documentation</td>
<td>• Safety Documentation</td>
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<tr>
<td></td>
<td>• Self-Test Libraries</td>
<td>• Self-Test Library specification</td>
<td>• Self-Test Libraries</td>
<td></td>
</tr>
<tr>
<td>Package name</td>
<td>X-CUBE-STL</td>
<td>STM8A-SafeASIL</td>
<td>X-CUBE-CLASSB</td>
<td>STM8-SafeCLASSB</td>
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</tbody>
</table>
Functional Safety Ecosystem
Get support from ST authorized partners

Reduce your project time and cost

Safety Requirements  HW & SW Design  Validation  Certification

Functional Safety expertise
Functional safety authorized partners

- **Embedded Software**
  - arm KEIL
  - Embedded Office
  - Microsoft
  - SCIOP TA
  - SEGGER
  - WITTENSTEIN

- **Software Development Tools**
  - arm KEIL
  - IAR SYSTEMS

- **Engineering, consulting, development or design services**
  - Embedded Office
  - embeX
  - hitex
  - innotec
  - MESCO
  - NewTec

- **Training**
  - innotec
  - MESCO
  - NewTec
Arm Compiler for Functional Safety

Qualified toolchain for safety development

Safety Standards:
- IEC 61508 (Industrial) – SIL 3
- ISO 26262 (Automotive) – ASIL D
- EN 50128 (Railways) – SIL 4
- IEC 62304 (Medical) – CLASS C

*At any Safety Integrity Level

Licensed as ‘Standalone’ or via Arm IDE Toolkits:
- Arm Development Studio
  - Gold/Platinum Edition
- Keil MDK-Professional

Safety Qualified Toolchain
Simplifies Tool Justification
- TÜV Certificate by TÜV SUD
- Qualification Kit
  - Safety Manual
  - Defect Report

Baseline toolchain for Arm Safety Software development:
- Certified C Library
- Arm FuSa Run-Time System
- Arm Software-Test Libraries
Arm FuSa RTS: Run-Time System for Functional Safety

Software components certified for safety-critical applications

User Application code

- FuSa RTX RTOS
- FuSa Event Recorder
- Software test library (STL)
  - Self-test code for run-time verification
- FuSa CMSIS-Core
  - (Arm-Core specific)
- Certified C library (Cortex-M)

Arm Cortex-M processor

Covered safety standards:
- Automotive: ISO 26262, ASIL D
- Industrial: IEC 61508, SIL 3
- Railways: EN 50128, SIL 4
- Medical: IEC 62304, Class C

Supported processors:
- Cortex-M0/M0+
- Cortex-M3
- Cortex-M4
- Cortex-M7

FuSa RTS components certified with Arm Compiler for Functional Safety
5 Steps to Your Safety Platform

1. Safety Concept
   Analyze system needs and provide a safety concept

2. Setup Safety Platform
   Integrate software components and realize missing parts

3. Select Software
   ST Microcontroller & Embedded Office products or whatever the system needs

4. Pre-Certification
   Harmonize safety manuals, certify remaining parts, assessment with authority

5. Long-term Maintenance
   Active functional safety management, workshops and training
5 Steps to Your Safety Platform

Safety & Cyber Security Engineers
TÜV Rheinland certified engineers

300+ Successful Customer Projects
Aerospace, Industrial, Automotive, Rail, Medical

70+ Satisfied Customers Worldwide
Products, Development Services, Mentoring

Certified Software Components
Safety RTOS, Safety AddOns, HW Selftests
Development of Turn-Key Certified Products

Main Industrial Sectors

System Engineering
- Software
- Hardware
- Mechanics
- Certification
- Production
- Prod. Life Cycle Management

Functional Safety
SIL 4 / PL e

- Industrial Automation
- Mobile Automation
- Process Automation
- Transportation
- Medical Engineering
- Energy & Drives

More than 150 Experts - 20 years of experience
Recognized company in functional safety worldwide

- TÜV Rheinland awarded the first Functional Safety Management (FSM) certificate with the highest maturity level (5) to embeX

- Offering
  - Development of certified turnkey safety products and subsystems
  - Transfer of development processes and know-how to customers
  - Consulting
Thus, embeX offers:

- Risk Analysis
- Consultancy
- Developments achieving SIL 3 (IEC 61508) and SL 4 (IEC 62443)
- Verification including pen tests and fuzzing

Further information:
Consulting & Engineering

- Excellent know-how in leading micro controller architectures for automotive & industrial
- STM32 functional safety experts
- Consulting & Development and Certification support according to standards: IEC 61508, ISO 26262, ISO 13849 ... and more

- Consulting for process, system & concept
- Architecture and design specification
- Hardware and software development
- Unit testing & system verification
Expertise out of our Customer projects

**DC/DC converters**
Implementing security requirements

**Safety integration & certification**
Emulator for special micro controllers

**IoT implementation and integration**

**eDrive development**

- Battery management
- ECU for powertrain & combustion engine
IAR Embedded Workbench for safety-critical applications

World leading embedded development tools

- More than 30 years of experience as a compiler vendor
- More than 1 million embedded devices built with our tools
- More than 150,000 users worldwide

The build chains are certified by TÜV SÜD as compliant with the international umbrella standards and the certification validates the quality of IAR Systems’ entire development processes, as well as the delivered software.

Certified toolchain
- A special functional safety edition of IAR Embedded Workbench

Simplified validation
- Functional Safety certificate from TÜV SÜD
- Safety report from TÜV SÜD
- Safety guide

Guaranteed support through the product life cycle
- Prioritized support
- Validated service packs
- Regular reports of known problems

Available for Arm and STM8
Our obsession is SafeWare Engineering!

- Consulting
- Training
- Development Support
- Project Implementation
- Standardization, Approval and Certification
- Safety Management
- Specifications and Mathematical Methods

- Hard and Software (IEC61508)
- Machinery (ISO13849, IEC62061)
- Factory automation (IEC61131-6, IEC61800-5-2)
- Railway Technology (IEC 50126, IEC 50128, IEC 50129)
- Process industry (IEC 61511)
- Nuclear, Wind and Solar Energy
- Automotive Systems (ISO26262)
- Farming Machines (EN16590, ISO25119)

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Our range of services: Factory & Process Automation

Tailor-made Development Solutions
Customized hardware and software development with flexible use of design packages.

Directly applicable DESIGN PACKAGES
Proven circuits and software components for rapid implementation of your development project.

Development Consulting
Development accompanying consulting and coaching in the areas of functional safety, explosion-proof and industrial communication.
Our offering: Your success is our driving force

Consulting
- Technology Consulting
- Functional Safety Management
- Explosion-proof trainings
- Industrial Communication
- Support in the creation of Requirements

Development – Design / Implementation / Prototyping
- Hardware Development
- Software Development
- Safety Development
- PCB Layout
- Prototyping
- Type Testing
- Integration Test
- Use of existing Safety Design Packages
- Support of product launching into production

Concept – Architecture
- Creation of the Functional Safety Concept
- Creation of the Explosion-proof Concept
- System Architecture
- Quality Assurance Measures

Certification
- Comprehensive Support of the Certification
MESCO Safety Design Packages

Build-up with a base board & expansion boards

Design Packages based on ST solutions

Built up with a main board & expansion boards as a reference design, our Design Packages simplify and accelerate the development in both safety- and non-safety-related environments.

Expansion boards
### Azure RTOS Functional Safety

<table>
<thead>
<tr>
<th>Component</th>
<th>Description</th>
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<tbody>
<tr>
<td><strong>Azure RTOS ThreadX &amp; ThreadX SMP</strong></td>
<td>A high-performance real-time operating system</td>
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<tr>
<td><strong>Azure RTOS FileX</strong></td>
<td>An embedded FAT file system that offers optional fault tolerant features</td>
</tr>
<tr>
<td><strong>Azure RTOS USBX</strong></td>
<td>A USB stack that provides host, device, and on-the-go support</td>
</tr>
<tr>
<td><strong>Azure RTOS NetX and NetX Duo</strong></td>
<td>A TCP/IP IPv4/IPv6 embedded network stack that includes cloud connectivity and IPsec and TLS/DTLS security protocols</td>
</tr>
<tr>
<td><strong>Azure RTOS GUIX Studio and GUIX</strong></td>
<td>A complete design environment and run-time to create and maintain 2D graphical user interfaces</td>
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</tbody>
</table>
Azure RTOS Functional Safety

- ThreadX, FileX, GUIX, NetX Duo, USBX pre-certified by TUV to IEC 61508 SIL 4, IEC 62304 Class C, ISO 26262 ASIL D, EN 50128 SW-SIL 4, UL 1998, UL/IEC 60730, UL/IEC 60335
- Azure RTOS pre-certification covers generic C code
- Same source code whether or not certification is needed
- Pre-certification artifacts are licensed separately
NTSafetySolutions

Training & Consulting

- Varied range of seminars for functional safety in practice
- Safety workshops for individual customers

Products, e.g.

- SafeFlex – Reference platform for safety development
- NTSafeDriveMonitor – Safety module for monitoring of drives
- NTBMS – Safety reference platform for Battery Management Systems

Expert services to do with all aspects of product development

- Safety management assessment
- Safety risk assessment
- Safety requirement analysis
- Licensing strategy
- Safety planning
- Safety concept
- Concept examination
- Functional safety management

Managed Services in Product Lifecycle

- Safety system development
- Safety engineering
- Safety software development
- Safety hardware development
- Integration, verification & validation
- Documentation & traceability
Reduce cost and time-to-market of your safety application development with NTSafeFlex STM32 evaluation board and Safety Software Library

- The board is based on two STM32G070 with additional Software Library for functional safety solutions up to SIL 3 and PLe, Cat4.
- Typical applications: safety control logic, motor supervision, general safety applications with low performance standards, etc.
<table>
<thead>
<tr>
<th>Feature</th>
<th>Description</th>
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<tbody>
<tr>
<td>SAFE</td>
<td>SCIOPTA RTOS is designed with safety in mind.</td>
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<tr>
<td>CERTIFIED</td>
<td>SCIOPTA RTOS is certified according following standards: IEC61508 (SIL 3), EN50128/129 (SIL 3/4) and ISO26262 (ASIL D).</td>
</tr>
<tr>
<td>MIGRATION NON SAFE – SAFE</td>
<td>SCIOPTA RTOS’ certified API does not differ from the non-certified version. All system call are certified.</td>
</tr>
<tr>
<td>FAST</td>
<td>SCIOPTA RTOS is tailored to the specific CPU exploiting all its features to provide short latencies, small overhead and deterministic execution.</td>
</tr>
<tr>
<td>SMALL</td>
<td>SCIOPTA RTOS is designed to be compact and still offering a wide range of system calls to enable almost any kind of application</td>
</tr>
<tr>
<td>DYNAMIC</td>
<td>SCIOPTA RTOS can be used in a complete dynamic manner so that the application can react on upcoming needs.</td>
</tr>
<tr>
<td>SCHEDULING</td>
<td>SCIOPTA RTOS uses pre-emptive scheduling based on priorities and round-robin scheduling with optional time slice.</td>
</tr>
<tr>
<td>EASY TO USE</td>
<td>SCIOPTA RTOS hides many of the burden other RTOSs put on the developer. A set of six system calls is sufficient for 80% of an application</td>
</tr>
<tr>
<td>FUTURE PROOF</td>
<td>SCIOPTA RTOS’s asynchronous direct message passing fits perfect future challenges like many-core SoCs or distributed systems.</td>
</tr>
<tr>
<td>USE CASES</td>
<td>SCIOPTA RTOS is successfully used in different areas like Automotive, Defense, Rail Way, Medical, Industrial Automation and Consumer Electronics.</td>
</tr>
</tbody>
</table>
embOS-Safe

Deployed and proven in several billion devices
embOS is deployed in several billion devices and is a proven choice for embedded products. It has been deployed in all kinds of applications, such as home appliances, IoT, transportation, industrial, medical or automotive.

More than 27 years of continuous development
SEGGER started to offer embOS in the early 90s as a product and has continued to develop the RTOS and add device support until today. It has become the core for SEGGER’s own products as well as a multitude of customer products.

Easy transition from standard to certified
While any application benefits from a reliable operating environment, in some cases, proof in form of certification is required. In markets where certification might become a requirement, embOS is the ideal choice, as it uses the same code base as embOS-Safe making a later conversion as easy as possible.

embOS features
• Guarantees 100% deterministic real-time operation
• Highest performance with lowest use of memory
• Powerful and easy to use API
• Kernel awareness plugins available
• Zero interrupt latency
• Cycle Precise System Time
• MadeForSTM32

Medical
Industrial
Home Appliances
Transportation
Automotive
and more ..
TÜV Süd has verified the embOS development process and confirms, that embOS-Safe is ideally suited as fundamental component for safety products. embOS-Safe is certified for functional safety according to IEC 61508 SIL 3 and IEC 62304 Class C.

The Application Programming Interface (API) is unchanged in relation to embOS. Therefore existing software parts can be (re-)used easily. This helps to use embOS-Safe in existing applications.

The embOS-Safe certification kit includes all necessary documents, including the comprehensive embOS Safety Manual.

The certified RTOS embOS-Safe is also available for SEGGER's IDE Embedded Studio, offering a one-stop-solution. Naturally, embOS-Safe is fully suited for usage with SEGGER's extensive portfolio of outstanding middleware, debug probes and production tools, too.
SAFERTOS® is a pre-certified safety Real Time Operating System (RTOS) for embedded processors. It delivers superior performance and dependability, whilst utilizing minimal resources.

SAFERTOS is a safety critical upgrade to FreeRTOS:
- Based on the FreeRTOS functional model
- Rebuilt to comply with SIL 3 requirements
- No open source code

<table>
<thead>
<tr>
<th>Sector</th>
<th>Standard/Regulatory</th>
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<tbody>
<tr>
<td>Industrial</td>
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<td>EN 50128</td>
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</table>

SAFERTOS can be found in:
- Dialysis machines
- Prostheses
- Control systems found on trains
- Safety critical servo controllers
- Industrial control systems and many more
### SAFERTOS Support for ST

#### SAFERTOS Supported Platforms

<table>
<thead>
<tr>
<th>Platform</th>
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<tr>
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<td>STM32F2, STM32F1, STM32L1, STM32W</td>
<td>Cortex-M3</td>
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<td>Cortex-M0</td>
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<tr>
<td>STM32F7, H7</td>
<td>Cortex-M7</td>
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<tr>
<td>STM32H7 Dual Core</td>
<td>Cortex-M7 &amp; Cortex-M4</td>
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</tbody>
</table>

**SAFERTOS supports:**
- X-CUBE-STL;
- STM32Cube embedded software;
- STM32 SIL Functional Safety Package;
- Secure boot.

**SAFERTOS Demos for ST are available:**
- 30-days evaluation packages with full source code on request. [Download Demos here.](#)

**Free White Paper:**
Based on the X-CUBE-STL Functional Safety Package.
[Free to Download](#)
WITTENSTEIN high integrity systems (WHIS) are safety RTOS specialists, part of The WITTENSTEIN Group. WHIS specialize high integrity and safety critical embedded systems design.

**SAFERTOS® Source Code**

- Royalty Free, Perpetual Licensing
- 12 Months Free Support & Maintenance
- Smooth path to certification

**Design Assurance Pack**

**Middleware**

**Safety Components**

**Tools**

**Training & Support**

WHIS also offer Board Support Packages, Training Courses and more…
Our technology starts with You

Find out more at www.st.com/functionalsafety