



2-Mbit serial EEPROM



Application benefits

- The 2-Mbit EEPROM is ideal for applications requiring the following benefits of EEPROM:
 - **Low energy**
 - V_{DD} min: 1.8 V support
 - I_{CC} read: 1 mA
 - I_{CC} write: 2 mA
 - I_{CC} standby: 3 μ A
 - **Flexibility for data updates**
 - Byte alterability
 - Fast erase time (10 ms)
 - **High endurance**
 - 1M write cycles per page
 - 1 billion write cycles over full memory area
 - At characterization level, each of the 1024 pages was cycled 1M times
 - More than 40 years' data retention





2-Mbit EEPROM features

- 2-Mbit memory array (256 Kbytes)
- 2 bus types: I²C and SPI
- Additional write lockable page (identification page)
- Page size: 256 bytes
- 1.8 to 5.5 V power supply
- Package: SO8N

2-Mbit EEPROM dashboard



- SO8N samples available and ready for production
 - I²C: M24M02-DRMN6TP
 - SPI: M95M02-DRMN6TP
 - Programming support with:
 - **Dediprog** model EE-100
 - **Leap** model LP-56
 - and many more to come



Dediprog EE-100

- WLCSP (available in Q2 2012)
 - WLCSP (wafer chip scale package)
 - X = 3.5 mm
 - Y = 2.0 mm
 - Z = 290 μm

