

# World's First 4Mbit SPI EEPROM M95M04-DR

January 2020



## 4Mbit SPI EEPROM now available



SO8N & TSSOP8 packages

- **1.8V to 5.5V** -40 / +85°C
- 10MHz SPI Bus
- **5ms Write Time**
- 4 Million Erase/Write cycles
- 40 years data retention
- 4Mbit memory organized as 512K bytes
- 1024 **pages of 512 Bytes**
- **Byte alterability**
- Lockable page
- Error Code Correction for high reliability



# 4Mbit EEPROM - Features

- SPI 10MHz at 85°C
  - Less than 0.5 seconds for Sequential Read of 4Mbit (512Kbytes)
- Write time: 5ms for 512 bytes
  - 5 seconds for Data storage of 4Mbit (512K bytes)
  - 1.4 seconds for Data storage of 1Mbit (128K bytes)
- Lockable page of 512 bytes
  - Specific instruction set and read-only lock to Protect sensitive Data
- 4 Million Write cycles at 25°C
  - Datalog: 1 cycle per minute over 8 years





# 4Mbit EEPROM - Low power

- Standby mode: 1 $\mu$ A typical
  - 90% of application operating time
- Read operation: 400 $\mu$ A typical at 1V8 5MHz
  - 9% of application operating time
- Write operation: 800 $\mu$ A typical at 1V8
  - 1% of application operating time



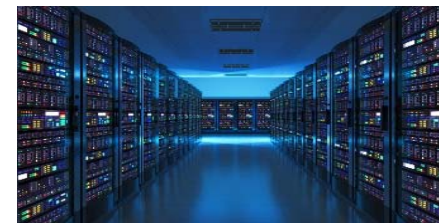
# 4Mbit EEPROM – More accuracy & Flexibility



- Medical & health care
- Wearables



- Smart meter
- Automation



- 5G network
- Routers
- Servers

Mix Boot code, Calibration tables, user Parameters and Datalog

# Apply for samples at St.com

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SO8N M95M04-DRMN6TP



TSSOP M95M04-DRDW6TP

Serial EEPROM

4Mbit product page

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