STM32H750 & STM32H7B0
High-performance Value lines

Extra flexibility to create affordable performance-oriented systems

Focusing on real-time performance and scalability, ST's new Value lines lower the barrier to access STM32H7 microcontrollers with products keeping just the essential Flash memory.

With execution performance up to 2424 CoreMark at the heart of a secure, power-efficient architecture, the new Value line microcontrollers are the entry point to IoT innovation in medical, industrial and consumer applications.

STM32H750 & STM32H7B0 devices embed 128-Kbyte Flash memory to accommodate the most critical and secure code, while supporting external memory extension using NOR, NAND, SDRAM, dual-mode Quad SPI and Octal-SPI Flash memory.

Notes:
1. Tightly Coupled Memories
2. Digital Filters for Sigma Delta Modulator

CORE, MEMORIES
- Arm® Cortex®-M7 core up to 480 MHz
- Up to 16-Kbyte data and 16-Kbyte instruction cache
- Up to 4 DMA controllers
- 128-Kbyte Flash memory and up to 1.4-Mbyte RAM
- ITCM/DTCM¹: 64-Kbyte ITCM RAM + 128-Kbyte DTCM RAM for time-critical routines

CONNECTION
- Up to 2 x USB 2.0 OTG FS/HS with optional embedded HS PHY
- USART, UART, SPI, and I²C
- 2 x CAN FD
- Ethernet MAC
- FMC (supporting SDRAM in 32-bit mode up to 133 MHz), dual-mode Quad SPI Flash memory and dual Octal SPI
- 2 x SDMMC

EMBEDDED FLASH
- Secure Boot for customer Root of Trust
- Fast boot time
- High-execution speed from ultra fast embedded memory

ENERGY EFFICIENT
- Flexible power mode
- Gated power domains
- On-chip power management

AUDIO
- 3 x PS + audio PLL
- 4 x SAI
- 2 x 12-bit DAC
- SPDIF-RX

GRAPHICS
- LCD TFT controller
- JPEG Codec
- Chrom-ART Accelerator™
- Chrom-GRC™

OTHER
- 8- to 14-bit Camera interface
- Crypto and Hash hardware acceleration
- DFSDM² interface to connect microphone MEMs or sigma delta ADC front ends
- 16- and 32-bit timers
- 3x ADCs with up to 16-bit resolution (up to 3.6 MSPS)
- Analog (comparators and Op amps)
- Power supply down to 1.62 V

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¹ Tightly Coupled Memories
² Digital Filters for Sigma Delta Modulator
## STM32H7B0 & STM32H750 Value Line Block Diagram

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<tr>
<th></th>
<th>STM32H7B0</th>
<th>STM32H750</th>
</tr>
</thead>
<tbody>
<tr>
<td>Core</td>
<td>Arm® Cortex®-M7, MPU, DFPU, ETM</td>
<td>Arm® Cortex®-M7, MPU, DFPU, ETM</td>
</tr>
<tr>
<td>Frequency</td>
<td>280 MHz</td>
<td>480 MHz</td>
</tr>
<tr>
<td>Cache</td>
<td>2 x 16-Kbyte cache</td>
<td></td>
</tr>
<tr>
<td>Embedded memories</td>
<td></td>
<td></td>
</tr>
<tr>
<td>RAM</td>
<td>1.4-Mbyte RAM including 128-Kbyte DTCM</td>
<td>1-Mbyte RAM including 128-Kbyte DTCM</td>
</tr>
<tr>
<td>Flash memory</td>
<td>64-Kbyte ITCM</td>
<td></td>
</tr>
<tr>
<td>Backups</td>
<td>128-Kbyte Flash memory</td>
<td></td>
</tr>
<tr>
<td>Main DMA</td>
<td>4-Kbyte backup RAM</td>
<td></td>
</tr>
<tr>
<td>Memory Interfaces</td>
<td></td>
<td></td>
</tr>
<tr>
<td>FMC</td>
<td>FMC (SDRAM, NOR, NAND)</td>
<td></td>
</tr>
<tr>
<td>SPI</td>
<td>2 x Octal SPI with On-The-Fly Decryption</td>
<td>Dual-mode Quad SPI Flash</td>
</tr>
<tr>
<td>MMC</td>
<td>2 x SD/SDIO/MMC</td>
<td></td>
</tr>
<tr>
<td>Accelerators</td>
<td>Chrom-ART Accelerator™ JPEG codec</td>
<td>Chrom-GRC™ Crypto/Hash accelerator and tamper detection</td>
</tr>
<tr>
<td>Peripherals</td>
<td>Advanced analog</td>
<td>Advanced connectivity</td>
</tr>
</tbody>
</table>

The STM32Trust ecosystem combines knowledge, design tools, and ready-to-use original ST software to build strong cyber-protection into new IoT devices, leveraging industry best-practices. [www.st.com/stm32trust](http://www.st.com/stm32trust)

## STM32 High-Performance Value Lines

<table>
<thead>
<tr>
<th>Product lines</th>
<th>Core</th>
<th>f_CPU (MHz)</th>
<th>ID cache (KB)</th>
<th>ITCM/ DTCM (KB)</th>
<th>Flash memory (KB)</th>
<th>RAM (KB)</th>
<th>Graphic</th>
<th>Advanced analog</th>
<th>USB OTG</th>
<th>Ethernet</th>
<th>Camera I/F</th>
<th>CAN</th>
<th>Security &amp; Crypto/Hash acceleration</th>
<th>Packages</th>
</tr>
</thead>
<tbody>
<tr>
<td>STM32H750</td>
<td>CM7</td>
<td>480</td>
<td>16+16</td>
<td>64/128</td>
<td>128</td>
<td>864</td>
<td>Chrom-ART Accelerator™ TFT controller JPEG Codec</td>
<td>3x 16-bit ADCs (3.6 MSPS), 2x Opamps, 2x Comparators, 2x 12-bit DACs</td>
<td>2</td>
<td>1</td>
<td>1 TTFD CAN, 1 FDCAN, Yes, PCROP†, SPF, SBSPUF*</td>
<td>LQFP100, LQFP144, LQFP176, UFBA1761, TFBGA2403</td>
<td></td>
<td></td>
</tr>
<tr>
<td>STM32H7B0</td>
<td>280</td>
<td>1 376</td>
<td>1 376</td>
<td>1 376</td>
<td>1 376</td>
<td>1</td>
<td>Chrom-GRC™ TFT-LCD JPEG codec</td>
<td>1 376</td>
<td>-</td>
<td>-</td>
<td>-</td>
<td>LQFP64, LQFP100, LQFP144, UFBA169, LQFP176, UFBA176</td>
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</tbody>
</table>

**Notes:**
1. PCROP: Proprietary Code Read Out Protection (protects part of the Flash memory to execution access only)
2. SPF: Secure Firmware Install. Security service and keys available on standard parts to securely install a Root of Trust (RoT)
3. SBSPUF: Secure Boot and Secure Firmware Update dedicated hardware memory protection mechanism.
4. 0.65 mm pitch
5. 0.8 mm pitch

## Hardware Tools

All existing STM32H7 hardware development tools are fully compatible with the new Value lines.

### Evaluation boards

- STM32H7B3I-EVAL
- STM32H753I-EVAL2

### Discovery kits

- STM32H750B-DK
- STM32H7B3I-DK

### Nucleo-144 development boards

- NUCLEO-H753ZI