Introduction

This document specifies the steps and tools required to prepare SFI (secure internal firmware install), SFIx (secure external firmware install) or SMI (secure module install) images (or a combination of both), and to program these into STM32 devices that support SFI/SFIx on-chip internal, or external Flash memory. It is based on the STM32CubeProgrammer tool set (STM32CubeProg).

These tools are compatible with all STM32 devices porting SFI/SFIx.

The main objective of the SFI/SFIx and SMI processes is the installation of security and cloning prevention in OEMs’ and software-partner’s firmware respectively.

Refer also to AN4992 [1], which provides an overview of the secure firmware install (SFI) solution, and how this provides a practical level of protection of the IP chain - from firmware development up to programming the device on-chip Flash memory.
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1 General information

1.1 Licensing information

STM32CubeProgrammer supports STM32 32-bit devices based on Arm® Cortex®-M processors.

1.2 Acronyms and abbreviations

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<th>Abbreviations</th>
<th>Definition</th>
</tr>
</thead>
<tbody>
<tr>
<td>AES</td>
<td>Advanced encryption standard</td>
</tr>
<tr>
<td>CLI</td>
<td>Command line interface</td>
</tr>
<tr>
<td>CM</td>
<td>Contract manufacturer</td>
</tr>
<tr>
<td>GCM</td>
<td>Galois counter mode (one of the modes of AES)</td>
</tr>
<tr>
<td>GUI</td>
<td>Graphical user interface</td>
</tr>
<tr>
<td>HSM</td>
<td>Hardware security model</td>
</tr>
<tr>
<td>MAC</td>
<td>Message authentication code</td>
</tr>
<tr>
<td>MCU</td>
<td>Microcontroller unit</td>
</tr>
<tr>
<td>OEM</td>
<td>Original equipment manufacturer</td>
</tr>
<tr>
<td>PCROP</td>
<td>Proprietary code read-out protection</td>
</tr>
<tr>
<td>PI</td>
<td>Position independent</td>
</tr>
<tr>
<td>ROP</td>
<td>Read-out protection</td>
</tr>
<tr>
<td>RSS</td>
<td>Root security service (secure)</td>
</tr>
<tr>
<td>RSSe</td>
<td>Root security service extension</td>
</tr>
<tr>
<td>SFI</td>
<td>Secure firmware install</td>
</tr>
<tr>
<td>STPC</td>
<td>STM32TrustedPackageCreator</td>
</tr>
<tr>
<td>SMI</td>
<td>Secure modules install</td>
</tr>
<tr>
<td>STM32</td>
<td>ST family of 32-bit ARM based microcontrollers</td>
</tr>
<tr>
<td>SW</td>
<td>Software</td>
</tr>
<tr>
<td>XO</td>
<td>Execute only</td>
</tr>
</tbody>
</table>

a. Arm is a registered trademark of Arm Limited (or its subsidiaries) in the US and/or elsewhere.
2 How to generate an execute-only/position independent library for SMI preparation

This section describes the requirements and procedures for the preparation of an execute-only (XO) and position independent (PI) library using a partner toolchain. These kinds of libraries serve in encrypted SMI-module generation.

2.1 Requirements

SMI modules run in execute-only (XO) areas, also called PCROP areas, and must be relocatable to be linkable with final OEM application. Nevertheless, today, 3rd party toolchains for STM32 devices (such as MDK-ARM™ for ARM, EWARM for IAR™ and GCC based IDEs) do not allow both features to be activated at the same time. So, starting from particular versions of 3rd party toolchains, the two features below are possible for SMI support:

- Position independent support (code + rw data + ro data)
- No literal pool generation: needed for the PCROP feature.

2.2 Toolchains allowing SMI generation

Three toolchains allow SMI generation:

- **EWARM**
  
  Version 7.42.0 allows execute-only (XO) and position independent (PI) library generation for SMI support through the following options: `--ropi_cb "rwpi" --no_literal_pool`.
  
  - `--ropi_cb" + "rwpi"` are needed for position independent support
  - option `--no_literal_pool` is needed for the PCROP feature.

- **MDK-ARM**
  
  The customized version allows execute-only (XO) and position independent (PI) library generation for SMI support through the following options: `fropi-cb", "frwpi", "mexecute-only"`.
  
  - `fropi-cb" is needed for ro data independent position
  - `frwpi" is needed for rw data independent position
  - option `--mexecute-only" is needed for PCROP feature.
  
  All library symbols being used in the final application must be added to the final project in a "txt" file format.

- **GCC**
  
  The customized version of GCC based toolchains allows execute-only (XO) and position independent (PI) library generation for SMI support through the following options: `--masset`.
  
  Option `--masset" has the same role as `--ropi --ropi_cb --rwpi --no_literal_pool" options used for the EWARM toolchain.
2.3 **Execute-only/position independent library scenario example under EWARM**

In order to generate an execute-only (XO) and position independent (PI) library a customized version of the IAR toolchain must be used: version 7.42.0.

2.3.1 **Relocatable library preparation steps**

1. Open the project available in the “Example” folder: double click on “Example/AdvEx.eww”.
   The project architecture is illustrated in [Figure 1](#).

![Figure 1. IAR example project overview](image-url)
The following steps update the old "lib.o" linked to the example application by making it support both PI and XO features:

2. Within Lib-Debug options -> C/C++ Compiler: go to tab “Extra Options” and add the following line:

   "--ropi_cb"

   This action is illustrated in Figure 2.

**Figure 2. Update compiler extra options**
3. Within Lib-Debug options -> Linker: go to the “Extra Options” tab and add the following lines:
   --no_literal_pool
   --ropi_cb
   --loadable
   --no_entry
   This action is illustrated in Figure 3.
   – “ropi_cb” is needed for Position Independent support
   – the “no_entry” is a linker option that sets the entry point field to zero.

Figure 3. Linker extra options
4. Within Lib-Debug options -> Build actions: in post build command line execute the batch file "postbuild.bat" by inserting, if it is not already configured, the following command line:

"$PROJ_DIR\postbuild.bat" "$TOOLKIT_DIR" "$TARGET_PATH" "$PROJ_DIR\lib.o"

This action is illustrated in Figure 4.

Figure 4. Setting post-build option
The “postbuild.bat” file is used to perform some key actions:

- **--wrap**: adds veneers to library functions to initialize registers used for ropi code
- **“iexe2obj.exe”**: transforms the elf into a linkable object file.

See Figure 5.

**Figure 5. Postbuild batch file**

```
rem This is a simple script that creates and object file (%3) from an image (%2)
rem using the tools in (%1).
rem Make sure the old files are deleted before we try to generate the new ones.
if exist %9.tmp {
  del %9.tmp
}
if exist %3 {
  del %3
}
if exist %3 {
  del %3
}
rem This is the list of functions that will have a wrapper generated.
SMT_WRAP--wrap ToString --wrap setup_memory --wrap setup_memory2
rem convert the image to a linkable object file using Lib as prefix
rem and keeping all node symbols (hols a bit with debugging)
%binwrap.obj.exe --prefix Lib --keep_node_symbols %wRAP %2.tmp %3
```

5. Rebuild the project “Lib”

### 2.3.2 Relocatable SMI module preparation steps

From the object file created, “lib.o”, generate the SMI relocatable module using the STM32TrustedPackageCreator tool “libr.smi” and its corresponding data clear part (“libr_clear.o: corresponding to the input “lib.o” without the protected section code).

To execute this step, follow the steps explained for SMI generation under section “Section 3.5.2: Steps for SMI generation(CLI)”.
2.3.3 Application execution scenario

1. Flash the already generated SMI relocatable module to address 0x08080000 using STM32Cube Programmer v0.4.0 or newer (see Section 6: Example SMI programming scenario to perform this action).

2. Link the data clear part, "libr_clear.o", generated from STM32TrustedPackageCreator tool to the final IAR example application instead of the old previously used "lib.o".

3. Exclude "lib.o" from the build (Figure 6).

Figure 6. How to exclude the “lib.o” file from build

4. Rebuild the application.

5. Do these modifications in an example application ICF file:
   a) Define region for PCROP block:
      
      ```
      define symbol __ICFEDIT_region_PCROP_start__ = 0x08080000;
      define symbol __ICFEDIT_region_PCROP_end__   = 0x0809FFFF;
      define region PCROP_region  = mem:[from __ICFEDIT_region_PCROP_start__
      to __ICFEDIT_region_PCROP_end__];
      ```
   b) Define PCROP region as 'noload' (since it is already installed using STM32CubeProgrammer so no need to load it again):
      
      ‘SMI’: place noload in PCROP_region { ro code section __code__Lib};
These modifications are illustrated within the “app.icf” file, which is shown in Figure 7.

Figure 7. app.icf file

6. To check that example application executed successfully on the STM32H7 device:
   a) Check that address 0x08080000 was protected with PCROP
   b) The expected “printf” packets appears in the terminal output.
3 Encrypted firmware (SFI) / module (SMI) preparation using STM32TrustedPackageCreator

The STM32TrustedPackageCreator tool allows the generation of SFI and SMI images for STM32H7 devices. It is available in both CLI and GUI modes free of charge from www.st.com.

3.1 System requirements

Using the STM32TrustedPackageCreator tool for SFI/SFIx and SMI image generation requires a PC running on either Windows® 7/10, or Ubuntu® 14 in 64-bit versions and MacOs®.

3.2 SFI generation process

The SFI format is an encryption format for internal firmware created by STMicroelectronics that transforms internal firmware (in ELF, Hex, Bin or Srec formats) into encrypted and authenticated firmware in SFI format using AES-GCM algorithm with a 128-bit key. The SFI preparation process used in the STM32TrustedPackageCreator tool is described in Figure 8.

---

**Figure 8. SFI preparation mechanism**
The SFI generation steps as currently implemented in the tool are described in Figure 9.

**Figure 9. SFI image process generation**

Before performing AES-GCM to encrypt an area, we calculate the Initialization Vector (IV) as:

$$IV = nonce + \text{Area Index}.$$  

The tool partitions the firmware image into several encrypted parts corresponding to different memory areas.

These encrypted parts appended to their corresponding descriptors (the unencrypted descriptive header generated by the tool) are called areas.
These areas can be of different types:

- ‘F’ for a firmware area (a regular segment in the input firmware)
- ‘M’ for a module area (used in SFI-SMI combined-image generation, and corresponds to input from an SMI module)
- ‘C’ for a configuration area (used for option-byte configuration)
- ‘P’ for a "pause" area
- ‘R’ for a "resume" area.

Areas ‘P’ and ‘R’ do not represent a real firmware area, but are created when an SFI image is split into several parts, which is the case when the global size of the SFI image exceeds the allowed RAM size predefined by the user during the SFI image creation.

The STM32TrustedPackageCreator overview below (Figure 10) shows the RAM size input for SFI image generation, and also the 'Continuation token address' input, which is used by SFI multi install to store states in Flash memory during SFI programming.

Figure 10. RAM size and CT address inputs used for SFI multi install
Figure 11 (below) shows the specifics of these new areas compared to a regular SFI area.

![Figure 11. 'P' and 'R' area specifics versus a regular SFI area](image)

<table>
<thead>
<tr>
<th>Area format</th>
<th>New Pause Area</th>
<th>New Resume Area</th>
</tr>
</thead>
<tbody>
<tr>
<td>Type ('F', 'M', 'C')</td>
<td>Type 'P'</td>
<td>Type 'R'</td>
</tr>
<tr>
<td>Version</td>
<td>Version</td>
<td>Version</td>
</tr>
<tr>
<td>Index</td>
<td>Index</td>
<td>Index</td>
</tr>
<tr>
<td>Size</td>
<td>Size = 0</td>
<td>Size = 0</td>
</tr>
<tr>
<td>Address</td>
<td>Address of CT</td>
<td>Address of CT</td>
</tr>
<tr>
<td>Total Nb of areas</td>
<td>Total Nb of areas</td>
<td>Total Nb of areas</td>
</tr>
<tr>
<td>Tag</td>
<td>Tag</td>
<td>Tag</td>
</tr>
</tbody>
</table>

Encrypted Area Content
- Firmware
- Module
- Configuration

A top-level image header is generated then authenticated, for this the tool performs AES-GCM with authentication only (without encryption), using the SFI image header as an AAD, and the nonce as IV.

An authentication tag is generated as output.

Note: To prepare an SFI image from multiple firmware files, make sure that there is no overlap between their segments, otherwise an error message appears (Figure 12: Error message when firmware files with address overlaps used).
Figure 12. Error message when firmware files with address overlaps used
For combined SFI-SMI images, there is also an overlap check between firmware and module areas. If the check fails, an error message appears (Figure 13).

Figure 13. Error message when SMI address overlaps with a firmware area address
Also, all SFI areas must be located in Flash memory, otherwise the generation fails and the following error message appears (Figure 14).

Figure 14. Error message when a SFI area address is not located in Flash memory
The final output from this generation process is a single file, which is the encrypted and authenticated firmware in ".sfi" format. The SFI format layout is described in Figure 15.

Figure 15. SFI format layout
When the SFI image is split during generation, areas ‘P’ and ‘R’ appear in the SFI image layout, as in the example below *Figure 16*.

**Figure 16. SFI image layout in case of split**

```
<table>
<thead>
<tr>
<th>Unsplit Image</th>
<th>Split Image</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Image Header</strong></td>
<td><strong>Image Header</strong></td>
</tr>
<tr>
<td>Area 1 ‘F’</td>
<td>Area 1 ‘F’</td>
</tr>
<tr>
<td>Area 2 ‘F’</td>
<td>Area 2 ‘F’</td>
</tr>
<tr>
<td>Area 3 ‘F’</td>
<td>Area 3 ‘P’ause</td>
</tr>
<tr>
<td>Area 4 ‘M’</td>
<td>Area 4 ‘R’esume</td>
</tr>
<tr>
<td>Area 5 ‘C’</td>
<td>Area 5 ‘F’</td>
</tr>
<tr>
<td></td>
<td>Area 6 ‘M’</td>
</tr>
<tr>
<td></td>
<td>Area 7 ‘C’</td>
</tr>
</tbody>
</table>
```
3.3 SFIx generation process

In addition to the SFI preparation process mentioned in the previous section, two extra areas are added in the SFI image for the SFIx preparation process:

- ‘E’ for an external firmware area
- ‘K’ for a key area (used for random keys generation by RSS)

The key ‘K’ area is optional and it can be stored in the area ‘F’.

Area E

The area ‘E’ is for external Flash memory. It includes the following information at the beginning of an encrypted payload:

- OTFD region_number (uint32_t):
  - 0…3: OTFD1 (STM32H7A/B an STM32L5)
  - 4…7: OTFD2 (STM32H7A/B)
- OTFD region_mode (uint32_t) bit [1:0]:
  - 00: instruction only AES-CTR)
  - 01: data only (AES-CTR)
  - 10: instruction + data (AES-CTR)
  - 11: instruction only (EnhancedCipher)
- OTFD key_address in internal Flash memory (uint32_t)

After this first part, area ‘E’ includes the firmware payload (as for area ‘F’). The destination address of area ‘E’ is in external Flash memory (0x9… / 0x7…).

Area K

The area ‘K’ triggers generation of random keys by the RSS. It contains N couples; each one defines a key area as follows:

- the size of the key area (uint32_t)
- the start address of the key area (uint32_t): address in internal Flash memory.

Example of an area ‘K’:

0x00000002 0x00000080 0x08010000 0x00000020 0x08010100

There are two key areas:

- the first key area starts at 0x08010000 with size = 0x80 (8 x 128-bits keys)
- the second key area starts at 0x08010100 with size 0x20 (256-bits key)

The STM32TrustedPackageCreator overview below (Figure 17: RAM size and CT address inputs used for SFIx multi install) shows the RAM size input for SFIx image generation, and also the ‘Continuation token address’ input, which is used by SFIx multi install to store states in external/internal Flash memory during SFIx programming.

The ‘Continuation token address’ is mandatory due to the image generation which adds areas P and R whatever be the configuration.
Encrypted firmware (SFI) / module (SMI) preparation using STM32TrustedPackageCreator

Figure 17. RAM size and CT address inputs used for SFIx multi install

Note: To prepare an SFIx image from multiple firmware files, make sure that there is no overlap between their segments (Intern and extern), otherwise an error message appears as same as in the SFI use case.
The final output from this generation process is a single file, which is the encrypted and authenticated internal/external firmwares in ".sfix" format. The SFIx format layout is described in Figure 18.

Figure 18. SFIx format layout

![SFIx format layout diagram](image-url)
When the SFIx image is split during generation, areas ‘P’ and ‘R’ appear in the SFIx image layout, as in the example below *Figure 19*.

*Figure 19. SFIx image layout in case of split*
3.4 SMI generation process

SMI is a format created by STMicroelectronics that aims to protect partners’ Software (SW: software modules and libraries).

The SMI preparation process is described below (Figure 20).

Figure 20. SMI preparation mechanism
The SMI generation steps as currently implemented in the tool are described in the diagram below *(Figure 21)*.

**Figure 21. SMI image generation process**

The AES-GCM encryption is performed using the following inputs:

- 128-bit AES encryption key
- The input nonce as Initialization Vector (IV)
- The security version as Additional Authenticated Data (AAD).
Before SMI image creation, PCROP checks are performed on the SMI image validity:

- A PCROP section must be aligned on a Flash word (256 bits), otherwise a warning is shown.
- The section’s size must be at least 2 Flash words (512 bits), otherwise a warning is shown.
- The section must end on a Flash word boundary (a 256-bit word), otherwise a warning is shown.
- If the start address of the section immediately following the PCROP section overlaps the last Flash word of the PCROP section (after performing the PCROP alignment constraint), the generation fails and an error message appears.

If everything is OK, two outputs are created under the specified path:

- The SMI image (Figure 22 represents the SMI format layout).
- The library data part.

**Figure 22. SMI format layout**
3.5 **STM32TrustedPackageCreator tool in the command line interface**

This section describes how to use the STM32TrustedPackageCreator tool from the command line interface in order to generate SFI/SFIx and SMI images. The available commands are listed in Figure 23.

Figure 23. STM32TrustedPackageCreator tool - available commands

```
SFI preparation options:
  -sfl, --sfl  : Generate SFI image.
  --flr, --firmware : add input firmware file
  --flx, --firma : add input for external firmware file
  -f, --fname  : supported firmware files are ELF, HEX, SRC, BIN
  --flr : only in case of BIN input file (in any base)
  --flx : only in case of BIN input file (in any base), only the last 16-bit value of the instruction
  -k [key] : AES-CTB key
  -nx : nonces (AES-CTB)
  --v, --ver  : version number
  --sfl_file, --sfl_file : SFI file contains a set of couple (size, start address)
  --sfl_file : SFI file, its size must be 12 bytes
  --sfl_file : STM32TrustedPackageCreator tool - available commands

SMI preparation options:
  -smd, --smd  : Generate SMI image.
  --elf, --elf_file : ELF file
  --elf_file : ELF file
  --sec : section to be encrypted
  --sec : section name is the elf file
  --key, --key_file : AES-CTB encryption key
  --key_file : SMI file, its size must be 10 bytes
  --sec : SMI file, its size must be 12 bytes
  --smd_file, --smd_file : SMI file to be created
  --clear : clear elf file
```
3.5.1 Steps for SFI generation (CLI)

In order to generate an SFI/SFIx image in CLI mode, the user must use the “-sfi, --sfi” command followed by the appropriate inputs. Inputs for “sfi” command are:

- **-fir, --firmware**

**Description:** adds an input firmware file (supported formats are Bin, Hex, Srec and ELF). This option can be used more than once in order to add multiple firmware files.

**Syntax:**
```bash
-fir <Firmware_file> [Address]
```

- **-firx, --firmwx**

**Description:** Add an input for external firmware file. Supported formats are Bin, Hex, Srec and ELF. This option can be used more than once in order to add multiple firmware files.

**Syntax:**
```bash
-firx <Firmware_file> [Address] [Region_Number] [Region_Mode] [key_address]
```

- **-k, --key**

**Description:** sets the AES-GCM encryption key.

**Syntax:**
```bash
-k <Key_file>
```

- **-n, --nonce**

**Description:** sets the AES-GCM nonce.

**Syntax:**
```bash
-n <Nonce_file>
```

- **-v, --ver**
Description: sets the image version.

Syntax: `-v  <Image_version>

   <Image_version> : A value between 0 and 255 in any base.

-ob, --obfile

Description: provides an option bytes configuration file.
The option bytes file field is only mandatory for SFI applications (first install) to allow
option bytes programming, otherwise it is optional.
Only CSV (comma separated value) file format is supported as input for this field, it
is composed from two vectors: register name and register value respectively.

Example: for STM32H7xx devices, 9 option bytes registers must be configured, which
corresponds to a total of 9 lines in the csv file (Figure 24).

Syntax: `-ob  <CSV_file>

   <CSV_file> : A csv file with 9 values.

Figure 24. Option bytes file example

-m, --module

Description: adds an input SMI file.
This option can be used more than once in order to add multiple SMI files.
This is optional (used only for combined SFI-SMI).

Syntax: `-m <SMI_file>

   <SMI_file> : SMI file. [<Address>] : Address is provided only for relocatable SMI.

-rs, --ramsize

Description: define the available ram size (in case of SFI multi-install)

Syntax: `-rs <Size>

   <Size> : RAM available size in bytes

-ct, --token

Description: continuation token address (in case of SFI multi-install)

Syntax: `-ct <Address>

   <Address> : continuation token Flash address

-o, --outfile
Description: sets the output SFI file to be created.

Syntax: `-o <out_file>

*out_file* : the SFI file to be generated (must have the "sfi" extension).

Example of SFI generation command using an ELF file:

```
STM32TrustPackageCreator_CLI.exe -sfi -fir tests.axf -k
test_firmware_key.bin -n nonce.bin -ob ob.csv -v 23 -o out.sfi
```

The result of previous command is shown in Figure 25.

Figure 25. SFI generation example using an Elf file

3.5.2 Steps for SMI generation (CLI)

In order to generate an SMI image in CLI mode, the user must use the "-smi, --smi" command followed by the appropriate inputs.

Inputs for the "smi" command are:

- `-elf, --elfile`
  
  Description: sets the input ELF file (only ELF format is supported).

  Syntax: `-elf <ELF_file>

  *<ELF_file>* : ELF file. An ELF file can have any of the extensions: "elf", "axf", "o", "so", "out".

- `-s, --sec`
  
  Description: sets the name of the section to be encrypted.

  Syntax: `-s <section_name>

  *<section_name>* : Section name.

- `-k, --key`
  
  Description: sets the AES-GCM encryption key.

  Syntax: `-k <Key_file>

  *<Key_file>* : A 16-byte binary file.

- `-n, --nonce`
  
  Description: sets the AES-GCM nonce.

  Syntax: `-n <Nonce_file>

  *<Nonce_file>* : A 12-byte binary file.

- `-sv, --sver`
  
  Description: sets the security version file
The security version file is used to make the SMI image under preparation compatible with a given RSS version, since it contains a corresponding identifying code (almost the HASH of the RSS).

**Syntax:**
```plaintext
-sv  <SV_file>
```


**-o, --outfile**

**Description:** Sets the SMI file to be created as output

**Syntax:**
```plaintext
-o  <out_file>
```

`<out_file>`: SMI file to be generated, must have the .smi extension.

**-c, --clear**

**Description:** Sets the clear ELF file to be created as output corresponding to the data part of the input file

**Syntax:**
```plaintext
-c  <ELF_file>
```

`<ELF_file>`: Clear ELF file to be generated.

Example of SMI generation command:

```plaintext
STM32TrustPackageCreator_CLI.exe   –smi   -elf   FIR_module.axf   -s   "ER_PCROP"   -k   test_firmware_key.bin   -n   nonce.bin   -sv   svFile   -o   test.smi   -c   clear.smi
```

**Figure 26. SMI generation example**

The section does not end on a Flash word boundary.

SUCCESS
3.6 Using the STM32TrustedPackageCreator tool graphical user interface

The STPC is also available in graphical mode, this section describes its use. The STM32TrustedPackageCreator tool GUI presents two tabs, one for SFI generation, one for SFix generation and one for SMI generation.

3.6.1 SFI generation using STPC in GUI mode

*Figure 27* shows the graphical user interface tab corresponding to SFI generation.

![Figure 27. SFI generation Tab](image)

To generate an SFI image successfully from the supported input firmwares formats, the user must fill in the interface fields with valid values.
SFI GUI tab fields

- Firmware files:
  The user needs to add the input firmware files with the “Add” button.
  If the file is valid, it is appended to the “input firmware files” list, otherwise an error message box appears notifying the user that either the file could not be opened, or the file is not valid.
  Clicking on “input firmware file” causes information related information to appear in the “Firmware information” section (Figure 28).

Figure 28. Firmware parsing example
• Encryption key and nonce file:
The encryption key and nonce file are selected by entering their paths (absolute or relative), or by selecting them with the “Open” button. Notice that sizes must be respected (16 bytes for the key and 12 bytes for nonce).

• Option bytes file:
The option bytes file are selected the same way as the encryption key and nonce. Only csv files are supported.

• SMI files:
SMI files are added the same way as the firmware files. Selecting a file causes related information to appear in the “Firmware information” section.

• Image version:
Choose the image version value of the SFI under generation within this interval: [0..255].

• Output file:
Sets the folder path in which the SFI image is to be created. This is done by entering the folder path (absolute or relative) or by using the “Select folder” button.

Note: By using the “Select folder” button, the name “out.sfi” is automatically suggested. This can be kept or changed.

• ‘Generate SFI’ button:
Once all fields are filled in properly, the “Generate SFI” button becomes enabled. The user can generate the SFI file by a single click on it.

If everything goes well, a message box indicating successful generation appears (Figure 29: SFI successful generation in GUI mode example) and information about the generated SFI file is displayed in the SFI information section.
Figure 29. SFI successful generation in GUI mode example
3.6.2 SFIx generation using STPC in GUI mode

*Figure 30 shows* the graphical user interface tab corresponding to SFIx generation.

*Figure 30. SFIx generation Tab*

To generate an SFIx image successfully from the supported input firmware formats, the user must fill in the interface fields with valid values.
SFIx GUI tab fields

Firmware files: The user needs to add the input firmware files with the "Add" button. If the file is valid, it is appended to the "input firmware files" list, otherwise an error message box appears notifying the user that either the file could not be opened, or the file is not valid. Clicking on "input firmware file" causes information related information to appear in the "Firmware information" section (Figure 31).

As is the case for the SFI use case, once all fields are filled in properly, the "Generate SFIx" button becomes enabled. The user can generate the SFIx file by a single click on it. If everything goes well, a message box indicating successful generation appears (Figure 32: SFIx successful generation in GUI mode example) and information about the generated SFIx file is displayed in the SFIx information section.

![Figure 31. Firmware parsing example](image)
Figure 32. SFIx successful generation in GUI mode example
3.6.3 SMI generation using STPC in GUI mode

Figure 33 shows the graphical user interface tab corresponding to SMI generation.

To generate an SMI image successfully from an Elf file, the user must fill in the interface fields with valid values.
SMI GUI tab fields

- Elf file:
  In this case the input file can only be an elf file.
  If the file is valid, information is displayed in the “ELF information” tab, otherwise an error message box appears notifying the user that either the file could not be opened or the file is not valid.

- Encryption key and nonce file:
  As for SFI, the encryption key and nonce file are selected in the same way as the Elf file. Notice that sizes must be respected (16 bytes for the key and 12 bytes for nonce file).

- Security version file:
  The security version file is used for the same purpose as explained in the CLI section. The security version file size must be 16 bytes.

- Section:
  This is a section list that can be used to select the name of the section to be encrypted.

- Output files:
  Sets the folder path into which the SMI image and its clear part are to be created. This is done by entering the folder path (absolute or relative) or by using the “Select folder” button.

  Note: For both output fields, when using the “Select folder” button, a name is suggested automatically. This can be kept or changed.

- ‘Generate SMI’ button:
  When all fields are filled in properly the ‘Generate SMI’ button is enabled, and the user can generate the SMI file and its corresponding clear data part by a single click on it. A message box informing the user that generation was successful must appear (Figure 34: SMI successful generation in GUI mode example), with additional information about the generated SMI file displayed in the ‘SMI information’ section. In the case of invalid input data, an error message box appears instead.
Encrypted firmware (SFI) / module (SMI) preparation using STM32TrustedPackageCreator

Figure 34. SMI successful generation in GUI mode example
3.6.4 Settings

The STPC allows generation to be performed respecting some user-defined settings. The settings dialog are displayed by clicking the settings icon (see Figure 35) in the tool bar or in the menu bar by choosing: Options -> settings.

Figure 35. Settings icon and Settings dialog box

Settings can be performed on:

- **Padding byte:**
  When parsing Hex and Srec files, padding can be added to fill gaps between close segments in order to merge them and reduce the number of segments. The user might choose to perform padding either with 0xFF (default value) or 0x00.

- **Settings file:**
  When checked, a “settings.ini” file is generated in the executable folder. It saves the application state: window size and fields contents.

- **Log file:**
  When checked, a log file is generated in the selected path.
3.6.5 Log generation

A log can be visualized by clicking the “log” icon in the tool bar or menu bar: Options-> log. Figure 36 shows a log example:

Figure 36. Log example
3.6.6 SFI and SMI file checking function

This function checks the validity and information parsing of an SFI or SMI file. It is accessed by clicking the Check SFI/SMI button in the tool bar or the menu bar: File -> Check SFI/SMI.

*Figure 37* shows a check SFI example:

*Figure 37. Check SFI file example*
4 Encrypted firmware (SFI/SFIx) / module (SMI) programming using STM32CubeProgrammer

STM32CubeProgrammer is a tool for programming STM32 devices through UART, USB, SPI, CAN, I2C, JTAG and SWD interfaces. So far, programming via JTAG/SWD is only supported with ST-LINK probe.

The STM32CubeProgrammer tool currently also supports secure programming of SFI and SMI images using UART, USB, SPI, JTAG/SWD interfaces, and SFIx using only JTAG/SWD interfaces. The tool is currently available only in CLI mode, it is available free of charge from www.st.com.

4.1 Chip certificate authenticity check and license mechanism

The SFI solution was implemented to provide a practical level of IP protection chain from the firmware development up to Flashing the device, and to attain this objective, security assets are used, specifically device authentication and license mechanisms.

4.1.1 Device authentication

The device authentication is guaranteed by the device’s own key.

In fact, a certificate is related to the device’s public key and is used to authenticate this public key in an asymmetric transfer: the certificate is the public key signed by a Certificate Authority (CA) private key. (This CA is considered as fully trusted).

This asset is used to counteract usurpation by any attacker who could substitute the public key with their own key.

4.1.2 License mechanism

One important secure Flashing feature is the ability of the firmware provider to control the number of chips that can be programmed. This is where the concept of licenses comes into play. The license is an encrypted version of the firmware key, unique to each device and session. It is computed by a derivation function from the device’s own key and a random number chosen from each session (the nonce).

Using this license mechanism the OEM is able to count each install for a given piece of firmware, since each license is specific to a unique chip, identified by its public key.

Licenses mechanism general scheme

When a firmware provider wants to distribute new firmware, they generate a firmware key and use it to encrypt the firmware.

When a customer wants to download the firmware to a chip, they send a chip identifier to the provider server, HSM or any provider license generator tool, which returns a license for the identified chip. The license contains the encrypted firmware key, and only this chip can decrypt it.
License distribution

There are many possible ways for the firmware provider to generate and distribute licenses:

- **Server based:** an internet server can be set up, and when a customer needs to Flash the firmware on to a chip, they connect to the server which generates a license for this chip.
- **HSM based:** Hardware security modules can be built, one of which is installed on the programming house production line.
- **Licenses can be generated in advance** (but the firmware provider must know which chips to generate licenses for).

There is no STMicroelectronics secret involved in license generation, so each firmware provider is free to choose their preferred method.

For ST we offer an SFI solution based on SmartCards HSM as a license distribution tool for use in programming houses.

HSM programming by OEM for license distribution

When an OEM needs to deliver an HSM to a programming house for deployment as a license generation tool for programming of relevant STM32 devices, some customization of the HSM must first be performed.

The HSM needs to be programmed with all the data needed for the license scheme deployment. In the production line, a dedicated API is available for each piece of data to be programmed in the HSM.

These data are:

- **The counter:** the counter is set to a maximum value that corresponds to the maximum number of licenses that could be delivered by the HSM. It aims to prevent over-programming.
  - It is decremented with each license delivered by the HSM.
  - No more licenses are delivered by the HSM once the counter is equal to zero.
  - The maximum counter value must not exceed a maximum predefined value, which is 16 K for HSM version 1.0.

- **The firmware key:** this key is 32 bytes and is composed of two fields, the initialization vector (IV) (first field) and the key (last field) that were used to AES128-GCM encrypt the firmware.
  - Both fields are 16 bytes long, but the last 4 bytes of the IV must be zero (only 96 bits of IV are used in the AES128-GCM algorithm).
  - Both fields must remain secret; that’s why they are encrypted before being sent to the chip.
  - The key and IV remains the same for all licenses for a given piece of firmware. However, they must be different for different firmware or different versions of the same firmware.

- **The firmware identifier:** allows the correct HSM to be identified for a given firmware.

- **The personalization data:** these 108 data bytes are encrypted before being sent to the chip, where each device has its own configuration. It is only used for HSM version 1.

The HSM must be in “OPERATIONAL STATE” (locked) when shipped by the OEM to guarantee his data confidentiality and privacy.
ST provides the tools needed to support SFI/SF\text{x} via HSM. In fact, HSM programming is supported by the STM32TrustedPackageCreator tool. Figure 38 shows the GUI for HSM programming in STPC tool.

Figure 38. HSM programming GUI in the STPC tool

During SFI install, STM32CubeProgrammer communicates with the device to get the chip certificate, upload it into the HSM to request the license. Once the license is generated by the HSM, it gives it back to the STM32 device. This process is illustrated in Figure 39: HSM programming toolchain.
4.2 Secure programming using bootloader interface

4.2.1 Secure firmware installation using Bootloader interface flow

The production equipment on the OEM-CM production line needs to be equipped with a Flashing Tool (FT) supporting the programming of SFI images. The Flashing tool to be used on OEM-CM production line is STM32CubeProgrammer, which is given the data blob prepared by the STPC, containing the image header and the encrypted image data blob.

Note: The SFI install is performed successfully only if a valid license is given to the Flashing tool.

STM32CubeProgrammer supports secure firmware install for such device as well as STM32H7, STM32L4, STM32L5, STM32MP devices so far.

For STM32H753xl devices, one important outcome is that RSS fully manages the installation (no secure bootloader) and SFI is supported for USART, SPI and USB interfaces for those devices. Otherwise for STM32L462CE specific part number devices the installation is performed through a secure bootloader via USART or SPI interfaces only.

For more details on SFI over these STM32 devices refer to AN4992 [1]. This document is available on www.st.com.

For further details, refer to the SFI-HSM specification document and the SFI-HSM user manual (UM2428) [2].
The general flow of the secure firmware installation using bootloader interface on a chip for H7 and L4 secure devices is shown respectively in *Figure 40* and *Figure 41* below.

**Figure 40. Secure programming via STM32CubeProgrammer overview on STM32H7 devices**

![STM32H7 Diagram](image1)

**Figure 41. Secure programming via STM32CubeProgrammer overview on STM32L4 devices**

![STM32L4 Diagram](image2)
4.2.2 Secure Module installation using bootloader interface flow

As explained in Section 3.4: SMI generation process, outputs are generated for this particular use case:

- The first part, not encrypted: this is a regular ELF/AXF file, containing all the sections except the code section extracted by the STPC to prepare the SMI module.
- The encrypted SMI module, which contains the protected code.

The first part is programmed into the chip using any means (JTAG Flasher, UART Bootloader and so on, as for any regular ELF/AXF file.

The full content of the SMI image file and its corresponding license are given to STM32CubeProgrammer which places them in RAM

The `RSS_SMI_resetAndInstallModules()` function is then invoked through the `start_smi()` secure bootloader command with the following parameters:

- Pointer to the license
- Pointer to the content of the SMI image file.

This causes a reset and the decryption, authentication and install of the protected module code into a properly setup Pc-ROP area

Note: The SMI install is performed successfully only if the adequate license is given to the Flashing tool.

4.2.3 STM32CubeProgrammer for SFI using bootloader interface

For SFI programming, the STM32CubeProgrammer is used in CLI mode (the only mode so far available) by launching the following command:

- `sfi`, `--sfi`

**Syntax:** `-sfi protocol=<Ptype> <file_path> <licenseFile_path>`

  `[<protocol=Ptype>]` : Protocol type to be used : static/live
  Only static protocol is supported so far
  Default value static

  `<file_path>` : Path of sfi file to be programmed

  `[hsm=0|1]` : Set user option for HSM use value in
  `{0 (do not use HSM), 1 (use HSM)}
  Default value : hsm=0

  `<lic_path|slot=slotID>` : Path to the SFI license file (if hsm=0)
  or reader slot ID if HSM is used (hsm=1)

  `[<licMod_path>|slot=slotID]` : List of the integrated SMI license files paths

If HSM is not used (hsm=0) or readers slot ID list if HSM is used (hsm=1, used only in combined cases). The list order must correspond to modules integration order within the SFI file.
4.2.4 STM32CubeProgrammer for SMI via Bootloader interface

For SMI programming, STM32CubeProgrammer is used in CLI mode by launching the following command:

```
-smi, --smi
```

**Syntax:**

```
-smi protocol=<Ptype> <file_path> [<address>] <licenseFile_path>
```

- `<protocol=Ptype>` : Protocol type to be used : static/live
- `Only static protocol is supported so far`:
- `Default value static`
- `<file_path>` : Path of smi file to be programmed
- `[hsm=0|1]` : Set user option for HSM use
- `value in {0 (do not use HSM), 1 (use HSM)}`
- `Default value: hsm=0`
- `[<address>]` : Destination address of the smi module
- `needed only for relocatable SMI`
- `<lic_path|slot=slotID>` : Path to the SMI license file (if hsm=0) or reader slot ID if HSM is used (hsm=1)

Example using UART bootloader interface:

```
STM32_Programmer.exe -c port=COM1 br=115200 -sfi protocol=static "C:\SFI\data.sfi" "C:\SFI\license.bin"
```

This command allows secure installation of firmware “data.sfi” into a dedicated Flash memory address.

**Example using UART bootloader interface:**

```
STM32_Programmer.exe -c port=COM1 br=115200 -smi protocol=static "C:\SMI\data.smi" 0x08080000 "C:\SMI\license.bin"
```

This command allows programming of the SMI specified file “data.smi” into a dedicated PCROPed area.

4.2.5 STM32CubeProgrammer for certificate via Bootloader interface

To get the chip certificate, STM32CubeProgrammer is used in CLI mode by launching the following command:

```
-gc, --getcertificate
```

**Syntax:**

```
-gc <file_path>
```

Example using UART bootloader interface:

```
STM32_Programmer.exe -c port=COM1 -gc "C:\Demo_certificate.bin"
```

This command allows the chip Certificate to be read and uploaded into the specified file: "C:\Demo_certificate.bin"

The execution results are shown in [Figure 42](#).
4.3 Secure programming using JTAG/SWD interface

4.3.1 SFI/SFIx programming using JTAG/SWD flow

It is also possible to program the SFI/SFIx image using the JTAG interface. Here the read out protection mechanism (RDP level 1) cannot be used during SFI/SFIx as user Flash memory is not accessible after firmware chunks written to RAM through the JTAG interface.

The whole process happens in RDP level 0. The code in Flash memory is protected from the debugger by the PCROP mechanism. The whole user Flash memory is PCROPed during SFIx. In the case of SFIx programming the code is protected by the OTFDEC crypt.

One important outcome is that RSS fully manages the installation. This means that the whole SFI/SFIx image and the license must be transferred to RAM before starting. The SFI/SFIx image header and areas can be written to different locations. However, in the case of STM32L5 product not only the RSSe manages the installation and its binary file is located in STM32CubeProgrammer bin/RSSe folder.

SFI via debug interface is currently supported for STM32H753I, STM32H7A/B, STM32L4, STM32L5 devices.

SFIx via debug interface is currently supported for STM32H7A/B and STM32L5 devices.

For these devices, there is around 1 Mbyte of RAM available, with 512 Kbytes in main SRAM. This means that the maximum image size supported is 1 Mbyte, and the maximum area size is 512 Kbytes.

To remedy this, we resort to splitting the SFI/SFIx image into several parts, so that each part fits into the allowed RAM size.

An SFI/SFIx multi install is then performed. Once all its SFI/SFIx parts are successfully installed, the global SFI/SFIx image install is successful.

Other limitations are that security must be left activated in the configuration area if there is a PCROP area. In case of STM32L5 product the RDP Level must be set on 0.5.
Encrypted firmware (SFI/SFIx) / module (SMI) programming using STM32CubeProgrammer

The SFI flow for programming through JTAG is described in Figure 43.

**Figure 43. SFI programming by JTAG/SWD flow overview**

(monolithic SFI image example)
The SFIx flow for programming through JTAG is described in Figure 44 through Figure 46.

Figure 44. SFIx programming by JTAG/SWD flow overview (monolithic SFIx image example) (1)
Figure 45. SFix programming by JTAG/SWD flow overview
(monolithic SFix image example) (2)
Figure 46. SFIx programming by JTAG/SWD flow overview (monolithic SFIx image example) (3)
4.3.2 SMI programming through JTAG/SWD flow

For SMI programming through JTAG/SWD the process flow is similar to that using the UART bootloader. In fact, RSS fully manages the installation in both cases.

This means that the whole SMI image and its corresponding license must be transferred to RAM before starting. Then, to access RSS services through JTAG, there are two options:

- write a small program in RAM that calls the public API
- use the secure API directly.

Once the RSS function "RSS_SMI_resetAndInstallModules" execution has finished successfully, the SMI module is decrypted, Flashed and protected by the PCROP mechanism.

The essential steps of the SMI programming by JTAG flow are described in Figure 47: SMI programming by JTAG flow overview.
Figure 47. SMI programming by JTAG flow overview
4.3.3 STM32CubeProgrammer for secure programming using JTAG/SWD

The only modification in the STM32CubeProgrammer secure command syntax is the connection type which must be set to “jtag” or “swd”, otherwise all secure programming syntax for supported commands is identical.

Note: Using a debug connection “HOTPLUG” mode must be used with the connect command.

Example of “getcertificate” command using JTAG:

STM32_Programmer.exe -c port=jtag mode=HOTPLUG -gc testJTAG_Certif.bin

The result of this example is shown in Figure 48.

![Figure 48. Example of getcertificate command using JTAG](image)

Example of “smi” command using SWD

-c port=swd mode=HOTPLUG -smi protocol=static
"RefSMI_MDK/FIR_module.smi" "RefSMI_MDK/licenseSMI.bin" -vb 3 -log
5 Example SFI programming scenario

5.1 Scenario overview

The actual user application to be installed on the STM32H753xl (or STM32L5) device makes “printf” packets appear in serial terminals. The application was encrypted using the STPC.

The OEM provides tools to the CM to get the appropriate license for the concerned SFI application.

5.2 Hardware and software environment

For successful SFI programming, some hardware and SW prerequisites are needed:

- STM32H743I-EVAL board
- STM32H753xl with Bootloader v13.2-RC2 and RSS v0.9 programmed
- RS232 cable for SFI programming via UART
- Micro-USB for debug connection
- PC running on either Windows 7 or Ubuntu 14 in both 32-bit and 64-bit versions
- STM32TrustPackageCreator v0.2.0 (or greater) package available from www.st.com
- STM32CubeProgrammer v0.4.0 (or greater) package available from www.st.com.

5.3 Step-by-step execution

5.3.1 Build OEM application

OEM application developers can use any IDE to build their own firmware.

5.3.2 Perform the SFI generation (GUI mode)

To be encrypted with the STM32TrustedPackageCreator Tool, OEM firmware is provided in AXF format in addition to a CSV file to set the option bytes configuration. A 128-bit AES encryption key and a 96-bit nonce are also provided to the tool. They are available in the “SFI_ImagePreparation” directory.

An “.sfi” image is then generated (out.sfi).

*Figure 49: STPC GUI during SFI generation* shows the STPC GUI during the SFI generation.
Figure 49. STPC GUI during SFI generation
5.3.3 Performing HSM programming for license generation using STPC (GUI mode)

The OEM must provide a license generation tool to the programming house to be used for license generation during the SFI install process.

In this example, HSMs are used as license generation tools in the field. See Section 4.1.2: License mechanism for HSM use and programming.

*Figure 50* shows an example for HSM programming by OEM to be used for SFI install.

The maximum number of licenses delivered by the HSM in this example is 1000.

This example uses HSM version 2, and is also valid for version 1 when the ‘Version’ field is set accordingly. The HSM version can be identified before performing the programming operation by clicking the Refresh button to make the version number appear in the ‘Version’ field.

*Figure 50. Example of HSM programming using STPC GUI*
Note: When using HSM version 1, the “Personalization data file” field is ignored when programming starts. It is only used with HSM version 2.

When the card is successfully programmed, a popup window message “HSM successfully programmed” appears, and the HSM is locked. Otherwise an error message is displayed.

5.3.4 Performing HSM programming for license generation using STPC (CLI mode)

STM32TrustedPackageCreator provides CLI commands to program HSM cards. In order to configure the HSM before programming, the user must provide the mandatory inputs by using the specific options.

Example of HSM version 1 provisioning:

STM32TrustedPackageCreator_CLI -hsm -i 1 -k "C:\TrustedFiles\key.bin" -n "C:\TrustedFiles\nonce.bin" -id HSMv1_SLOT_1 -mc2000

- -i: select the slot ID
- -k: set the encryption key file path
- -n: set the nonce file path
- -id: set the firmware identifier
- -mc: set the maximum number of license.

Example of HSM version 2 provisioning:

A new option [-pd] must be inserted to include the personalization data:

STM32TrustedPackageCreator_CLI -hsm -i 1 -k "C:\TrustedFiles\key.bin" -n "C:\TrustedFiles\nonce.bin" -id HSMv2_SLOT_2 -mc 2000 -pd "C:\TrustedFiles\enc_ST_Perso_L5.bin"

- -pd: Set the personalization data file path.

Note: A green message display indicates that the programming operation succeeded, otherwise a red error message is displayed.

If the HSM is already programmed and there is a new attempt to reprogram it, an error message being displayed to indicate that the operation failed and the HSM is locked.

Example of HSM get information:

If the HSM is already programmed or is virgin yet and whatever the version, a get information command can be used to show state details of the current HSM by using the command below:

STM32TrustedPackageCreator_CLI -hsm -i 1 -info
5.3.5 Programming input conditions

Before performing an SFI install be sure that:

- Flash memory is erased.
- No PCROPed zone is active, otherwise destroy it.
- The chip must support security (a security bit must be present in the option bytes).
- When using a UART interface the User security bit in option bytes must be enabled before launching the SFI command. For this, the following STM32CubeProgrammer command is launched:
  - Launch the following command (UART Bootloader used => Boot0 pin set to VDD):
    ```
    -c port=COM9 -ob SECURITY=1
    ```
  - When using a UART interface the Boot0 pin must be set to VSS:
    - After enabling security (boot0 pin set to VDD), a power off/power on is needed when switching the Boot0 pin from VDD to VSS: power off, switch pin then power on.
- When performing an SFI install using UART BL then, no debug interface must be connected to any USB host. If a debug interface is still connected, disconnect it then perform a power off/power on before launching the SFI install to avoid any debug intrusion problem.
- Boot0 pin set to VDD When using a debug interface.
- A valid license generated for the currently-used chip must be at your disposal, or a license generation tool to generate the license during SFI install (HSM).
5.3.6 Perform the SFI install using STM32CubeProgrammer

In this section the STM32CubeProgrammer tool is used in CLI mode (the only mode so-far available for secure programming) to program the SFI image “out.sfi” already created in the previous section.

STM32CubeProgrammer supports communication with ST HSMs (Hardware Secure Modules based on smart card) to generate a license for the connected STM32 device during SFI install.

Using JTAG/SWD

After making sure that all the input conditions are respected, open a cmd terminal and go to `<STM32CubeProgrammer_package_path>/bin`, then launch the following STM32CubeProgrammer command:

STM32_Programmer_CLI.exe -c port=swd mode=HOTPLUG -sfi protocol=static "<local_path>/out.sfi" hsm=1 slot=<slot_id>

Note: In the case of an STM32L5 device the SFI install uses the RSSe and its binary file is located in the STM32CubeProgrammer bin/RSSe folder.

The STM32CubeProgrammer command is as follows:

STM32_Programmer_CLI.exe -c port=swd mode=HOTPLUG -sfi protocol=static "<local_path>/out.sfi" hsm=1 slot=<slot_id> -rsse <RSSe_path>
Figure 52 shows the SFI install via SWD execution and the HSM as license generation tool in the field.

Figure 52. SFI install success using SWD connection (1)
Figure 53. SFI install success using SWD connection (2)

RSS command execution OK  
Reconnecting...  
ST-LINK SN: 0672FFE54949677867834831  
ST-LINK Firmware version: U2330MI9  
Target voltage: 3.21V  
Error: ST-LINK error (DEU_NO_DEVICE)  
Retrying...  
ST-LINK SN: 0672FFE54949677867834831  
ST-LINK Firmware version: U2330MI9  
Target voltage: 3.21V  
SWD frequency: 4000 KHz  
Connection code: Hot Plug  
Device ID: 0x450  
Reconnected!  
Requesting security state...  
SECURITY State Success  
SFI SUCCESS!  
SFI file out_EN.sfi Install Operation Success
6 Example SMI programming scenario

6.1 Scenario overview

In this scenario, the 3rd party's library to be installed on the STM32H753xI device makes "printf" packets appear in the serial terminal if the library code execution called by the application does not crash.

The library code was encrypted using the STPC.

The OEM provides tools to the CM to get the appropriate license for the concerned SMI module.

6.2 Hardware and software environment

The same environment as explained in Section 4.1.1: Device authentication.

6.3 Step-by-step execution

6.3.1 Build 3rd party Library

ST or 3rd party developers can use any IDE to build the library to be encrypted and installed into the STM32H7 device.

In this scenario the SMI module based on the built library is not relocatable. The destination address is hardcoded in SMI module to the following value: 0x08080000.
6.3.2 Perform the SMI generation

For encryption with the STM32TrustedPackageCreator Tool, the 3rd party module is provided in ELF format. A 128-bit AES encryption key, a 96-bit nonce and a security version file are also provided to the tool. They are available in the “SMI_ImagePreparation” directory. After choosing the name of the section to be encrypted, a “.smi” image is then generated (FIR_module.smi).

The clear data part of the library without the encrypted section is also created in ELF format (FIR_module_clear.axf).

Figure 54 shows the STPC GUI during SMI generation.

![Figure 54. STPC GUI during SMI generation](image_url)
6.3.3 Programming input conditions

Before performing the SMI install be sure that:

- The SMI module destination address is not already PCROPed, otherwise destroy this PCROPed area.
- The Boot0 pin set to VDD.
- The chip supports security (existing security bit in option bytes).
- When performing SMI install using UART BL, no debug interface is connected to any USB host. If a debug interface is still connected, disconnect it then perform a power off/power on before launching the SMI install to avoid any debug intrusion problem.
- The proper license generated for the currently-used chip must be at your disposal (or an HSM or secure server to generate it during SMI programming).

6.3.4 Perform the SMI install

Using JTAG/SWD

After making sure that all the input conditions are respected, open a cmd terminal and go to <STM32CubeProgrammer_package_path>/bin, then launch the following STM32CubeProgrammer command:

```
STM32_Programmer_CLI.exe -c port=swd mode=HOTPLUG -smi protocol=static "<local_path>/FIR_module.smi"
"<local_path>/licenseSMI.bin"
```

This command allows the SMI specified file “FIR_module.smi” to be programmed into a dedicated PCROPed area at address (0x08080000).

*Figure 55: SMI install success via debug interface* shows the SMI install via SWD execution.
Figure 55. SMI install success via debug interface

```
C:\Users\hannah\Documents\Projects\STM32H7\project\docs\docs_forSTM32H7Release\STM32H7_project\docs\docs_forSTM32H7Release\STM32H7_project\docs\docs_forSTM32H7Release\STM32H7_project\docs\docs_forSTM32H7Release\STM32H7_project\docs\docs_forSTM32H7Release\STM32H7_project\documents\STM32H7_programmer_CLI.exe -p port -s SMI -n SMI -p port -s SMI -n SMI
```

```
RT-LINK Firmware version : V2.32.1M5
SWD frequency : 4000 KHz
Connection mode : Hot Plug
Device ID : 0x558

Protocol : static
SMI File Information :

SMI file path : C:\Users\hannah\Documents\Projects\STM32H7\project\docs\docs_forSTM32H7Release\STM32H7_project\documents\STM32H7_programmer_CLI.exe
SMI license file path : C:\Users\hannah\Documents\Projects\STM32H7\project\docs\docs_forSTM32H7Release\STM32H7_project\documents\STM32H7_programmer_CLI.exe
License file successfully written at address 0x24050000
Writing SMI module image to address 0x24050000

SMI image successfully written at address 0x24050000
Starting SMI process with license @ 0x24050000 and Image @ 0x24050000

USB process started...
USB command execution OK
Reconnecting...
RT-LINK Firmware version : V2.32.1M5
SWD frequency : 4000 KHz
Connection mode : Hot Plug
Device ID : 0x558

Reconnected

Requesting security state...
SECURITY State Success
SMI file C:\Users\hannah\Documents\Projects\STM32H7\project\docs\docs_forSTM32H7Release\STM32H7_project\documents\STM32H7_programmer_CLI.exe

Time elapsed during the SMI Install operation is: 08:00:03.294
```

```
78/104 AN5054 Rev 4
```
6.3.5 How to test for SMI install success

1. Flash the clear data part “FIR_module_clear.hex” (available under the “Tests” directory) into address 0x08084000 using STM32CubeProgrammer or any other Flashing tool.

2. Flash the test application “tests.hex” (which is based on the SMI module), available under the “Tests” directory at start user Flash address “0x08000000” using STM32CubeProgrammer or any other Flashing tool.

   The option bytes configuration becomes as below (Figure 56).

   **Figure 56. OB display command showing that a PCROP zone was activated after SMI**

   ![OB display command showing that a PCROP zone was activated after SMI](image)

   **OPTION BYTES BANK: 0**

   **Read Out Protection:**
   
   - **RDP**: 0x80 (Level 0, no protection)
   
   **RST:**
   
   - **RSL1**: 0x8 (No SPI process on going)
   
   **BOR Level:**
   
   - **BOR_LEV**: 0x0 (reset level is set to 2.1 V)

   **User Configuration:**
   
   - **IWDG**: 0x1 (Independent watchdog is controlled by hardware)
   
   - **NS1_STOP_D1**: 0x1 (STOP node on Domain 1 is entering without reset)
   
   - **NS1_STBY_D1**: 0x1 (STANDBY node on Domain 1 is entering without reset)
   
   - **FZ_UDG_STOP**: 0x1 (Independent watchdog is running in STOP node)
   
   - **FZ_UDB_STDY**: 0x1 (Independent watchdog is running in STANDBY node)
   
   **SECURITY**: 0x1 (Security feature enabled)

   **BCMP**: 0x1 (CM-7 boot enabled)

   - **NS2_STOP_D2**: 0x1 (STOP node on Domain 2 is entering without reset)
   
   - **NS2_STBY_D2**: 0x1 (STANDBY node on Domain 2 is entering without reset)

   **SRAM_BANK**: 0x0 (after boot loading, no SRAM for user sectors)

   **DMPA**: 0x1 (delete PchOP protection and erase protected area)

   **DMFA**: 0x1 (delete Secure protection and erase protected area)

   **Boot address Option Bytes:**

   - **BOOT_CM7_ADDR**: 0x8000 (0x80000000)
   
   - **BOOT_CM7_ADDR**: 0x1FFFF (0x1FFFF0000)

   **PCROP Protection:**

   - **PCROPa_str**: 0x8000 (0x80000000)
   
   - **PCROP END**: 0x8006 (0x80000006)

   **Secure Protection:**

   - **SECa_str**: 0xFF (0xBEE0FF00)
   
   - **SEC END**: 0x0 (0x00000000)

   **DMC RAM Protection:**

   - **ST_RAM_SIZE**: 0x2 (0x2 KB)

   **Write Protection:**

   - **nWRPA**: 0x1 (Write protection not active on this sector)
   
   - **nWRPA**: 0x1 (Write protection not active on this sector)
   
   - **nWRPA**: 0x1 (Write protection not active on this sector)
   
   - **nWRPA**: 0x1 (Write protection not active on this sector)
   
   - **nWRPA**: 0x1 (Write protection not active on this sector)
   
   - **nWRPA**: 0x1 (Write protection not active on this sector)
   
   - **nWRPA**: 0x1 (Write protection not active on this sector)
3. If a UART connection is available on the board used, open the “Hercule.exe” serial terminal available under the “Tests” directory, open the connection. On reset the dedicated “printf” packets appears.
7  **Example SFIx programming scenario for STM32H7**

7.1 **Scenario overview**

There are three steps during this scenario:
- Generate SFIx image using the STPC.
- Provisioning HSM card via STPC.
- Use STM32CubeProgrammer to perform the SFIx process.

If the actual scenario was successfully installed on the STM32H7B provides the following results:
- Write internal firmware data in the internal Flash memory starting at the address 0x08000000.
- Write external firmware data in the external Flash memory starting at the address 0x90000000.
- Verify that the option bytes were correctly programmed (depends on area C).

7.2 **Hardware and software environment**

For successful SFIx programming, some SW prerequisites are needed:
- STM32H7B board containing external Flash memory.
- Micro-USB for debug connection.
- PC running on either Windows 7/10 or Ubuntu 14 64-bit or macOS High Sierra.
- STM32TrustPackageCreator v1.2.0 (or greater) package available from www.st.com
- STM32CubeProgrammer V2.3.0 (or greater) package available from www.st.com
- HSMv1.1 card.

7.3 **Step-by-step execution**

7.3.1 **Build OEM application**

OEM application developers can use any IDE to build their own firmware.

*Note:* In this use case there are different user code each one is specific for each Flash memory type (Internal/external).

7.3.2 **Perform the SFIx generation (GUI mode)**

To be encrypted with the STM32TrustedPackageCreator Tool, OEM firmware is provided in Bin/Hex/AXF format in addition to a CSV file to set the option bytes configuration. A 128-bit AES encryption key and a 96-bit nonce are also provided to the tool.

An “.sfix” image is then generated (out.sfix).
7.3.3 Performing HSM programming for license generation using STPC (GUI mode)

The OEM must provide a license generation tool to the programming house to be used for license generation during the SFI install process.

In this example, HSMs are used as license generation tools in the field. See Section 4.1.2: License mechanism for HSM use and programming.

*Figure 58: Example of HSM programming using STPC GUI* shows an example for HSM programming by OEM to be used for SFix install.

The maximum number of licenses delivered by the HSM in this example is 1000.

This example uses HSM version 1. The HSM version can be identified before performing the programming operation by clicking the “Refresh” button to make the version number appear in the Version field.
Figure 58. Example of HSM programming using STPC GUI

Note: When using HSM version 1, the “Personalization data file” field is ignored when programming starts. It is only used with HSM version 2. When the card is successfully programmed, a popup window message “HSM successfully programmed” appears, and the HSM is locked. Otherwise an error message is displayed.
7.3.4 Performing HSM programming for license generation using STPC (CLI mode)

Refer to Section 5.3.4: Performing HSM programming for license generation using STPC (CLI mode).

7.3.5 Programming input conditions

Before performing an SFiX install be sure that:

- Use JTAG/SWD interface.
- No PCROPed zone is active, otherwise disable it.
- The chip must support security (a security bit must be present in the option bytes).
- The SFiX image must be encrypted by the same key/nonce used in the HSM provisioning.

7.3.6 Perform the SFiX install using STM32CubeProgrammer

In this section the STM32CubeProgrammer tool is used in CLI mode (the only mode so-far available for secure programming) to program the SFiX image "out.sfix" already created in the previous section.

STM32CubeProgrammer supports communication with ST HSMs (hardware secure modules based on smart card) to generate a license for the connected STM32 device during SFiX install.

After making sure that all the input conditions are respected, open a cmd terminal and go to `<STM32CubeProgrammer_package_path>/bin`, then launch the following STM32CubeProgrammer command:

Using JTAG/SWD

```
STM32_Programmer_CLI.exe -c port=swd mode=HOTPLUG -sfi protocol=static "<local_path>/out.sfix" hsm=1 slot=<slot_id> -el <ExternalLoader_Path>
```

Figure 59: SFiX install success using SWD connection (1) through Figure 62: SFiX install success using SWD connection (4) shows the SFiX install via SWD execution and the HSM as license generation tool in the field.
Figure 59. SFIx install success using SWD connection (1)
Figure 60. SFlx install success using SWD connection (2)

```plaintext
Parsing Area 5/7 :
  Area type : R
  Area size  : 32
  Area destination address : 0x0001020

Parsing Area 6/7 :
  Area type : R
  Area size  : 32
  Area destination address : 0x0001020

Parsing Area 7/7 :
  Area type : C
  Area size  : 36
  Area destination address : 0x0

Reading the chip Certificate...

Requesting Chip Certificate from device ...

Get Certificate done successfully

requesting license for the current STM32 device

Init Communication ...

ldo_loadModule(): loading module "stdliblI_SAM.dll" ...
ldo_loadModule(STM32): OK loading library "stdliblI_SAM.dll": 0x62870000 ...
C_GetFunctionList() returned 0x00000000, g_pFunctionList=0xe20EF56B
Pll llb initialization Success!

Opening session with slot ID 1...

Succeed to Open session with reader slot ID 1

Succeed to generate license for the current STM32 device

Closing session with reader slot ID 1...

Session closed with reader slot ID 1

Closing communication with HSM...

Communication closed with HSM

Succeed to get License for Firmware from HSM slot ID 1

Starting Firmware Install operation...

Erase external flash size : 513 startAddress : 0x00000000 endAddress : 0x000000200
Erasing external memory sector 0
```
Figure 61. SFIx install success using SWD connection (3)
Figure 62. SFIx install success using SWD connection (4)
8 Example SFIx programming scenario for STM32L5

8.1 Scenario overview

There are three steps during this scenario:
• Generate SFIx image using the STPC.
• HSM card provisioning via STPC.
• Use STM32CubePrg to perform the SFIx process.

Successful installation of this scenario on the STM32L5 provides the following results:
• The internal Flash memory is readable from base addresses 0x08000000 and 0x08040000. It contains the internal firmware.
• The external Flash is programmed so as to be readable with external Flash loader. You can then read the external Flash encrypted by the OTFDEC keys. The pattern of values must be present in the binary files of external firmware.
• If the application works correctly, LED4 blinks.

8.2 Hardware and software environment

For successful SFIx programming, some hardware and software prerequisites are needed:
• an STM32L5-based evaluation board containing external Flash memory
• a Micro-USB for debug connection
• a PC running on either Windows 7/10 or Ubuntu 14 64-bit or macOS High Sierra
• an STM32TrustPackageCreator v1.2.0 (or greater) package available from www.st.com
• an STM32CubeProgrammer V2.3.0 (or greater) package available from www.st.com
• an HSMv1.1 card.

8.3 Step-by-step execution

8.3.1 Build OEM application

OEM application developers can use any IDE to build their own firmware. Note that in this use case there are different user codes, each being specific for a Flash memory type (internal/external).

8.3.2 Perform the SFIx generation (GUI mode)

To be encrypted with the STM32TrustedPackageCreator Tool, OEM firmware is provided in Bin/Hex/AXF format in addition to a CSV file to set the option bytes configuration. A 128-bit AES encryption key and a 96-bit nonce are also provided to the tool.

An “.sfix” image is then generated (out.sfix).
Use case 1 generation of SFIx without key area:

Internal FW files:
1. Add a non-secure binary with start address equal to 0x08040000.
2. Add an internal binary file at 0x0C000000 (application to be executed after downloading SFIx to verify full process success by blinking an LED).
3. Add an OTFDEC key binary at 0x0C020000 (to be used as the key in OTFD ENC-DEC).

External FW files: add an external binary at 0x90000000 with these parameters:
   - Region number = 0
   - Region mode = 0x2
   - Key address = 0x0C020000 (same as the OTFDEC key binary).

Encryption key: use the same key as HSM.
Nonce file: use the same nonce as HSM.
Option bytes file: use .csv contains option-byte configuration.
RAM size: 0x19000 to split the input areas avoiding memory overflow.

Figure 63. Successful SFIx generation use case 1
Use case 2 generation of SFIx with key area:

This is essentially the same process as test case1. The main difference is:

- Add a “.kcsv” file (to be used in OTFD ENC-DEC during SFIx downloading) in the key area field, instead of using an OTFDEC key binary file.
- The key address for external FW files is the first address of the Area ‘K’ key file, which is 0x0C020000.

**Figure 64. Successful SFIx generation use case 2**

After the generation of the SFIx image in this use case the output file should contain 12 internal segments (F area), and 166 external segments (E area).
8.3.3 Performing HSM programming for license generation using STPC (GUI mode)

Refer to Section 7.3.3: Performing HSM programming for license generation using STPC (GUI mode).

8.3.4 Performing HSM programming for license generation using STPC (CLI mode)

Refer to Section 7.3.3: Performing HSM programming for license generation using STPC (GUI mode).

8.3.5 Programming input conditions

Before performing an SFIx install be sure that:

- A JTAG/SWD interface is used
- The chip supports security (a security bit must be present in the option bytes)
- The SFIx image is encrypted by the same key/nonce as is used in the HSM provisioning.
- The option bytes are:
  - DBank=1
  - nSWBOOT0=1
  - nBOOT0=1
  - RDP=AA

8.3.6 Perform the SFIx install using STM32CubeProgrammer

In this section the STM32CubeProgrammer tool is used in CLI mode (the only mode so-far available for secure programming) to program the SFIx image “out.sfix” already created in the previous section.

STM32CubeProgrammer supports communication with ST HSMs (Hardware Secure Modules based on smart card) to generate a license for the connected STM32 device during SFIx install.

After making sure that all the input conditions are respected, open a cmd terminal and go to `<STM32CubeProgrammer_package_path>/bin`, then launch the following STM32CubeProgrammer command:

**Using JTAG/SWD**

```
STM32_Programmer_CLI.exe -c port=swd mode=HOTPLUG -sfi protocol=static "<local_path>/out.sfix" hsm=1 slot=<slot_id> -rsse <RSSe_Path> -el <ExternalLoader_Path>
```

**Note:** The RSSe binary file is located in STM32CubeProgrammer install path in the bin/RSSe folder.

*Figure 65: SFIx install success using SWD connection (1)* through *Figure 69: SFIx install success using SWD connection (5)* show the SFIx install via SWD execution and the HSM as license generation tool in the field.
Figure 65. SFIx install success using SWD connection (1)

Figure 66. SFIx install success using SWD connection (2)
Figure 67. SFIx install success using SWD connection (3)
Figure 68. SFIx install success using SWD connection (4)

Area Type = F
Processing Area 4...
Area Address = 0x0
Area Type = F
Processing Area 5...
Area Address = 0x0
Area Type = R
Processing Area 6...
Area Address = 0x00000000
Area Type = E
Buffer Address = 0x20005100
E Area Full Size = 102336
E Area Data Size = 102320

Figure 69. SFIx install success using SWD connection (5)

Area Address = 0x140
Area Type = R
Processing Area 0...
Area Address = 0x00007c00
Area Type = E
Buffer Address = 0x20005100
E Area Full Size = 25302
E Area Data Size = 25376

Processing Area 37...
Area Address = 0x0b0
Area Type = P
Processing Area 38...
Area Address = 0x0c0
Area Type = R
Processing Area 39...
Can not verify last area
Area Address = 0x0
Area Type = C
SFI Process Finished!
SFI Flls C:\data_store\app_LID_V.sfix Install Operation Success
Time elapsed during SFI install operation: 00:01:25.116
9 Example combined SFI-SMI programming scenario

9.1 Scenario overview

The user application to be installed on the STM32H753xI device makes “printf” packets appear in the serial terminal.

In this case the OEM application is built based on a third party’s library as explained in IAR example (Section 2.3: Execute-only/position independent library scenario example under EWARM.)

The application is encrypted using the STPC, the SMI module corresponding to 3rd party’s library code is uploaded as input during combined SFI generation and represented as an area of type ‘M’ within firmware application areas.

The SFI OEM application firmware could then be uploaded (on an OEM server for example) with all the inputs needed for license generation by the CM.

The OEM provides tools to the CM to get the appropriate licenses for the SFI application concerned and the integrated SMI module(s).

9.2 Hardware and software environment

The same environment as explained in Section 5.2: Hardware and software environment.

9.3 Step-by-step execution

1. Build the OEM application
   OEM application developers may use any IDE to build their firmware as well as using SMI modules provided by STMicroelectronics or 3rd parties for example.
   In this example we use firmware based on a single library (just one SMI module is integrated in the SFI image).
2. Perform the SFI generation.
   For encryption with the STM32TrustedPackageCreator tool, OEM firmware and the clear data part are both provided in Hex format (corresponding to the SMI module to be integrated within the SFI image). A CSV file to set the option bytes configuration is also necessary. The SMI module used is also provided as an input to the tool, in addition to a 128-bit AES encryption key and a 96-bit nonce. All inputs needed are available in the “SFI_ImagePreparation/Combined” directory. A “.sfi” image is then generated (out_comb.sfi).
Figure 70 shows the STPC GUI during combined SFI generation.

Figure 70. GUI of STPC during combined SFI-SMI generation

3. Programming input conditions are the same as for the SFI programming scenario (Section 5.3.4: Performing HSM programming for license generation using STPC (CLI mode)).

4. Perform the SFI install using the SWD/JTAG or bootloader interface (here the SWD interface is used).
9.3.1 Using JTAG/SWD

Once all input conditions are respected, go to the "stm32_programmer_package_v0.4.1/bin" directory and launch the following command:

```bash
STM32_Programmer_CLI.exe -c port=swd mode=HOTPLUG -sfi protocol=static "<local_path>/out_comb.sfi" "<local_path>/licenseSFI.bin"
```

Once all input conditions are respected, go to the "<STM32CubeProgrammer_package_path>/bin" directory and launch the following command:

```bash
STM32_Programmer_CLI.exe -c port=swd mode=HOTPLUG -sfi protocol=static "<local_path>/out_comb.sfi" "<local_path>/licenseSFI.bin"
```

*Figure 71: Combined SFI-SMI programming success using debug connection* shows the combined SFI-SMI install trace success.
AN5054 Example combined SFI-SMI programming scenario

Figure 71. Combined SFI-SMI programming success using debug connection

```
ST-LINK Firmware version: V2J26M15
SWD frequency: 4000 KHz
Connection mode: Hot Plug
Device ID: 0x450

Protocol: static
SFI File: RefSFI_MDK/SFI_Combined/ouy_comb.sfi

Starting SFI install operation for file: RefSFI_MDK/SFI_Combined/ouy_comb.sfi

SFI File Information:
- SPI file path: RefSFI_MDK/SFI_Combined/ouy_comb.sfi
- SPI license file path: RefSFI_MDK/SFI_Combined/licenseSFI_comb_h573bEH.bin

SFI header information:
- SPI protocol version: 1
- SPI total number of areas: 4
- SPI image version: 23

Parsing Area 1/4:
- Area type: F
- Area size: 12280
- Area destination address: 0x00000000

Parsing Area 2/4:
- Area type: F
- Area size: 116
- Area destination address: 0x08040000

Parsing Area 3/4:
- Area type: M
- Area size: 1688
- Area destination address: 0x08080000

Parsing Area 4/4:
- Area type: C
- Area size: 36
- Area destination address: 0x0

Setting write mode to SFI
Success to set write mode for SFI
Writing license to address 0x24007800
Writing line header to address 0x24003800
Writing areas and areas wrapper...
R8S process started...

R8S command execution OK
Reconnecting...
ST-LINK Firmware version: V2J26M15
SWD frequency: 4000 KHz
Connection mode: Hot Plug
Device ID: 0x450
Reconnected!
Requesting security state...
SECURITY State Success
SFI SUCCESS!
SFI file RefSFI_MDK/SFI_Combined/ouy_comb.sfi Install Operation Success

Time elapsed during the SFI install operation is: 00:00:04:056
Press <RETURN> to close this window...
```
9.3.2 How to test the combined SFI install success

The option bytes configuration must be modified as shown in Figure 72: Option bytes after combined SFI-SMI install success.

- 3rd party library module is programed into a PCROP area
- The SFI image is protected using RDP level1.

If a UART connection is available on the board used, open the “Hercule.exe” serial terminal available under the “Tests” directory, open the connection and on reset the dedicated “printf” packets appears.
Figure 72. Option bytes after combined SFI-SMI install success

<table>
<thead>
<tr>
<th>Option Bytes Bank: 0</th>
</tr>
</thead>
<tbody>
<tr>
<td>Read Out Protection:</td>
</tr>
<tr>
<td>ROP: 0x0 (Level 1, read protection of memories)</td>
</tr>
<tr>
<td>RPS:</td>
</tr>
<tr>
<td>RPS1: 0x0 (No SFI process on going)</td>
</tr>
<tr>
<td>BOR Level:</td>
</tr>
<tr>
<td>BORLEV: 0x2 (reset level is set to 2.7 V)</td>
</tr>
<tr>
<td>User Configuration:</td>
</tr>
<tr>
<td>LVLG1: 0x1 (Independent watchdog is controlled by hardware)</td>
</tr>
<tr>
<td>LVLG2: 0x1 (Window watchdog is controlled by hardware)</td>
</tr>
<tr>
<td>NRS1_STOP_D1: 0x1 (STOP node on Domain 1 is entering without reset)</td>
</tr>
<tr>
<td>NRS2_STOP_D1: 0x1 (STANDBY node on Domain 1 is entering without reset)</td>
</tr>
<tr>
<td>FZ_LVLG_STOP: 0x1 (Independent watchdog is running in STOP node)</td>
</tr>
<tr>
<td>FZ_LVLG_STBY: 0x1 (Independent watchdog is running in STANDBY node)</td>
</tr>
<tr>
<td>SECURITY: 0x1 (Security feature enabled)</td>
</tr>
<tr>
<td>BCMP: 0x1 (CM-7 boot enabled)</td>
</tr>
<tr>
<td>NRS1_STOP_D2: 0x1 (STOP node on Domain 2 is entering without reset)</td>
</tr>
<tr>
<td>NRS2_STOP_D2: 0x1 (STANDBY node on Domain 2 is entering without reset)</td>
</tr>
<tr>
<td>SWAP_BANK: 0x0 (after boot loading, no swap for user sectors)</td>
</tr>
<tr>
<td>DMEPA: 0x1 (delete PchOP protection and erase protected area)</td>
</tr>
<tr>
<td>DMESE: 0x1 (delete Secure protection and erase protected area)</td>
</tr>
<tr>
<td>Boot address Option Bytes:</td>
</tr>
<tr>
<td>BOOT_CM7_ADD0: 0x000 (0x00000000)</td>
</tr>
<tr>
<td>BOOT_CM7_ADD1: 0x1FF1 (0x1FF10000)</td>
</tr>
</tbody>
</table>

PCROP Protection:

| PCROPaStr: 0x000 (0x00100000) |
| PCROPaEnd: 0x806 (0x00806000) |

Secure Protection:

| SECaStr: 0x0FF (0x001FFFFF) |
| SECaEnd: 0x000 (0x00000000) |

DTCM HAM Protection:

| ST_HAM_SIZE: 0x3 (16 KB) |

Write Protection:

| nURP0: 0x1 (Write protection not active on this sector) |
| nURP1: 0x1 (Write protection not active on this sector) |
| nURP2: 0x1 (Write protection not active on this sector) |
| nURP3: 0x1 (Write protection not active on this sector) |
| nURP4: 0x1 (Write protection not active on this sector) |
| nURP5: 0x1 (Write protection not active on this sector) |
| nURP6: 0x1 (Write protection not active on this sector) |
| nURP7: 0x1 (Write protection not active on this sector) |
10 Reference documents

Table 2. Document references

<table>
<thead>
<tr>
<th>Reference</th>
<th>Version</th>
<th>Document title</th>
</tr>
</thead>
</table>
11 Revision history

Table 3. Document revision history

<table>
<thead>
<tr>
<th>Date</th>
<th>Revision</th>
<th>Changes</th>
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<tbody>
<tr>
<td>03-Aug-2018</td>
<td>1</td>
<td>Initial release.</td>
</tr>
<tr>
<td>18-Apr-2019</td>
<td>2</td>
<td>Updated publication scope from ‘ST restricted’ to ‘Public’.</td>
</tr>
<tr>
<td>16-Oct-2019</td>
<td>3</td>
<td>Updated:</td>
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<tr>
<td></td>
<td></td>
<td>– Section 4.1.2: License mechanism</td>
</tr>
<tr>
<td></td>
<td></td>
<td>– Section 5.3.3: Performing HSM programming for license generation using STPC (GUI mode)</td>
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<tr>
<td></td>
<td></td>
<td>– Figure 38: HSM programming GUI in the STPC tool (title caption)</td>
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<tr>
<td></td>
<td></td>
<td>– Figure 50: Example of HSM programming using STPC GUI</td>
</tr>
<tr>
<td>03-Feb-2020</td>
<td>4</td>
<td>Replaced occurrences of STM32L451CE with STM32L462CE in Section 4.2.1: Secure firmware installation using Bootloader interface flow.</td>
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<tr>
<td></td>
<td></td>
<td>Updated document to cover secure programming with SFIx.</td>
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