Silicon identification

This errata sheet applies to the STMicroelectronics S2-LP device; see the below information for details.

<table>
<thead>
<tr>
<th>Order code</th>
<th>Package</th>
<th>Device info (stored in 0xF0 and 0xF1 registers)</th>
<th>Comment</th>
</tr>
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<tbody>
<tr>
<td>S2-LPQTR</td>
<td>QFN24 4x4x1</td>
<td>0x0391</td>
<td>Cut 2.1</td>
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1 SDO pin not tri-stated

1.1 Part number affected
S2-LPQTR cut 2.1.

1.2 Description
The SDO pin is always forced to output with an internal low impedance and the pin is not tri-stated when the CSn pin is high. This prevents the external microcontroller to use the same SPI bus for driving other SPI slaves.

1.3 Workaround
Two actions can be taken:

- do not use multiple slaves on the SPI bus where the S2-LP is attached;
- each time an application needs to access another SPI slave, turn the S2-LP off via the SDN pin.
# Revision history

## Table 2: Document revision history

<table>
<thead>
<tr>
<th>Date</th>
<th>Revision</th>
<th>Changes</th>
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<tbody>
<tr>
<td>09-Nov-2016</td>
<td>1</td>
<td>Initial release</td>
</tr>
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