Board identification

This errata sheet applies to some samples of the STMicroelectronics X-NUCLEO-IDW01M1, see the below information for details.

<table>
<thead>
<tr>
<th>Order code</th>
<th>Device Info</th>
</tr>
</thead>
<tbody>
<tr>
<td>X-NUCLEO-IDW01M1</td>
<td>Presence of R21</td>
</tr>
</tbody>
</table>

Figure 1: Board identification (R21)
1 Limitation

1.1 STM32 Nucleo programming

1.1.1 Part number affected
Preliminary sample of X-NUCLEO-IDW01M1.

1.1.2 Description
These boards mount R21 (see Figure 1) which may interfere with JTAG programming of STM32 on STM32Nucleo board.

1.1.3 Workaround
Disassemble R21.
## Revision history

Table 2: Document revision history

<table>
<thead>
<tr>
<th>Date</th>
<th>Revision</th>
<th>Changes</th>
</tr>
</thead>
<tbody>
<tr>
<td>18-Jul-2016</td>
<td>1</td>
<td>Initial release</td>
</tr>
</tbody>
</table>
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