

Introduction

This release note contains all the relevant information about the latest version of SPC5STUDIO tool available on ST web site and the steps to follow to upgrade a previous version.

Table 1. Device summary

Order code	Reference
SPC5STUDIO	SPC5-STUDIO

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1 Delivery information

1.1 Delivery name

SPC5STUDIO v_5.8.0

1.2 Overview of the release

- Integrated development environment based on Eclipse
- Visual applications:
 - ClockTree,
 - PinMap wizard
 - MCU selection
 - Power consumption tool
- Additional software packages:
 - FreeRTOS
 - TCP/IP
 - Bootloader
 - Flash and EEPROM drivers
- Platform Header files
- Multiple toolchain support
- Debugger integration
- Code generation and code analysis
- Driver API documentation
- Full MISRA 2012 compliant

Figure 1. Overall release contents

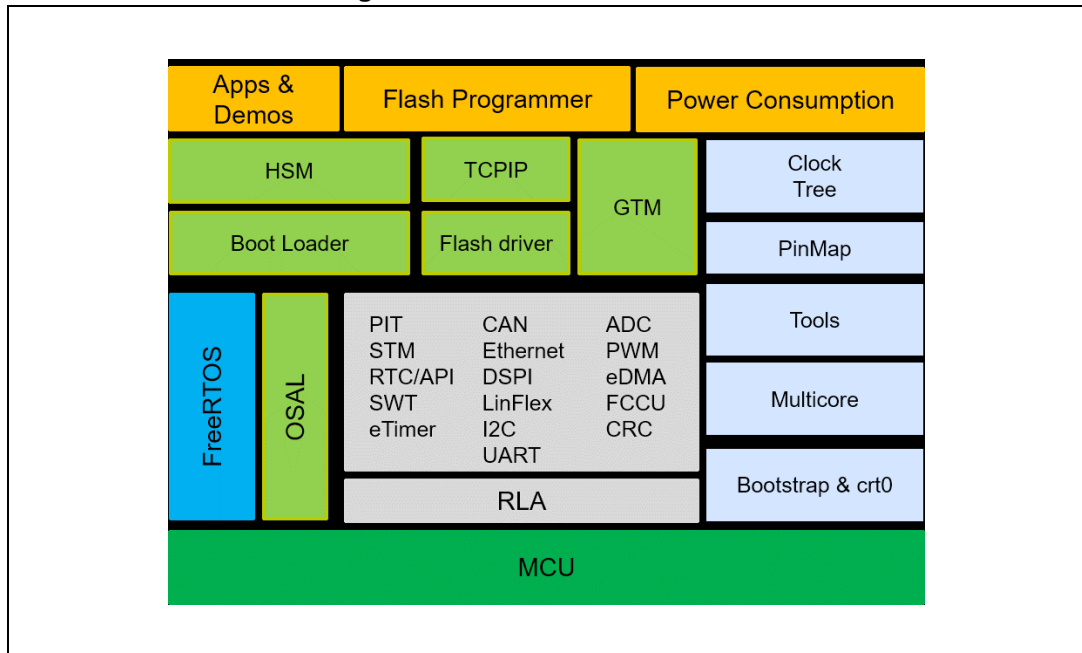


Figure 2. SPC56 Family supported drivers

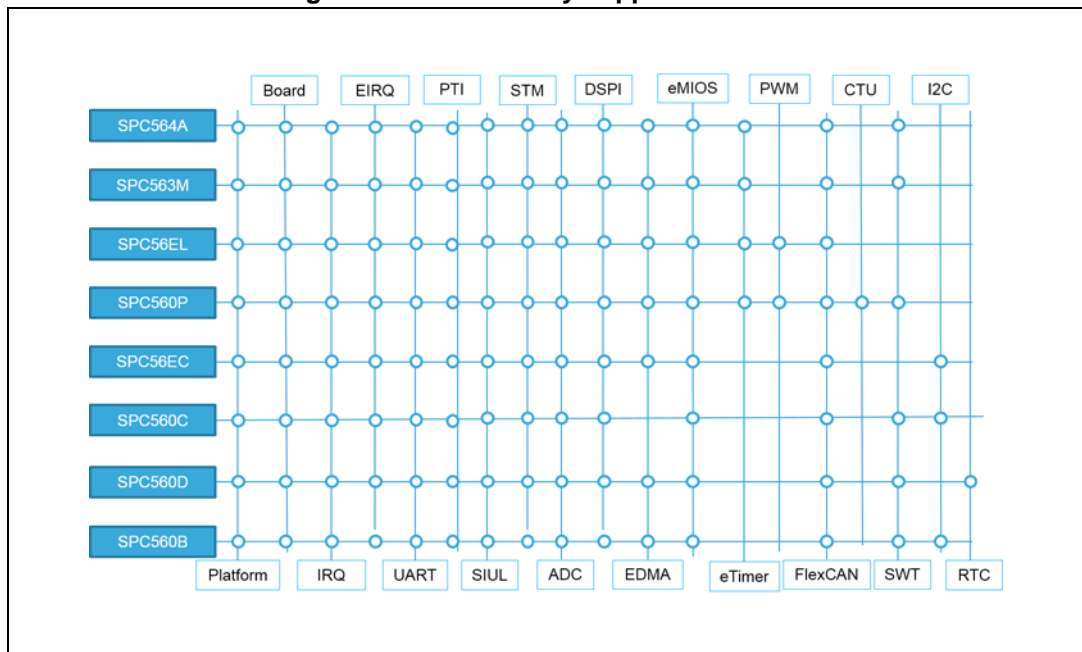
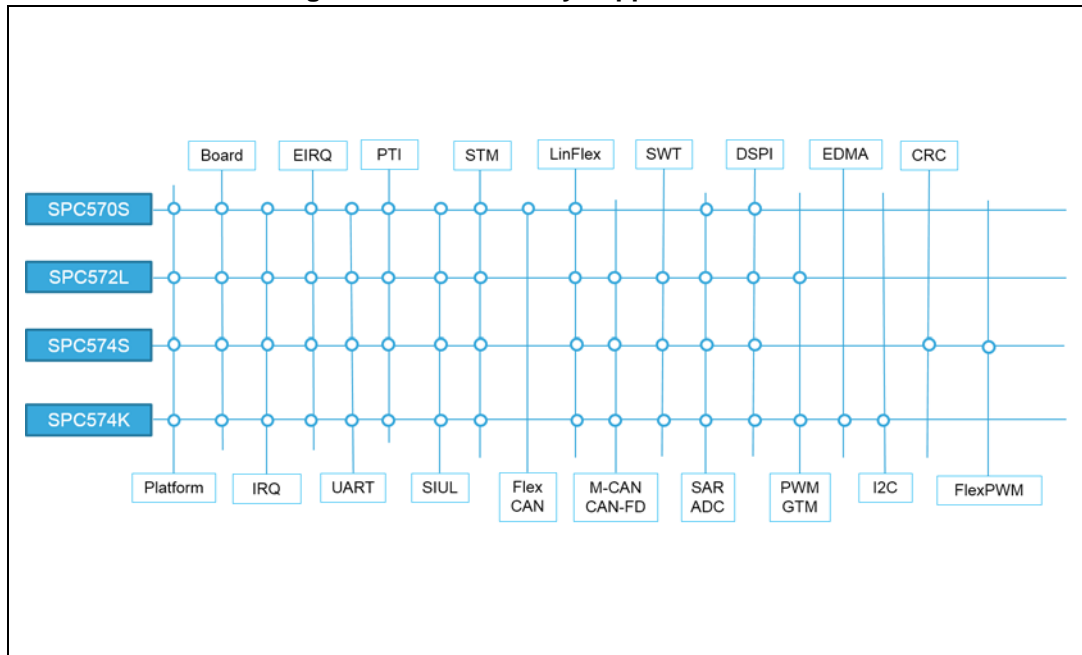
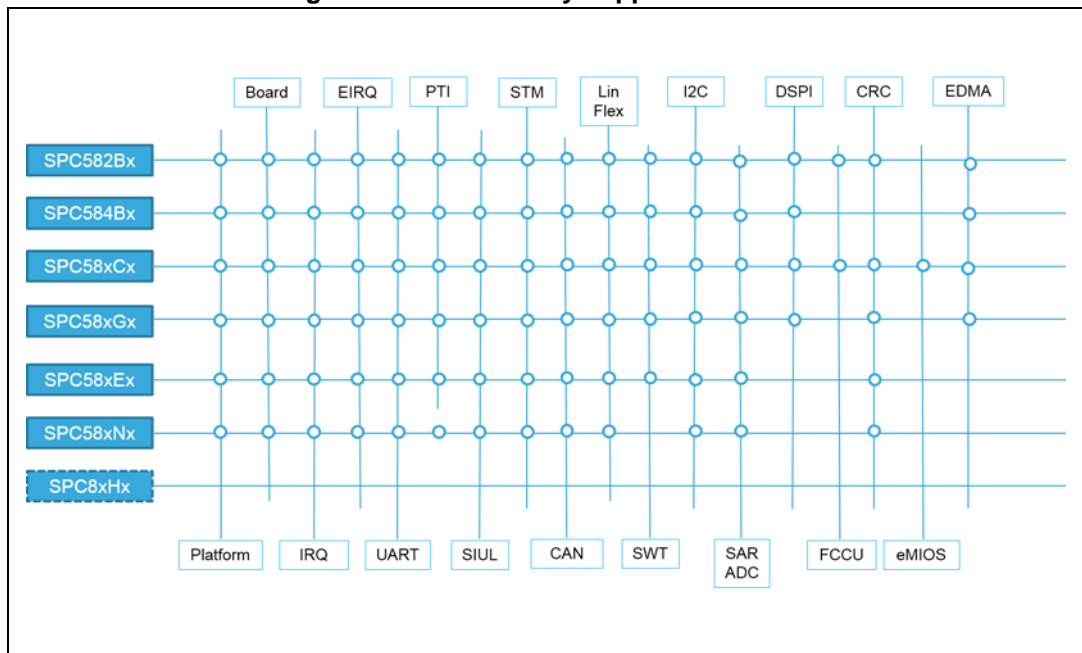


Figure 3. SPC57 Family supported drivers



1.3 SPC58 Family supported drivers

Figure 4. SPC58 Family supported drivers



1.4 GTM

Figure 5. GTM block diagram

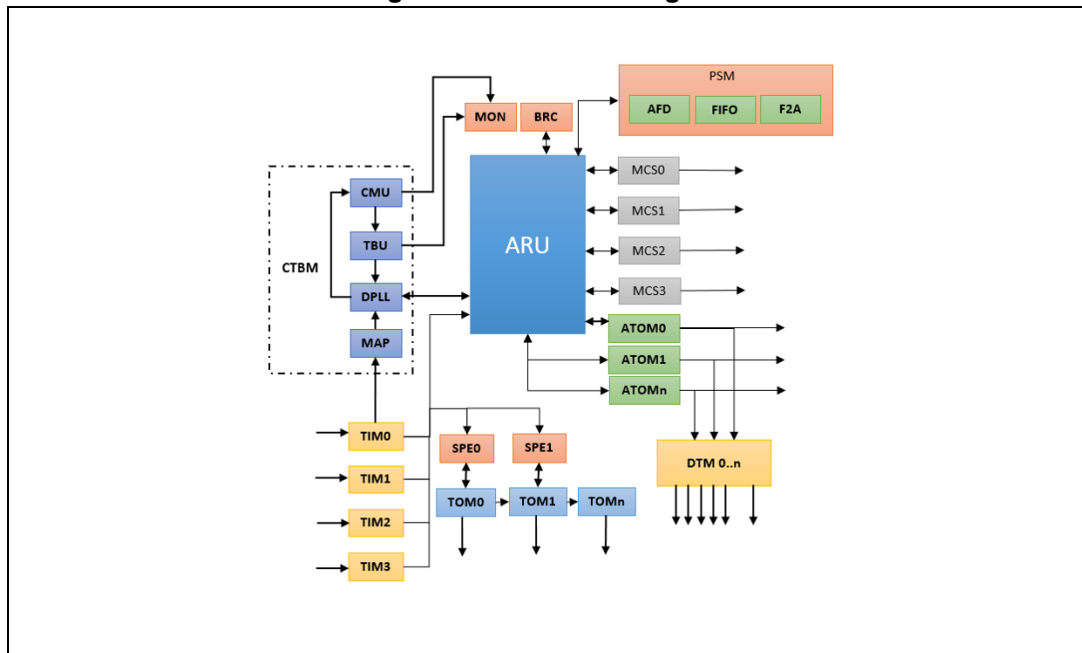
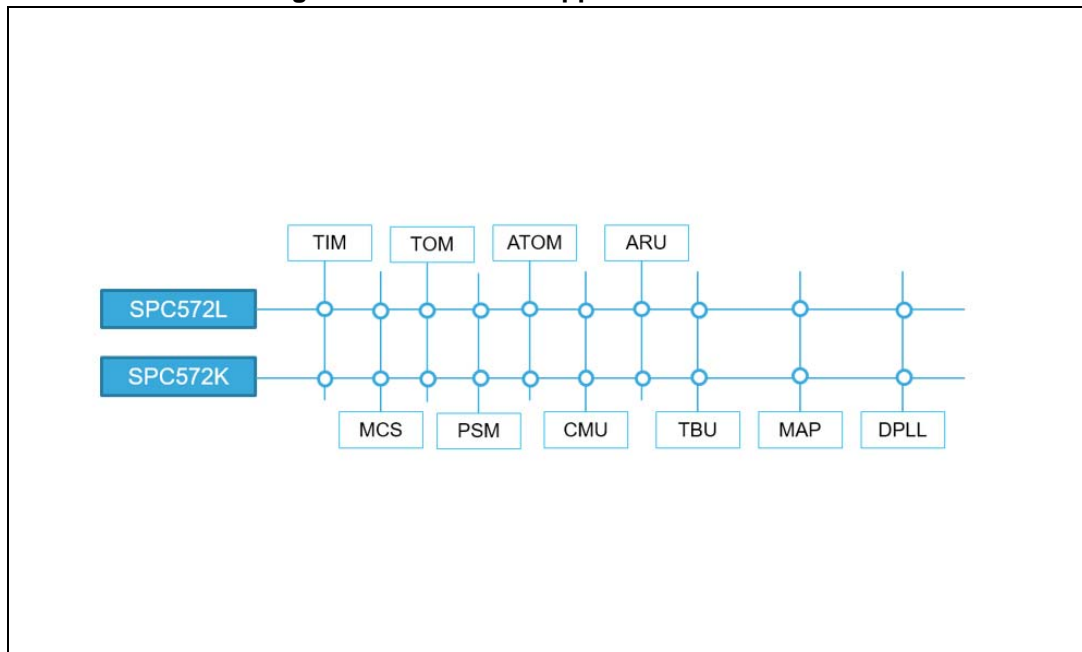


Figure 6. GTM block supported submodules



1.5 Changes in version 5.8.0

This release, classified as major, contains big changes in the user interface and functionalities as well as enhancements and fixes of issues.

What's new :

- Integration of free gcc 4.9.4
- New hightec compiler integration
- SPC56EC-SPC564B: Fast ethernet controler, Ping and SWT demos
- SPC570Sx: DSPI, CANFD
- SPC574Kx: DSPI, Saradc
- SPC574Kx: CANFD, I2C demo, cut2.4 support
- Code generation for all supported SPC58 platforms
- New SIUL2 MGT for SPC582Bx, SPC584BX, SPC584Cx and SPC58ECx, SPC584Gx, SPC58NGx, SPC58EGx1 and SPC58EHx, SPC58NHx SPC58xNx
- SPC582Bx: McanV2, new DMA driver
- SPC584Bx: McanV2, new DMA driver, I2C and EDMA demos
- SPC58xCx: New DMA driver, EDMA demo, improve clock performance
- SPC58xGx: McanV2, new DMA driver, EDMA demo
- SPC58xHx: new DMA driver
- SPC58xEx: CRC, I2C, new DMA driver
- SPC58xNx: McanV2, new DMA driver

Table 2. Fixed Issues

Ticket Nr	Summary
464140	Port CRC Driver to Eiger and create test application
464144	Add I2C Driver support to Eiger.
469366	CRC Driver for CH1M
473343	Add in Sphaero the support for LinFlex 2, 3 (CUT2.0)
473653	Add Benchmarks Component
475957	Add Benchmark Test Application
477278	Added Chorus 2M Benchmarks.
478871	PIT Configuration in Chorus and Bolero lines
475596	GTM CMU GetClock API
481234	K2 GTM tom0 driver issue
484427	Regenerate RLA components doxygen documentation
448011	Update freegcc in 4.9.4
458201	Wrong FreeGCC Version in Compiler Variant Field
459939	Linker file to update to support freegcc 4.9.4
474473	Create IO runtime component.
478108	Add cfg file for SPC560B40 (256KB flash)
478603	Update Documentation SPC5STUDIO creation Component
460377	Create IO runtime component.
467792	Sphaero: add SPI driver.

Table 2. Fixed Issues (continued)

Ticket Nr	Summary
475328	Add DSPI Test Application for SPC584B70E5
475806	Add DSPI Test Application for Sphaero
475848	Update test application configuration for Sphaero.
476149	SPC584Bx: add I2C demo
475848	Update test application configuration for Sphaero.
476149	SPC584Bx: add I2C demo
476238	CH2M: Missing SPC5_I2C_IRQ_PRIORITY define.
476503	K2: add I2C demo
476516	CH1M: Missing SPC5_I2C_IRQ_PRIORITY macro.
476527	I2C: In interrupt mode test application loop for ever.
478428	Create HSM component
479302	Add CAN support to Runtime IO Component
479948	Use Runtime IO component in old version of SPC5STUDIO
477236	Chinese characters in Pictus pinmap wizard
478973	SPC58EHx, SPC58NHx pinmap: missing functions
480833	Add Peripheral SET 0 clock point in the Leopard clocktree
482339	Update SPC582Bx clocktree
483300	PinMap Wizard resets configuration to default values if changed by using Board component.
483301	Missing I/O configuration for I2C SCL Pin
484029	Pin Map report chinese caracters and incorrect label
475675	I2C driver: wrong device address upper limit
475676	SPC582Bx: add I2C demo
476123	GTM: MCS files do not compile if parallel build is not enabled
481168	GTM driver: add private field for user application data
481172	GTM: Missing rules to compile MCS code when GreenHills toolchain is selected
482154	SPC584Cx, SPC58ECx Discovery: enable interrupts to I2C driver
481168	GTM driver: add private field for user application data
481172	GTM: Missing rules to compile MCS code when GreenHills toolchain is selected
482154	SPC584Cx, SPC58ECx Discovery: enable interrupts to I2C driver
483295	EIRQ: in case of multiple external interrupts only the last is served
484227	SPC58EC Discovery Plus: add ADC demo
465455	Fast Ethernet Controller (FEC) driver available in SPC5STUDIO
469367	FCCU Driver for SPC582Bx
469372	Check for ratio between PBRIDGE and LIN clock

Table 2. Fixed Issues (continued)

Ticket Nr	Summary
471672	Update some Sphaero minimum/maximum clock values
472852	SWT_3 missing in Chorus2M
473341	Add support for OPWMT in the Chorus4M eMIOS driver
473342	Add SWT driver to Bolero 3M
475297	Wrong debug.wsx in Leopard Flash test app
475366	Add DSPI driver to Velvety platform
475428	Create DSPI test app for Velvety Discovery
475478	Wrong Shadow Flash base address in Leopard Flash driver
475610	Some SWT don't work properly
475712	Port new SIUL2 on Chorus 4M
475713	Port new SIUL2 on Chorus 6M
475714	Port new SIUL2 on Bernina
476208	Issue with the EIRQ pins
476474	SPC584Bx Network test app doesn't work
476475	PWM stop function doesn't work properly
476494	Update readme.txt of Chorus 4M Network test application
476864	Create FCCU test application for CH1M Discovery
476872	Add FCCU reset in spc_clock_init
476945	Create SWT test app for Bolero 3M
477423	Define DSPI slave configuration
477940	ALT4 selection missing in the Board Component of Bolero3M
477942	Create Bolero3M Ping test application
478104	K2 Network test app doesn't work properly
478423	eDMA support for SPC584Bx
478473	Improve Chorus 4M clock performance
478534	eDMA support for SPC584Cx, SPC58ECx
478535	eDMA support for SPC584GX, SPC58NGx and SPC58EGx
478776	Create new DMA driver for 58 family
479326	Add DMA serial test app for Chorus2M and update all other test apps
479680	Add DMA serial test app for Chorus6M and update all other test apps
479819	Enable the DMA support for the DSPI driver on Chorus 4M.
480539	Add DMA serial test app for Chorus4M and update all other test apps
480551	Wrong delay when SYS_CLK_DIV is greater than 1
480825	Improve osalEnterCritical/osalExitCritical functions
482078	Fix misra check error on eDMA v2 driver

Table 2. Fixed Issues (continued)

Ticket Nr	Summary
483140	Remove PLATFORM_NAME from clock.h
484465	Fix Velvety DSPI test app after the introduction of the new slave mode interface
484569	Wrong readme file in CRC test app for Eiger
449999	Add in configuration.xml a field to identify the SPC5STUDIOversion
468534	Remove compiling restriction based on the selected core
470902	New siul2 management for all platforms
477895	Monaco and Andorra visual clock trees generate errors
478432	Demos should have parallel build enabled
472154	Add SUB_1 CAN_1 support for Sphaero
475290	Port SARADC Driver to Velvety and create test application
475370	add CANFD support on Sphaero
475426	Wrong IS clearing in SPCSetRunMode function for Pictus Large
475626	Wrong IS clearing in SPCSetRunMode function for Pictus Large
475715	CH4M MCAN Driver enhancement
476066	MCAN driver not compiling K2, LAV
476602	Add K2 cut2.4 support in platform component
476961	MCAN clock gating must be performed before SHARED RAM clock gating
477944	port MCAN V2 to Bernina
478180	MCAN Driver for K2 cut 2.4 and related test application
478421	CAN FD: manage up to 64 data bytes
478471	Add support for CANFD in K2 (MCANv2 driver)
478561	port MCAN v2 driver to CH2M
478595	Port MCANv2 driver to CH1M
478747	port MCAN v2 driver to CH6M
480372	CAN test app not compiling
480817	Create Firmware Updater component for Lavaredo
481703	SARDC test applications not working
482005	Wrong Prescaler value in STM test applications for Lavaredo
482008	FLEXCAN CR register bit CLKSRC must be set to 0 when external clock is disabled
482983	Copy in RAM not working when .data section is > 0

1.6 Recommendations

- In order to update to the 5.8.0 release we recommend to start form a blank installation using the 5.8.0 installation package download from SPC5STUDIO page (www.spc5studio.com) and upgrade it through the tool itself.
- In case you are using an old version of SPC5STUDIO, uninstall it, paying attention to save workspace and license folder from your SPC5STUDIO home directory before.
- In order to install the tool, follow the steps below:
 - Download SPC5STUDIO v.0 from www.spc5studio.com.
 - Read careful README-FIRST.txt file inside the package and follow the installation instructions.
 - Once the new package installation is done revert the folders into the new SPC5STUDIO home folder.
 - Open SPC5STUDIO and configure the network connections in the tool preferences (Windows menu).
 - Install the desired component through the Market Place accessible from the Help Menu.
 - Keep the tool up to date. Make sure you have the following link: <http://spc5.drive.cloudforge.com/projects/spc5releases/spc5studio/updates5> in the “Available Software Site” list.

In case you are using an holder SPC5STUDIO, remove the current installation paying attention to save workspace and license folder from your SPC5STUDIO home directory before. Once the new package installation is done (by following the steps above) revert the folders into the new SPC5STUDIO home folder.

1.7 Release path

1.8 R

Current release is available from the ST web site at the SPC5STUDIO dedicated page (www.spc5studio.com).

Updates will be delivered through the following link:
<http://spc5.drive.cloudforge.com/projects/spc5releases/spc5studio/updates5>.

Make sure you have this link in the “Available Software Site” list.

1.9 Nature of Release

Table 3. Nature of Release

Destination/Type		Description
Internal		Only SW Team and beta user have access
External	X	Shareable externally.
Patch		Includes hot fix or customization for specific customer, delivered through specific update site link

Table 3. Nature of Release

Destination/Type		Description
Major	X	Includes a big list of items, new key features that radically changes the shape and the usage of the tool
Minor		Includes New functionalities and bug fix

1.10 Delivered documents listing

Table 4. Delivered documents listing

File Name	Version	Location
Release Note	4	ST web site
How to Import and export projects in SPC5STUDIO.pdf	1.0	In Zip file containing the installation package
README-FIRST.txt	1.0	In Zip file containing the installation package
SPC5STUDIO_installation_procedure.pdf	3.5_P1	In Zip file containing the installation package
PLS_Debugger_Installation_driver_on_SPC560X_Discovery_board.pdf	3	In Zip file containing the Installation package

1.11 Customer support

In case of any issue on this release please refer to On Line Support on www.st.com web site or visit the SPC5STUDIO ST Communities reachable from Support tab from www.st.com home web page. For more information or help contact the ST nearest sales office. For a complete list of ST offices and distributors, refer to the www.st.com webpage.

1.12 Known Issues

Market place icons may disappear using a proxy native setting. This is a problem with NEON eclipse. The workaround is to set manual in the "Active Provider" and clear SOCKS "Proxy Entries" in the Network Connections Preferences of SPC5STUDIO.

1.13 Potential effects of bug fixes on Functionalities

N/A since non-regression tests are successful run.

2 Host PC system requirements

2.1 Supported operating systems and architectures

- Windows® XP: 32-bit (x86)
- Windows® 7: 32-bit (x86), 64-bit (x64)
- Windows® 8: 32-bit (x86), 64-bit (x64)
- Windows® 10: 32-bit (x86), 64-bit (x64)

2.2 Software requirements

More information on installation requirements and procedure can be found in the installation procedure document inside the zip file containing the installation package and downloadable from www.spc5studio.com web page.

3 **References**

All reference and documentation are available at www.spc5studio.com web site. For more information or help concerning SPC5STUDIO.

4 Release information for previous releases

4.1 Summary of changes in version 5.7.2

- SPC57xKx ethernet support
- SPC574Sx FlexPWM SARADC SPI
- Dual core demo for SPC56Px family
- SPC584Bx pinmap editor
- CANFD support for all SPC58 MCUs
- SPC584Cx, SPC58ECx: Emios driver
- SPC582Bx: DMA in SARADC
- Improved SIUL2 management for pin settings (editor and configurator)
- SPC58xEx support: starter kit and additional drivers:
 - MCAN, SARADC, FLASH
- GTM support(ARU, MCS, PSM, MAP)

4.2 Summary of changes in version 5.7.1

- Networks bugs fixing

4.3 Summary of changes in version 5.7.0

- Windriver compiler support(see application note)
- minor bugs fixing
- SPC584Bx stsrter kit and additional drivers:
 - CAN, Serial, PIT, STM, SPI, SWT FLASH
- SPC58xEx starter kit
- Power Consumption wizard
 - Low power mode

4.4 Summary of changes in version 5.6.0

- Integrated development Environment
 - C/C Development Tooling 9.2.1
 - Activity wizard refactoring
 - PLS eclipse plugin integration
- Wizard
 - SPC570Sx MCU wizard
 - SPC574Kx RLA clocktree
- Drivers
 - SWT for SPC56xPX
 - PWM driver
 - I2C for SPC57
 - SPC58xNx starter kit (no wizard)
 - SARADC: SPC57xKx, SPC58xNx, SPC57xLx
 - MCAN: SPC57xKx, SPC57xLx, SPC574Sx
 - EFI board support
- minor bugs fixing
- Wizards
 - Pinmap wizard SPC58EHx, SPC58NHx BGA386
 - New SIUL 2 MGT: C5PC582Bx and SPC58EHx, SPC58NHx
- Drivers
 - I2C for 58
 - SARADC for SPC584Cx, SPC58ECx and SPC584Gx, SPC58NGx, SPC58EGx
 - FLASH for SPC584Cx, SPC58ECx and SPC584GX, SPC58NGx, SPC58EGx
 - MCAN: SPC582Bx and SPC584Gx, SPC58NGx, SPC58EGx
 - GTM: TOM, TIM
 - Safety for SPC584Cx, SPC58ECx: FCCU and CRC
 - SPC584Cx, SPC58ECx 176 pins code generation support

4.5 Summary of changes in version 5.5.0

- SPC574S starter kit:
 - Platform component RLA
 - Board init component RLA
 - Basic serial RLA driver
 - Clock component RLA
 - IRQ component RLA
 - OSAL component RLA
 - PinMap Wizard
 - STM, Serial, PIT and EIRQ demos
- Flash driver for SPC57 family
- GTM foundation level component for SPC57xKx and SPC57xLx
- Minor bugs fixing
- SPC58xGx starter kit:
 - Platform component RLA
 - Board init component RLA
 - LLD component RLA
 - Clock component RLA
 - IRQ component RLA
 - OSAL component RLA
 - PinMAP wizard
- For SPC584Cx, SPC58ECx (SPC58EC) and SPC584Gx, SPC58NGx, SPC59EGx(SPC58G)
 - Ethernet, CAN and Slave Mode DSPI
 - New serial driver
 - eDMA
 - Flash driver
- Power Consumption wizard:
 - New SPC58xGx support

4.6 Summary of changes in version 5.4.0

What's new:

- Presets management fixes
- Add one shot preset for package component
- SPC572L starter kit
 - Platform component RLA
 - Board init component RLA
 - Clock component RLA
 - IRQ component RLA
 - PinMap wizard
 - Demos for SPC57xLx
- Minor bugs fixing

4.7 Summary of changes in version 5.3.1

What's new:

- Update the max values of SPC57xKx clockpoints
- Update SPC570Sx Header file
- SPC560p.h header file issue: MDIS bit missing in the definition of MCR register in PIT tag
- Write one to clear (w1c) register issue
- Pinmap for BGA: allow flip of image to see device in order to have A1 ball in top left corner

4.8 Summary of changes in version 5.3.0

What's new:

- Added FreeRTOS support for SPC56xB, SPC57xKx, SPC570Sx
- New platform for SPC56BxL3-ADIS board
- I2C support for SPC56xBx
- Add support for second CAN in SPC56Px
- SPC57xKx starter kit
- Platform component RLA
- Board init component RLA
- Basic serial RLA driver
- Clock component RLA
- IRQ Component RLA
- PinMap wizard
- DSPI driver and demos for SPC57xKx
- Dual core support for SPC57xKx
- Core selection for interrupt dispatching(SPC57xKx)
- SPC570Sx starter kit
- CAN driver support SPC570Sx

4.9 Summary of changes in version 5.2.3

What's new:

- RLA SPC570xx clocktree update.

4.10 Summary of changes in version 5.2.2

What's new:

- Updated site split between Mass Market version and Customer version

4.11 Summary of changes in version 5.2.1

What's new:

- FlexCANB and FlexCANC interrupt vector number definition issue of spc564a_registery.

4.12 Summary of changes in version 5.1

What's new:

- Added support for SPC58ECxx including PinMap wizard.
- (note that debugging for this MCU requires PLS debugger version 4.7.04 and beyond to be downloaded from PLS web site at this URL <http://www.pls-mc.com/spc5-udestk-udestk>).
- Added PLS debugger 4.8 eclipse integrated mode support.
- Minor bugs fixing.
- Updated of Gcc compiler tool chain to latest version.

4.13 Summary of changes in version 5.0

What's new:

- Gcc compiler tool chain with VLE support for SPC5x MCUs included in installation package.
- Added experimental support for SPC574Kxx including MCU and PinMap wizards.
- Added full support for GHS tool chain. RLA platform component fully reworked: "Copy in RAM" and "load in RAM" options are now fully working regardless the compiler option.
- Seamless switch among compiler tool chains (free gcc 4.9, Hightec 4.6, GHS 2.6).
- Added EMIOS driver for SPC5x platforms, with associated demo applications. EMIOS driver for SPC560B50 has been reworked allowing full functionality.
- Added MISRA 2012 automatic check for customer code. Please refer to SPC5STUDIO associated help content section: Check MISRA 2012 compliance.
- Added EIRQ driver for SPC5x platforms, with associated demo applications.
- Added STM driver for SPC5x platforms, with associated demo applications.
- Minor bugs fixing.

4.14 Summary of changes in version 4.2.1

What's new:

- Fix properties view.

4.15 Summary of changes in version 4.2.0

What's new:

- Rework generate buttons behaviour
- Source code is re-indexed at every generation to update SPC5STUDIOsearch
- No more unresolved inclusions with CDT settings update.
- SPC56 family RLA PIT driver added.
- Enabled Driver Tab Restyling.
- The activity wizard buttons have been duplicated in SPC5STUDIO toolbar, allowing customer to free more window editing space.

4.16 Summary of changes in version 4.1.0

- SPC56 RLA MISRA
- SPC56 Clock Tree
- SPC56 GHS Support
- SPC56 MCU wizard
- SPC570Sx starter kit

4.17 Summary of changes in version 4.0.0

What's new:

- New user interface
- Luna eclipse platform
- Free Gcc compiler
- Register level access (RLA) for all SPC56x family
- Light installer

4.18 Summary of changes in version 3.7

What's new:

- **Market Place (v3.6):** Updated SPC5STUDIO full feature for Marketplace support (with the official http web address) which will be fully working in the next major release with the completely new installer.
- **Register Level Access (RLA):** Added support for SPC563Mxx RLA with sample applications and related documentation.
- **SPC56 L-Family:** Added FlexCAN2 support for SPC56EL70L5 MCU
- **EEPROM emulation:** Software for SPC56 P,A,L,B,D,M Lines, with sample applications and related documentation.
- **Pin Map Wizard (v3.6.1):** Added Saving and restore Pin map wizard settings

Fixed issues:

- Minor problem on the Pinmap Wizard for SPC56ECxx MCU;
- Minor issue on FlexCAN1 on SPC563Mxx MCU;
- Watchdog threshold support solved for ADC driver for SPC56P/L/B/D/C MCU
- eSCI driver for SPC56M/A MCU;
- Minor cosmetics changes to test app for NILS OS, CRC, e Timer2 for L/B/D/C MCU

4.19 Summary of changes in version 3.6

What's new:

- **Market Place:** Added Marketplace feature to provide the capability of adding/removing SPC5STUDIOfeatures.
- **Floating point:** operation compiler option support added
- **Pin Map Wizard:** Added Saving and restore Pin map wizard settings
- **Clock Tree:** improved clock tree usability in SPC56xPx

Fixed issues:

- ADC driver could not work in NON DMA mode
- Compilation error when checking "oscillator bypass" option
- Global prescaler fix on B Line

4.20 Summary of changes in version 3.5

What's new:

- **Register Level Access (RLA):** provided support for P Line
- **Application Wizard:** Added demo Applications for Discovery on SPC560Bxx MCU
- **Documentation:** Re-generated HAL Driver Component documentation

Fixed Issues:

- Fixed support for Windows 8
- Corrected configuration for max of PWM channels

5 Glossary and acronyms

Table 5. Glossary and acronyms

Term	Description
API	Application Programming Interface
CR	Change Request (Product Enhanced Request)
DOS	Document Objective Specification
ER	Error (Bug fixing Request)
eSCI	Enhanced Serial Communication Interface
HV	Hardware
IDE	Integrated Development Environment
MCU	Micro Controller Unit
OS	Operating System
RAM	Random Access Memory
RLA	Register Level Access
SPI	Serial Peripheral Interface
SW	Software
SWD	Software Driver
MISRA	Motor industry software reliability association

Revision history

Table 6. Document revision history

Date	Revision	Changes
11-Nov-2015	1	Initial release.
09-Dec-2015	2	Release for SPC5-STUDIO v.4.0 Release. Added Section 4.3: Summary of changes in version 3.5.
01-Aug-2016	3	Release for SPC5-STUDIO v.5.0 Release. Updated Section 1: Delivery information ; Section 2: Host PC system requirements ; Section 3: References ; Section 4: Release information for previous releases .
25-Jun-2018	4	Updated Section 1.2: Overview of the release , Section 2: Host PC system requirements , Section 4: Release information for previous releases , Section 5: Glossary and acronyms . Added Section 1.7: Release path Updated Section 4.1: Summary of changes in version 5.7.2 Added Figure 1: Overall release contents , Figure 2: SPC56 Family supported drivers , Figure 3: SPC57 Family supported drivers , Figure 4: SPC58 Family supported drivers , Figure 5: GTM block diagram , Figure 6: GTM block supported submodules . Modified Table 4: Delivered documents listing . Minor text changes.

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