STEVAL-IEG001V2  Schematic diagrams

Figure 1. µSD connections

Figure 2. IrDA transceiver

Figure 3. EEPROM section
Figure 4. LCD connector on VTS motherboard

Figure 5. Graphical LCD daughter card
Figure 6. GPS module interface

Figure 7. GSM

Image of a diagram showing the GPS module interface and another diagram showing the GSM connections.
Figure 8. GSM module interface

Figure 9. SIM card section
Figure 10. Compatibility between STM32F10x and STM32F20x

Figure 11. Switch section
Figure 18. Accelerometer section