



STMicroelectronics Industrial&Power Supply Application LAB		
Title		
<b>L 6207 EVALUATION BOARD</b>		
Size A4	Document Number 1	Rev 2.1
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L 6207 EVALUATION BOARD

1 Revision: 2.1

Bill Of Materials

Item	Quantity	Reference	Part
1	4	CN1,CN2,CN3,CN4	CON 2 pins
2	1	CN5	CON 34 pins
3	1	C1	Kemet Electronics 220nF/100V CER
4	1	C2	Siemens Matsushita 220nF/100V POLIEST
5	1	C3	Panasonic FA 100uF/63V
6	1	C4	Siemens Matsushita 10nF/100V CER
7	1	C5	Panasonic KG 10uF/16V
8	2	C6,C7	100nF
9	2	C9,C8	68nF ceramic
10	2	C11,C10	820pF
11	2	D1,D2	1N1448
12	1	D3	Zener BZX79C5V1
13	1	JP1	JUMPER 3x1
14	2	JP2,JP3	JUMPER
15	1	R1	100 5% 0.25W
16	1	R2	3.17k 0.6W
17	2	R3,R4	4.7k 1% 1/4W
18	2	R5,R16	20k 1% 1/4W
19	2	R6,R7	Spectrol74W 100k
20	2	R8,R17	2.2k
21	6	R9,R10,R11,R12,R13,R14	1OHM 0,4W
22	2	R18,R15	Spectrol74W 5k

Important Notes

JP1 : close in INT position for use with PractiSPIN ST7 board

C6, C7 : recommended change to 5.6nF for safe Overcurrent protection

R3, R4 : recommended change to 100k for safe Overcurrent protection

R15, R18 : set the maximum current obtainable through PractiSPIN (see PractiSPIN documentation)

R2 recommended change to adequate value (depending on supply voltage) to obtain 5V across D3

JP2, JP3 close for safe Overcurrent protection