

Figure 3. Interface side schematic

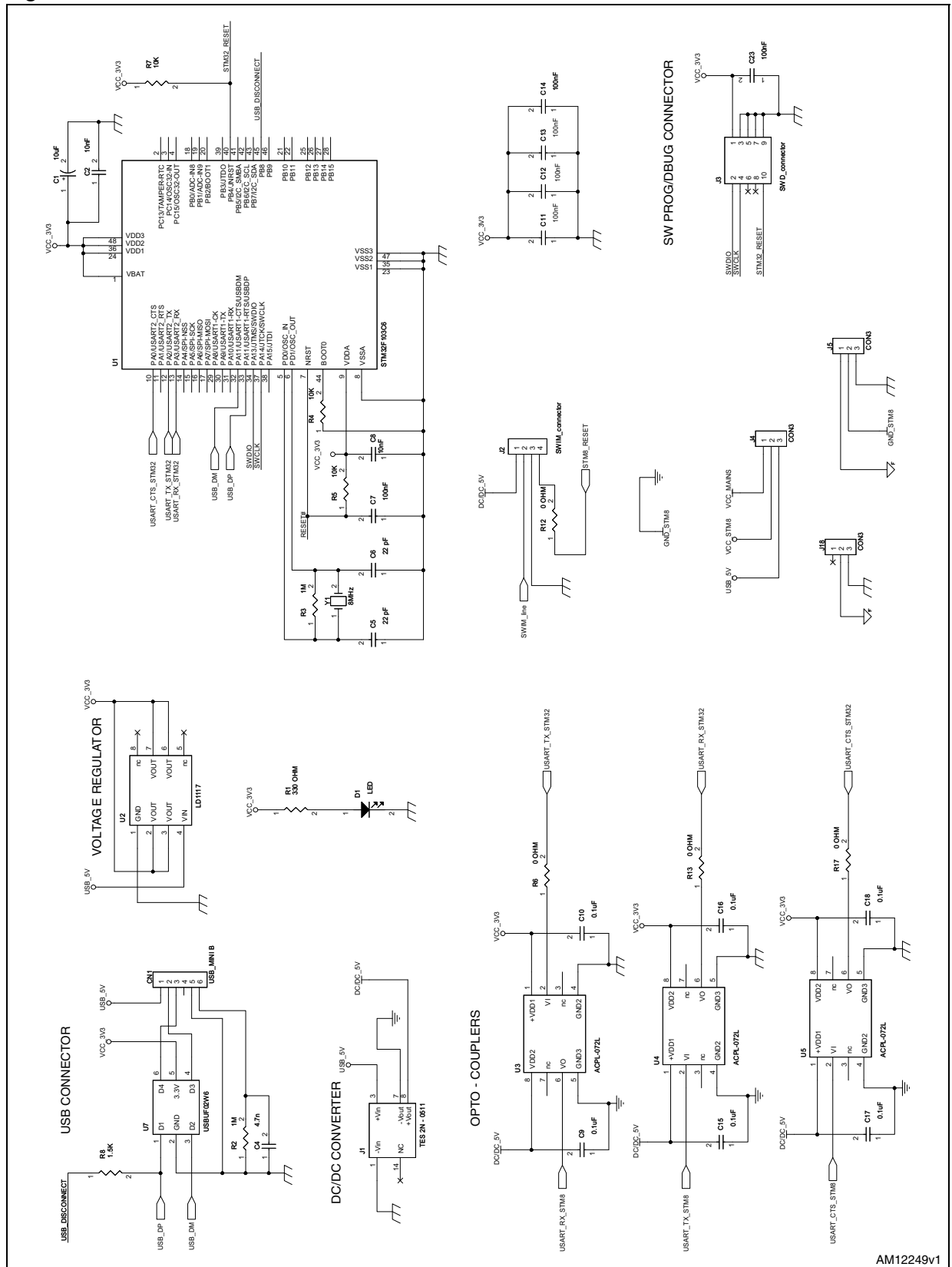


Figure 4. Additional pads schematic

