STM32G081B-EVAL USB-C daughter board

MB1352

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Legend

General comment such as function title, configuration, ...
Text to be added to silkscreen.
Warning text.
Notes to generate the board layout.

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U_MB1352_TOP
MB1352_TOP.SchDoc
**Source Power**

<table>
<thead>
<tr>
<th>Mode</th>
<th>Mount</th>
<th>Removed</th>
</tr>
</thead>
<tbody>
<tr>
<td>GPIO</td>
<td>SB13,SB14,SB15</td>
<td>SB2,SB3,SB23,SB26</td>
</tr>
<tr>
<td>PWM(Default)</td>
<td>SB2,SB3,SB23,SB26</td>
<td>SB13,SB14,SB15</td>
</tr>
</tbody>
</table>

**Step 1:** DCDC_EN detects its voltage to ensure PSU 19V exist
**Step 2:** PWM_CTL provides about 0.8V level before DCDC_EN=1
**Step 3:** DCDC_EN=1, then PWM_CTL changes to provide PWM waveform by 0 and open-drain.