LNA ON: D1 soldered a 100 pF cap between 1 - 2
D2 soldered a 100 pF cap between 1 - 3
LNA OFF: D1 soldered a 100 pF cap between 1 - 3
D2 soldered a 100 pF cap between 1 - 2

PA under control of MCU (requires three GPIOs from MCU):
D3 soldered a 0 ohm between 1 - 2
D4 soldered a 0 ohm between 1 - 2
D5 soldered a 0 ohm between 1 - 2

PA under control of S2-LP:
D3 soldered a 0 ohm between 1 - 3
D4 soldered a 0 ohm between 1 - 3
D5 soldered a 0 ohm between 1 - 3