1: Updated back light name port from PA5 to PI0
2: Updated CN2 name from PMOD+ to STMOD
3: Pull-up EN pin of USB power switch (U14) to 5V instead of 3V3
4: Swap "MFX_IRQ_OUT" and "MFX_WAKEUP"
5: Connect 4 fix holes to GND
6: Replace PA0 (MFX_aGPIO0) with PA1 (MFX_aGPIO1)
7: Replace FMC_A0 (PF0) with FMC_A18 (PD13)
8: Replace load capacitors C9&C11 to 5.6pF
10: Connect PG9 to CS1 of PSRAM via R12 & a two-pin jumper in parallel. Add possibility to power PSRAM by either 3V3 or VDD_LCD
11: Updated R18 to N/A, SB2 to OPEN, SB23 to JP10, Connect VDDA to VDD_USB instead of VDD_MCU
12: N-MOSFET BSN20 added to drive LD3
13: Added USBLC6-2SC6 for ST-LINK USB
WARNING: voltage applied to VIN <11.5V
WARNING: CN2-PIN4 is not a input PWR for BigOrga
The 2 LEDs are top side.

SD can only work under 3V3.
USB Full Speed operating range voltage: 3.0V < VDDUSB < 3.6V

transistor pins numbers follow SOT23 JEDEC standard,
**Title:** LCD Interface

**Date:** 2016/11/16

**Revision:** DB4

* standard orientation. Rotations by 90, 180, 270 degrees possible by register.

**SB28 open and SB27 closed = IM high: LCD in 16bits mode**

**SB28 closed and SB27 opened = IMlow: LCD in 8bits mode**

**LCD Interface**

- **SB28** open and **SB27** closed = IM high: LCD in 16bits mode
- **SB28** closed and **SB27** opened = IM low: LCD in 8bits mode

**Backlight driver & PFC connector for LCD panel**

**Notes:**
- Backlight control: ON/OFF or dimmer by low frequency (1 to 1kHz typ.) PWM using TIM5.
- Current regulated at 15mA

**Layout:** parts close to STLD40D and grouped in same area with BLGND as local ground plane.
Title: PSRAM
Project: STM32L496G-D15C0
Size: A4  Reference: MB1261
Revision: B.01
Date: 2016/10/11
Sheet: 11 of 14

3.3V functional only
Camera can only works under 3V3