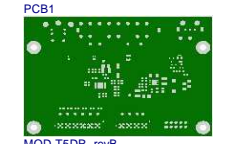


NOTE: When R21 is NM TCXO is 1.8V



Board Title		MOD-T5DR_REV6		CONFIDENTIAL AND PROPRIETARY INFORMATION	
Sheet Title		PAGE1: STA8100GA		This drawing and the information herein, is not to be used or copied without the prior written permission of STMicroelectronics.	
Doc No	Date	Rev	Sheet		
(435/xxxxxx)	Friday, March 12, 2021	A1	1 / 2		

new in rev1.1:

- Removed R24 from UART2_TX.
- Added R24 as a pull up on EXT_REG_SEL pin.
- Changed the footprint of the RTC from 1.6x1 to 2x1.2 on PCB rev1.1
- Changed the Part Number of the TCXO from DSB211SP to NT2016SE-26M-END4165E, the footprint is the same.

new in rev1.2:


- Changed the pin assignment of Q1 (on layout).
- Pull up on SPI_CS of Mems is referred to LDO_OUT.
- Optimized the connection of VCC_CORE between the internal regulator and the external regulator.
- Modified RF_IN.

new in MOD-T5DR_revA

- Remove LNA supply
- Remove LDO and connect core supply to external PIN #29
- Add external pin for core supply selection #28
- Add external pin with standby_out #47
- Add connection of VCORE_IN to external PIN #33
- Change NPN/NPN by NPN/PNP for STANDBY_OUT bug and to avoid current leakage in standby
- Add Enable pin capability on TCXO
- Change ODOMETER GPIO (from GPIO28 to GPIO1 for high speed tick capability)

new in MOD-T5DR_revB

- Add 4 clips for LID and LID connection on clips
- Replace 2 inductors for LID GND connection by 4 resistors 0ohms (one for each clip) connected to GND

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	Doc No (435/xxxxxx)	Date Friday, March 12, 2021	Rev A1	Sheet 2 / 2		