

EVB T5DR - Module EValuation Board

Schematics

Based on STA8100GA

Schematic Revision C0

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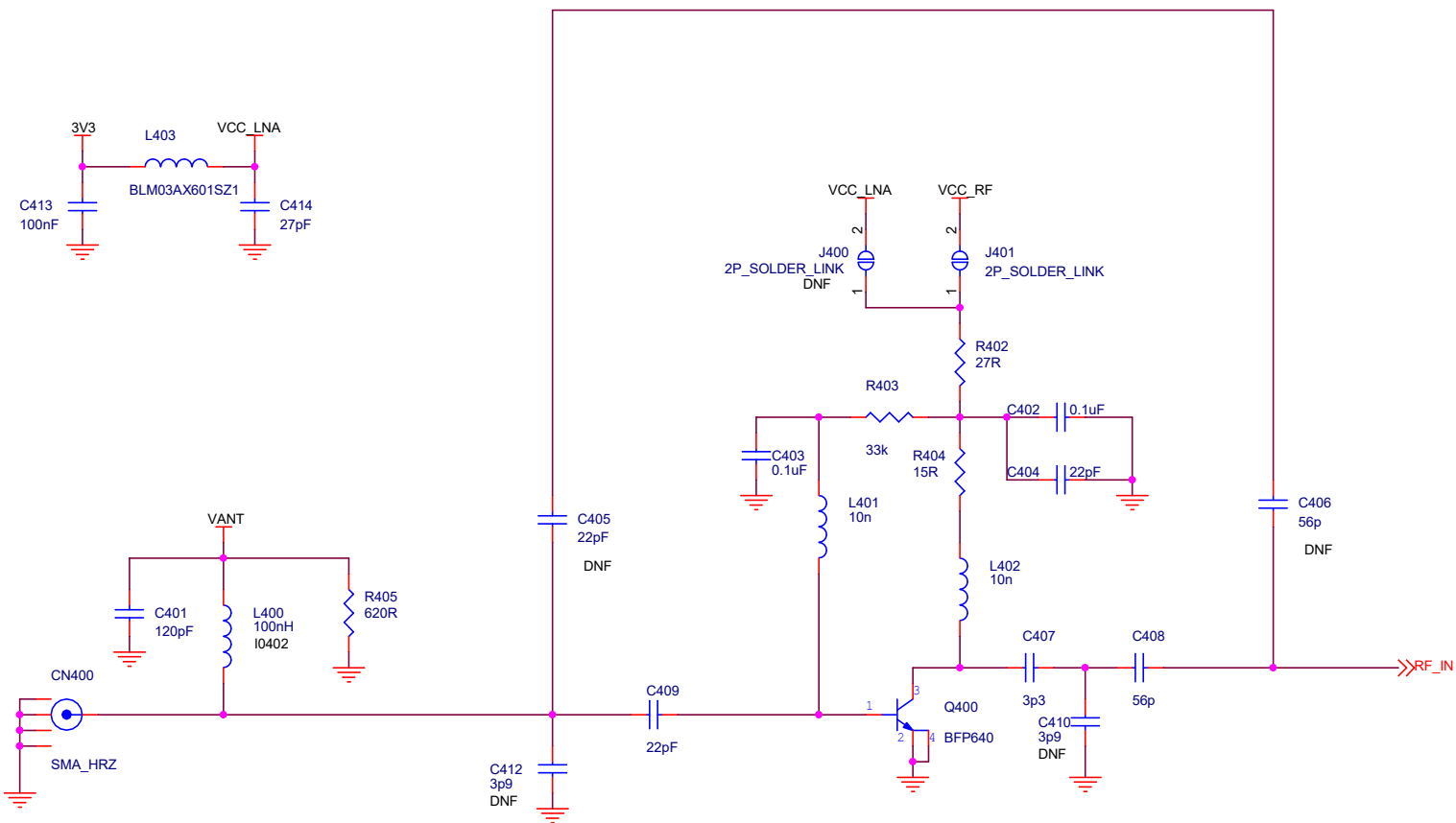
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Sheet Title PAGE 04: Front end

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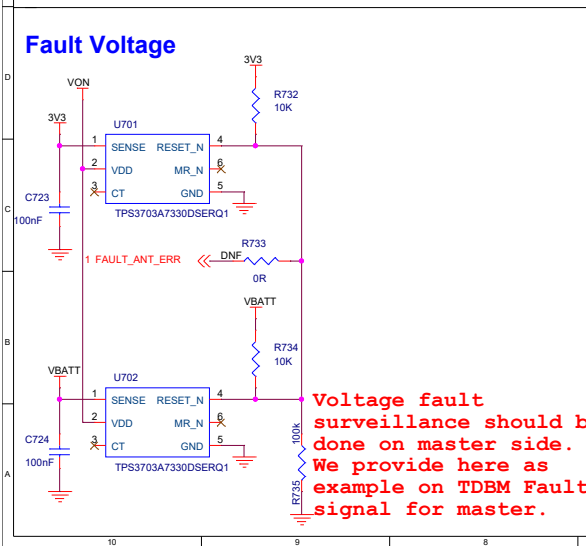
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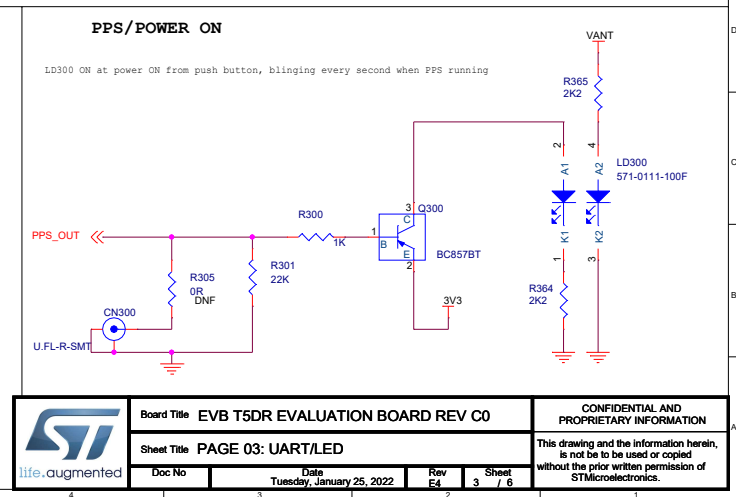
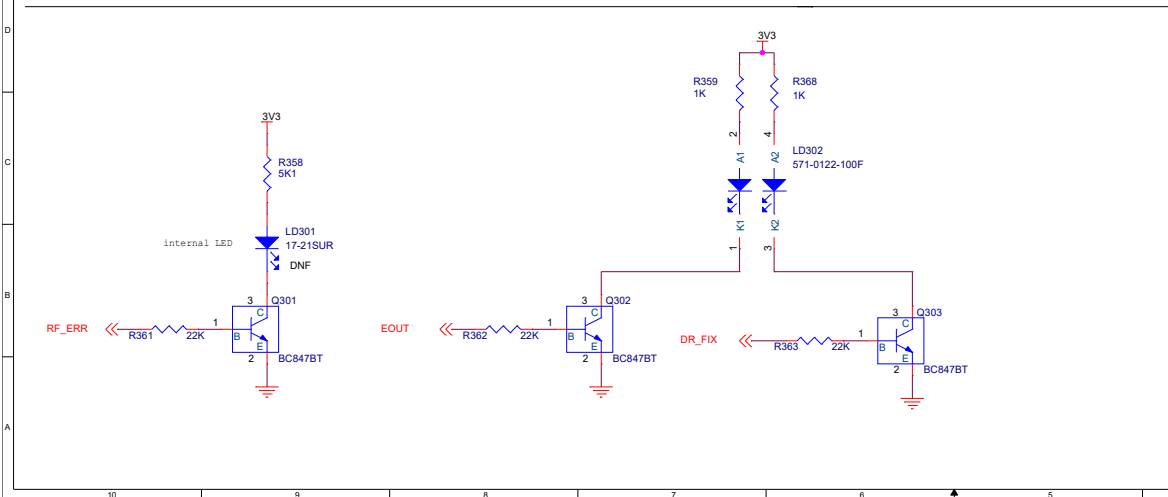
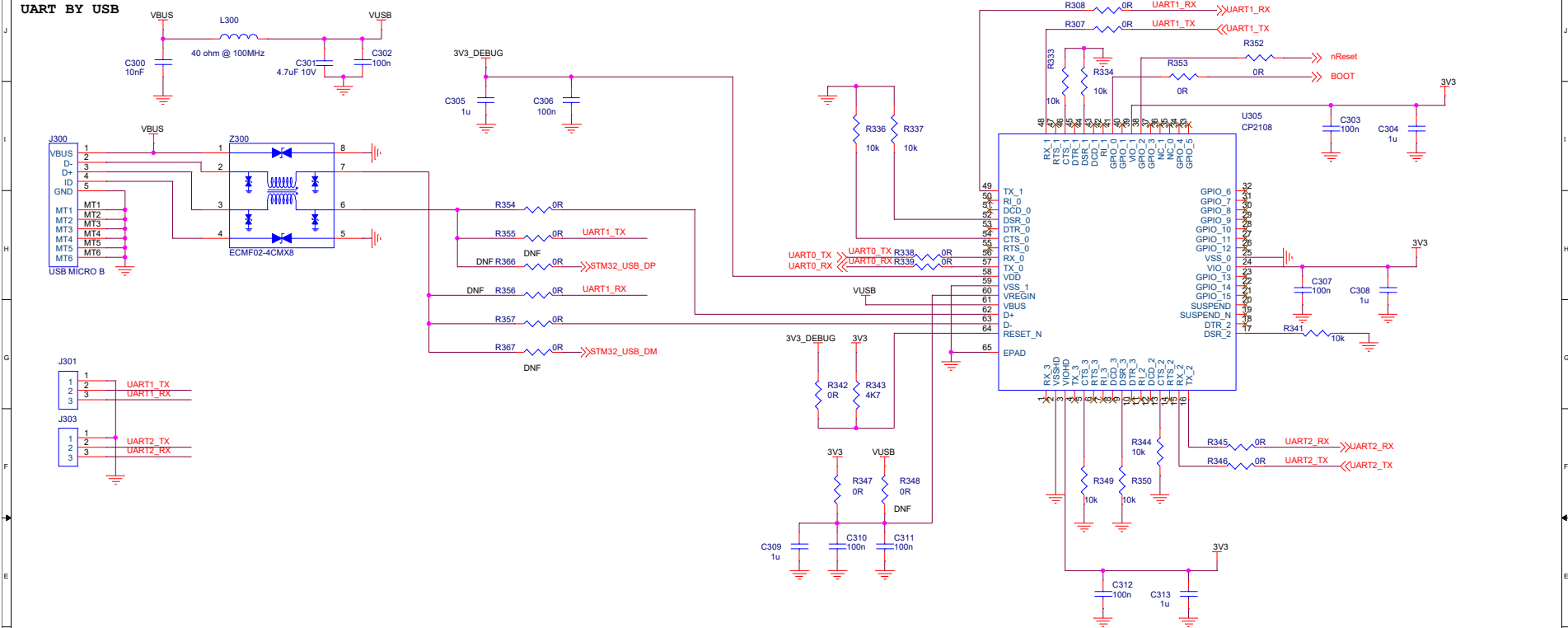
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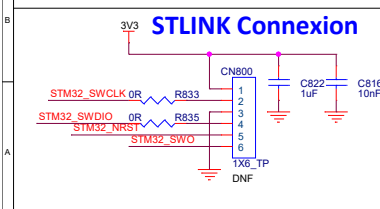
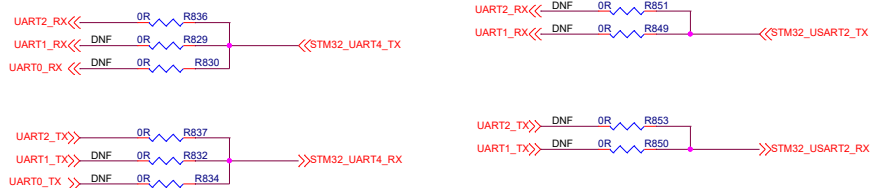


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UART BY USB

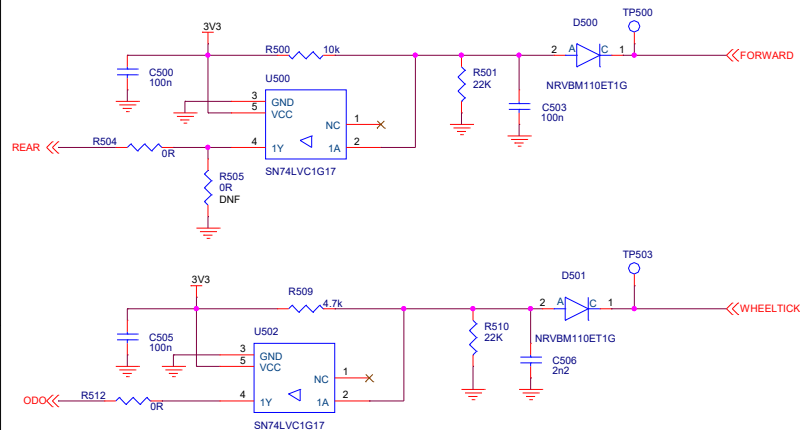


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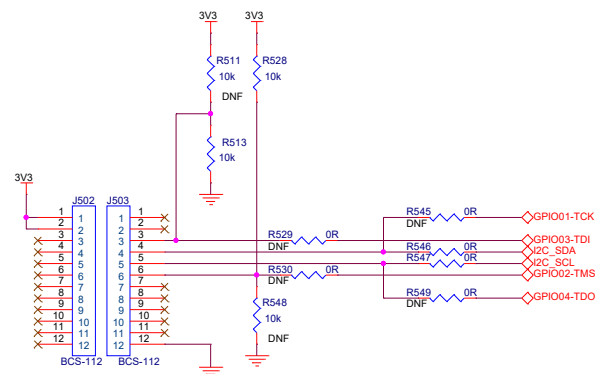


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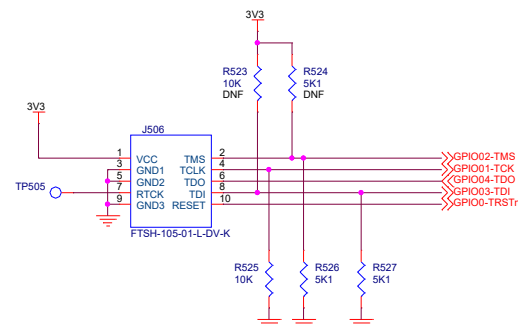
REAR & ODOmeter



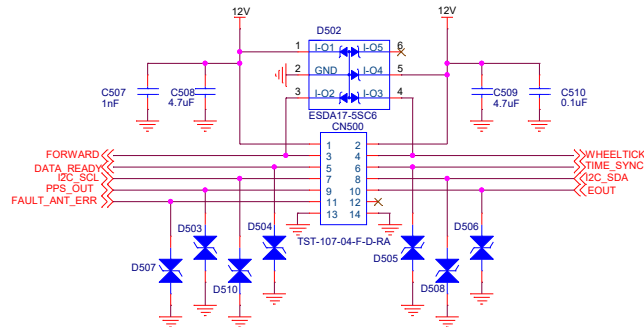
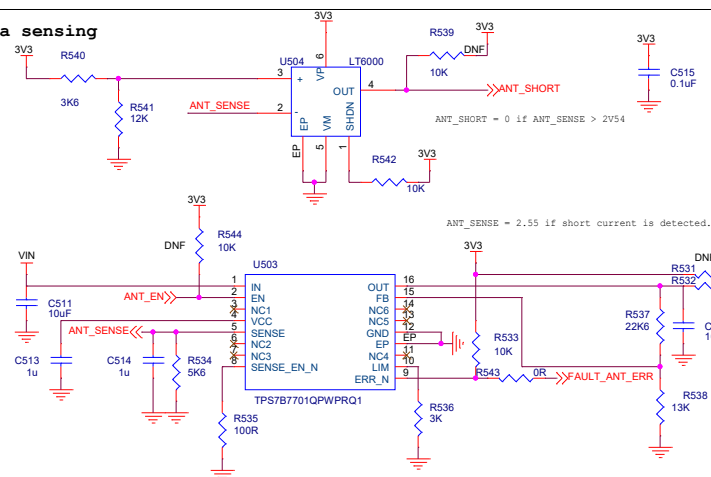
MEMs



JTAG



Antenna sensing



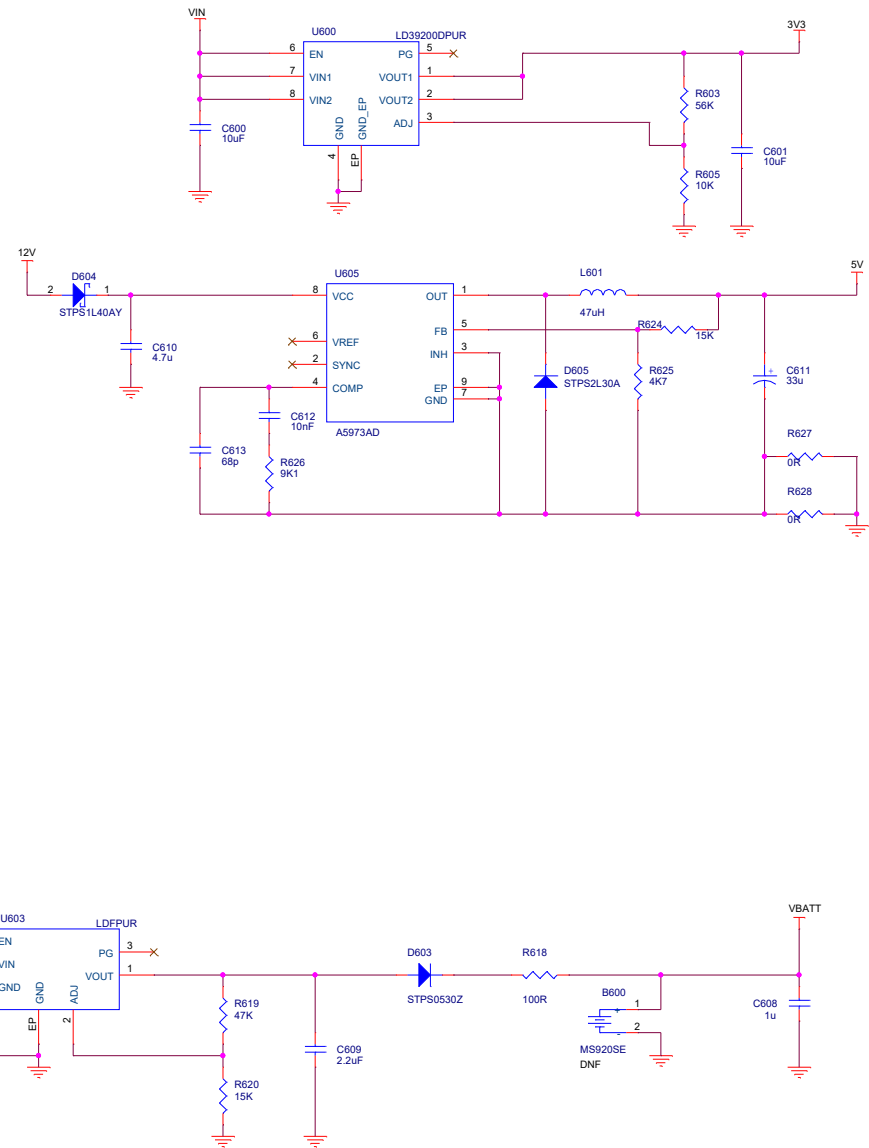
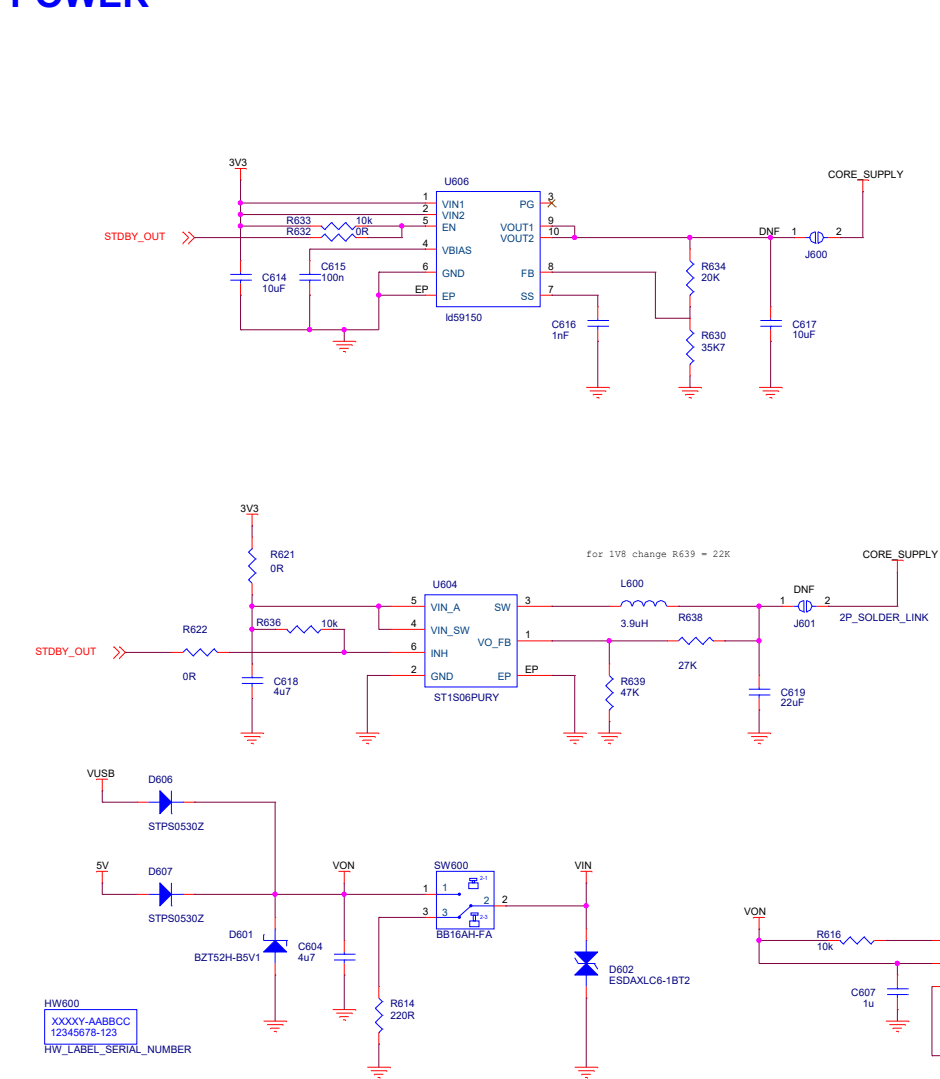
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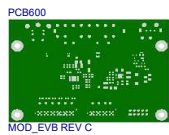
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POWER

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HW600
XXXXY-AABCC
12345678-123
HW_LABEL_SERIAL_NUMBER



MOD_EVB REV C



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