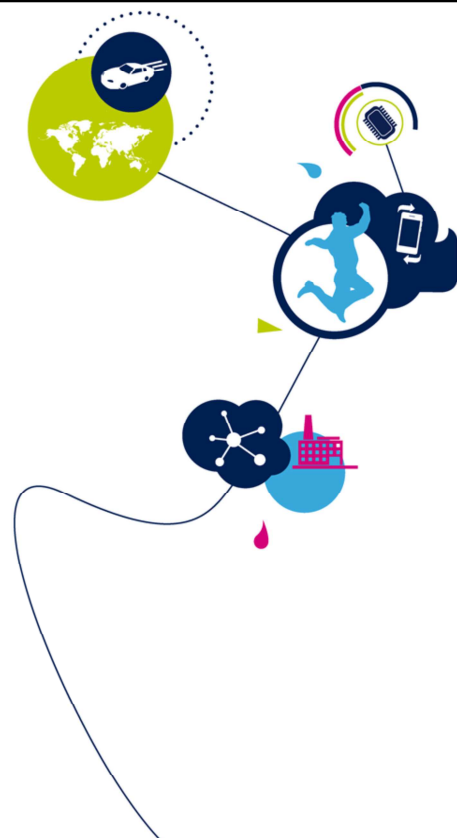
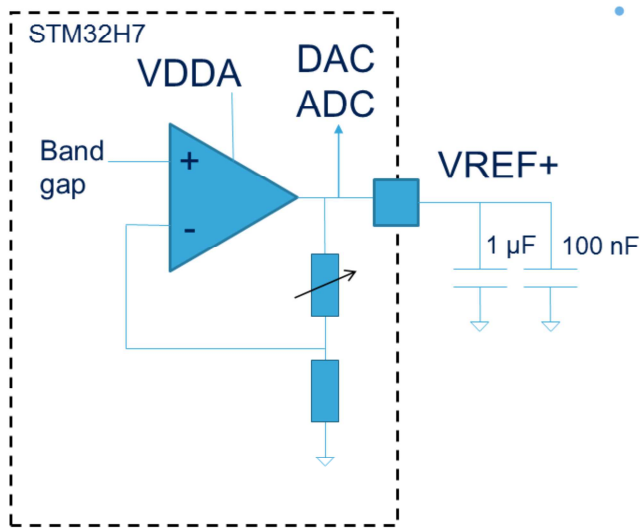


STM32H7 - VREFBUF

Voltage Reference Buffer
Revision 1.0



Hello, and welcome to this presentation of the STM32 Voltage Reference buffer. It covers the main features of this block, which creates an on-chip reference voltage.



- Provides an analog reference voltage
 - 2.5 / 2.048 / 1.8 / 1.5 V reference voltage for ADC/DAC
 - Can provide reference voltage and support external load up to 4 mA with low quiescent current.
 - User calibration mode

Application benefits

- Not necessary to have external reference voltage IC.
- On-chip VREF generator provides VDDA-independent reference voltage.



The VREF buffer embedded into STM32H7 microcontrollers provides a stable voltage based on an internal bandgap reference for use by both the analog-to-digital and digital-to-analog converters. Its output voltage is programmable to 1.5V to 2.5 V. This output voltage can also support external loads up to 4 mA. External bulk and bypass capacitors are required when the internal VREF buffer is used. A calibration mode enables a better accuracy on the output voltage for the application.

Applications can benefit from this on-chip voltage reference as it eliminates the need for an expensive, external standalone reference voltage IC. For space-constrained systems, it is common to use the analog supply as the reference voltage. By using this VREF buffer instead, it can create a stable voltage even if the analog supply is changing, for example when the VDDA supply comes from a battery output.

Low-power modes

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Mode	Description
Run	Active.
Sleep	Active.
Stop	Active.
Standby	Powered-down. The peripheral must be reinitialized after exiting Standby mode.

The VREF Buffer is active in the following power modes:
Run, Sleep and Stop modes.
In Standby mode, the VREF buffer is powered-down and it must be reinitialized afterwards.

Symbol	Condition	Typical	Unit
V_{DDA}	$V_{REF} = 1.5$	1.8~3.6 *	V
	$V_{REF} = 1.8$	2.1~3.6 *	V
	$V_{REF} = 2.048$	2.4~3.6 *	V
	$V_{REF} = 2.5$	2.8~3.6 *	V
I_{load}	Max. load current	4	mA
I_{VDDA}	$I_{LOAD} = 0 \mu A$	15	μA
	$I_{LOAD} = 500 \mu A$	16	μA
	$I_{LOAD} = 4 \text{ mA}$	32	μA
PSRR	DC	60	dB
t_{start_up}	$C_{LOAD} = 1 \mu F$	500	μs

* In normal mode



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This table shows some performance parameters for the VREF buffer. In Run mode, the VREF buffer can work from 2.4 to 3.6 V for a 2.048 V output, and 2.8 to 3.6 V for a 2.5 V output. The quiescent current is very small even with a 4 mA output current. It is possible to disable the VREF buffer when it is not being used. It can be available again 500 micro seconds after it is re-enabled.

Related peripherals

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- Refer to these trainings linked to this peripheral, for more information
 - Analog-to-digital converter (ADC)
 - Digital-to-analog converter (DAC)



The analog-to-digital and digital-to-analog converters use this VREF Buffer output. Please refer to training modules for these peripheral for additional information.