



| Series | Frequency | Graphics acceleration & optimizer | Display interfaces | Supported Resolutions | Packages |
|---|---------------|------------------------------------|---------------------|-----------------------|--|
| STM32G0 - CM0 | 64 MHz | - | SPI | Up to 480*272 | QFP, CSP |
| STM32WB - CM4 | 64 MHz | - | SPI | Up to 480*272 | QFP, BGA, CSP |
| STM32F4 - CM4 Access lines | 100 - 180 MHz | - | Parallel SPI | Up to 1024*768 | QFP, BGA, CSP |
| STM32F4 - CM4 advanced lines | 180 MHz | Chrom-ART | RGB-TFT MIPI-DSI | Up to 1024*768 | QFP, BGA, CSP |
| STM32L4 (CM4) Low Power | 80 MHz | Chrom-ART | Parallel SPI | Up to 240*480 | QFP, BGA, CSP |
| STM32L4+ (CM4) Low Power | 120 MHz | Chrom-ART Chrom-GRC | RGB-TFT MIPI-DSI | Up to 450*450 | QFP, BGA, CSP |
| STM32U5 (CM33) Low Power | 160 MHz | Chrom-ART Chrom-GRC Neochrom | RGB-TFT MIPI-DSI | Up to 800*480 | WLCSF, DIE <i>Samples available</i> |
| STM32F7 (CM7) | 216 MHz | Chrom-ART MJPEG | RGB-TFT MIPI-DSI | Up to 1024*768 | QFP, BGA, CSP |
| STM32H7 (CM7) (CM7 + CM4) | 550 MHz | Chrom-ART Chrom-GRC MJPEG | RGB-TFT MIPI-DSI | Up to 1024*768 | QFP, BGA, CSP |
| STM32MP1 (CA7) | 800 MHz | 3D Graphic PU | MIPI-DSI | UP to 1366*768. | BGA |