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# Wide bandgaps materials and power module solutions for new energy applications

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# Agenda

1 WBG materials

2 SiC introduction and benefits

3 ST SiC technology overview

4 ST SiC product profile

5 ST SiC and module solution for new energy applications

6 Q&A

# Why silicon carbide?

## SiC power devices for performance beyond silicon



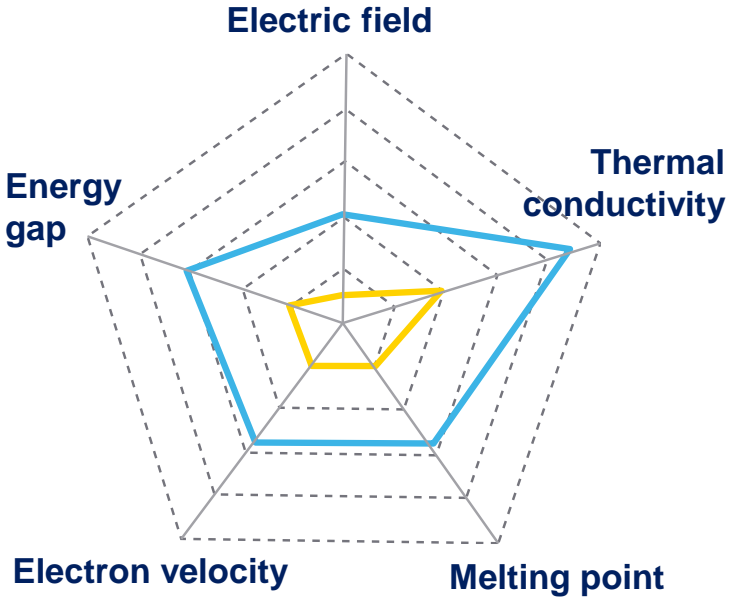
**Silicon Carbide**



**Higher breakdown voltage: x10**  
**Lower ON resistance & losses**

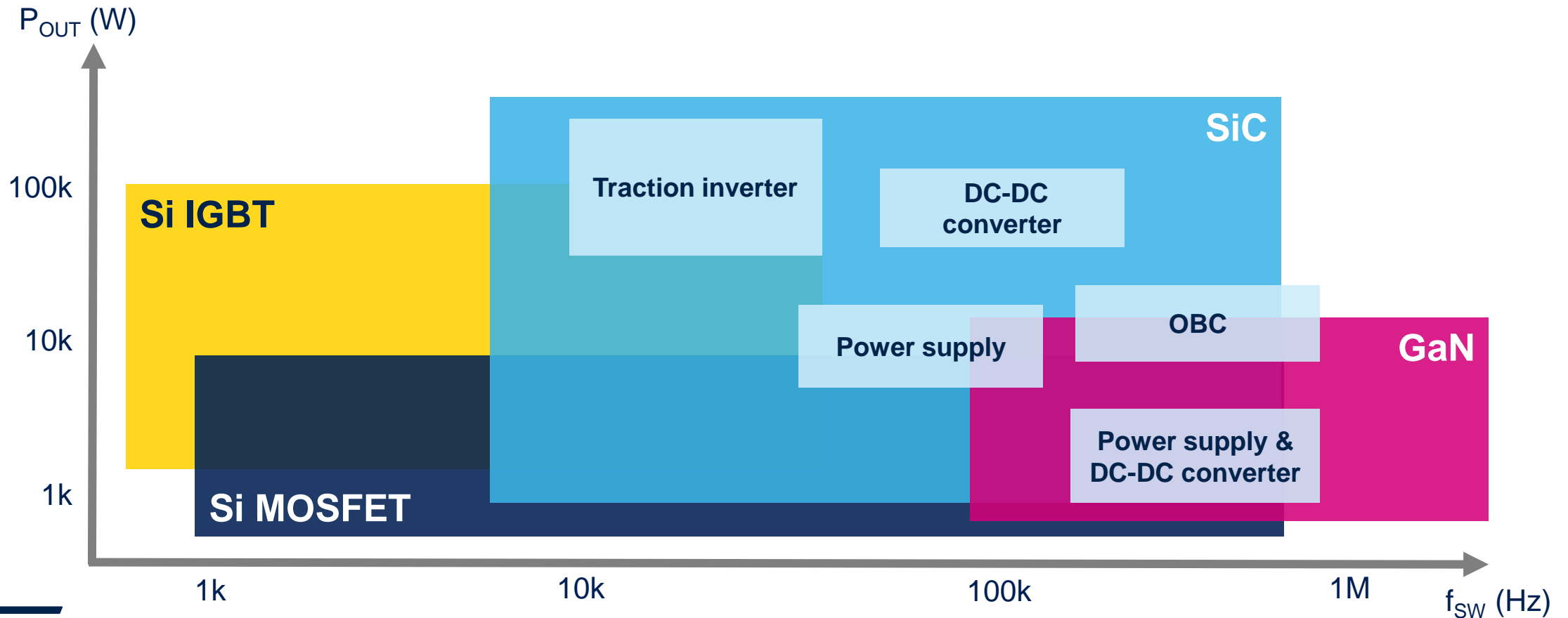
**Higher temperature (operation & endurance)**  
**Reduced cooling requirements**

**Higher switching frequency**  
**Lower switching losses**



# Power semiconductors for key applications

SiC MOSFET technology offers the best performance in high voltage, high frequency, and high-power system applications



# SiC benefits - Why SiC?



# Combining all 3 features of an ideal switch

Lowest  $R_{DS(on)}$ , highest speed, highest operating temperature

OFF		$I=0$ for any $V$
ON		$V=0$ for any $I$

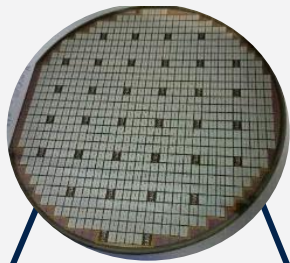
	SiC MOSFET (planar)	Silicon MOSFET	Silicon IGBT
$R_{DS(on)} \times \text{Area}$	1	30	3 (10 at low current)
Switching energy losses	1	>10	7
Max. junction temperature (°C)	≥200	150	175

# Key benefits of SiC Diode

## Energy savings generated by sustainable technology



Power Losses



Recovery Losses

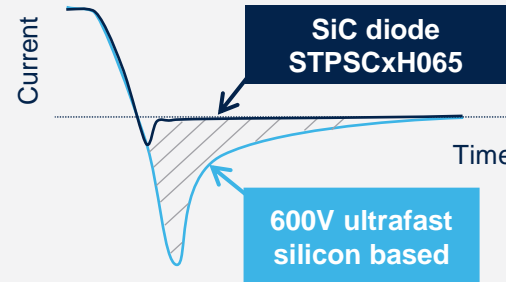
Conduction Losses

Si-based ultrafast diodes



Eliminate Recovery Losses

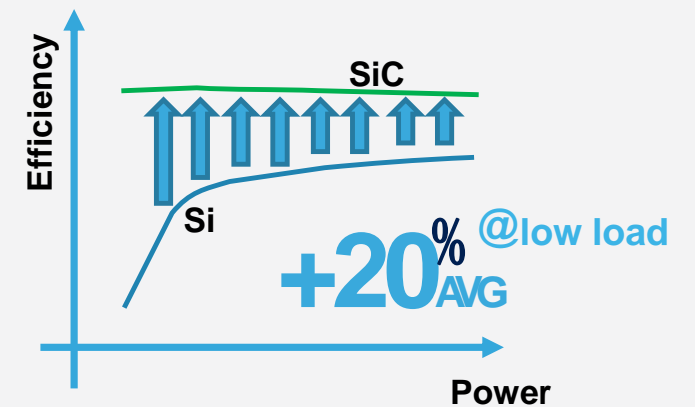
Switching performance comparison



SiC-based diodes

Reduced Dimensions

- 60%



More efficient power conversion

# Key benefits of SiC MOSFETs

**Extremely low energy losses and low  $R_{ON}$  especially at very high  $T_j$**

Higher operating frequency  
for smaller and lighter systems

**Thermal performance**

High operating temperature ( $T_{jmax} = 200^{\circ}\text{C}$ )  
Reduced cooling requirements & heat-sink  
Increased life time

**Easy to drive**

Fully compatible with  
standard gate drivers

**Very fast and robust intrinsic body diode**

More compact inverter



# SiC MOSFET Value Proposition

## SiC Technology Benefits SiC vs Conventional Silicon IGBT

### Higher Performance & Voltage Operation

- Extremely low power losses
- High efficiency at low current
- Intrinsic SiC body diode (4 quadrant operation)

### Higher Operating Frequency

- Lower switching losses
- Excellent diode switching performance

### Higher Operating Temperature

- Operating up to 200°C junction



## SiC Advantages for Automotive

**Electrification - mileage extension, smaller battery (or increased battery reliability), fast & efficient charging**

Efficiency gain in average

From ~2% (high load) to ~10% (low load)

Switching losses

~7x lower

Chip size

~5x smaller

Total loss

~50% lower

Switching frequency

~ 5 ..10 times higher

### Lower System Cost

~7x reduced form factor

~80% cooling system down sizing

~Simpler Sub-systems: smaller passives, no external freewheeling diode...

# ST SiC technology overview





# Silicon Carbide Manufacturing

Huge investments sustaining business growth

## Device manufacturing

In volume production with 150 mm since 2017

- High yields and automotive-grade quality
- Capacity expanded almost 4X in 2020 vs. 2017

Further 2.5X expansion, 200 mm tools compatible

- Second fab in Singapore to be qualified in 2021
- Back-end at 2 sites: Shenzhen (China) and Bouskoura (Marocco)

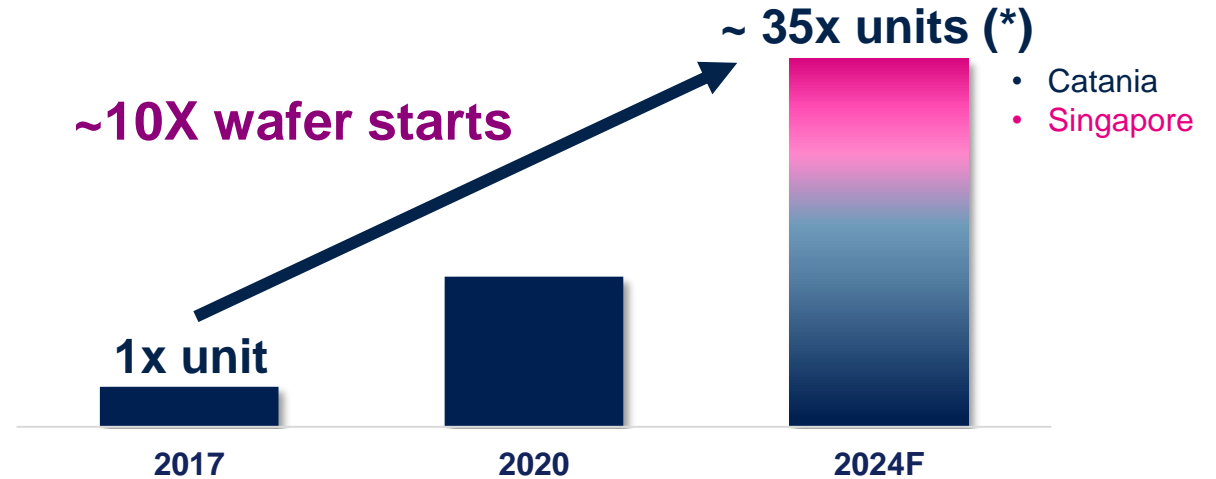
## Substrates

- Extended Supply Chain capability through Multi-Year supply agreement

## Vertical integration

- First internal supply of 150 mm from ST-Norstel, focus on 200 mm development
- Designing new plant to achieve > 40% internal sourcing by 2024

## Normalized Front-end\*\* capacity evolution



### Key Drivers

- Technology Evolution (Gen4)
- 200mm wafer size
- Yield improvement



Catania (Italy)



Singapore



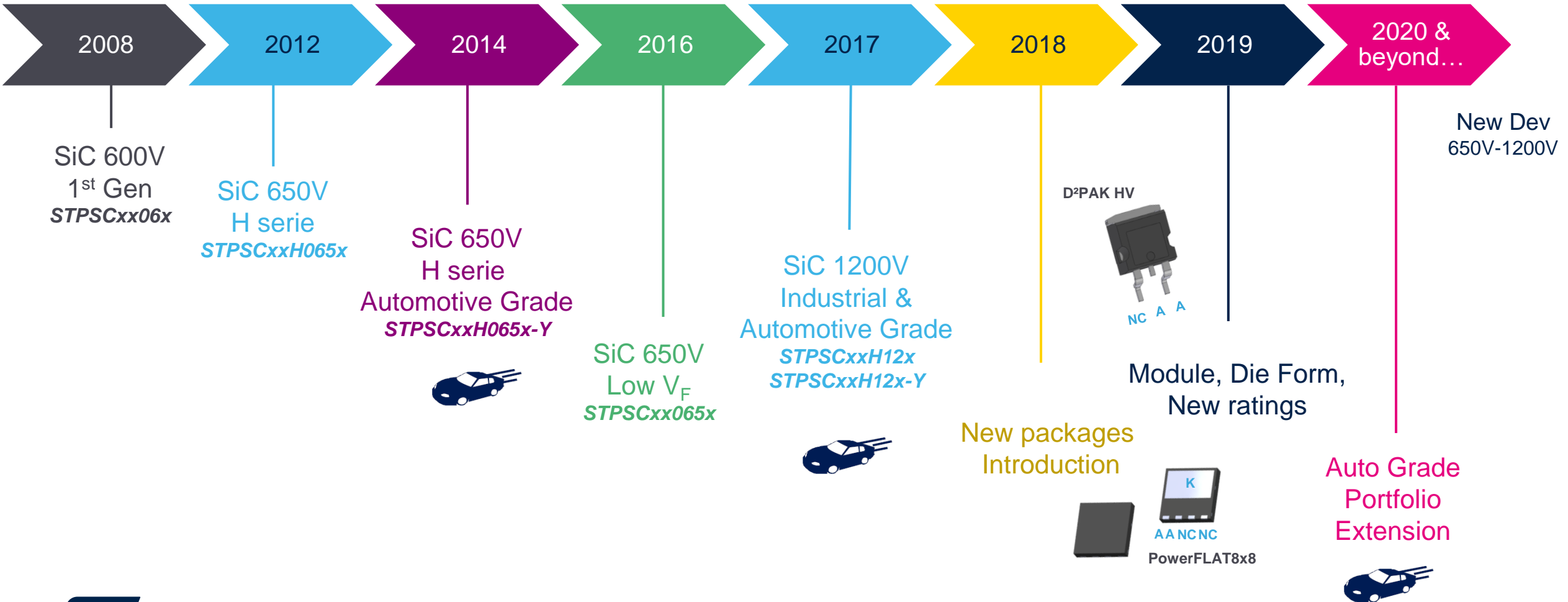
(\*) with Technology shrinkage and yield improvement

(\*\*) Front-end: wafer diffusion and manufacturing



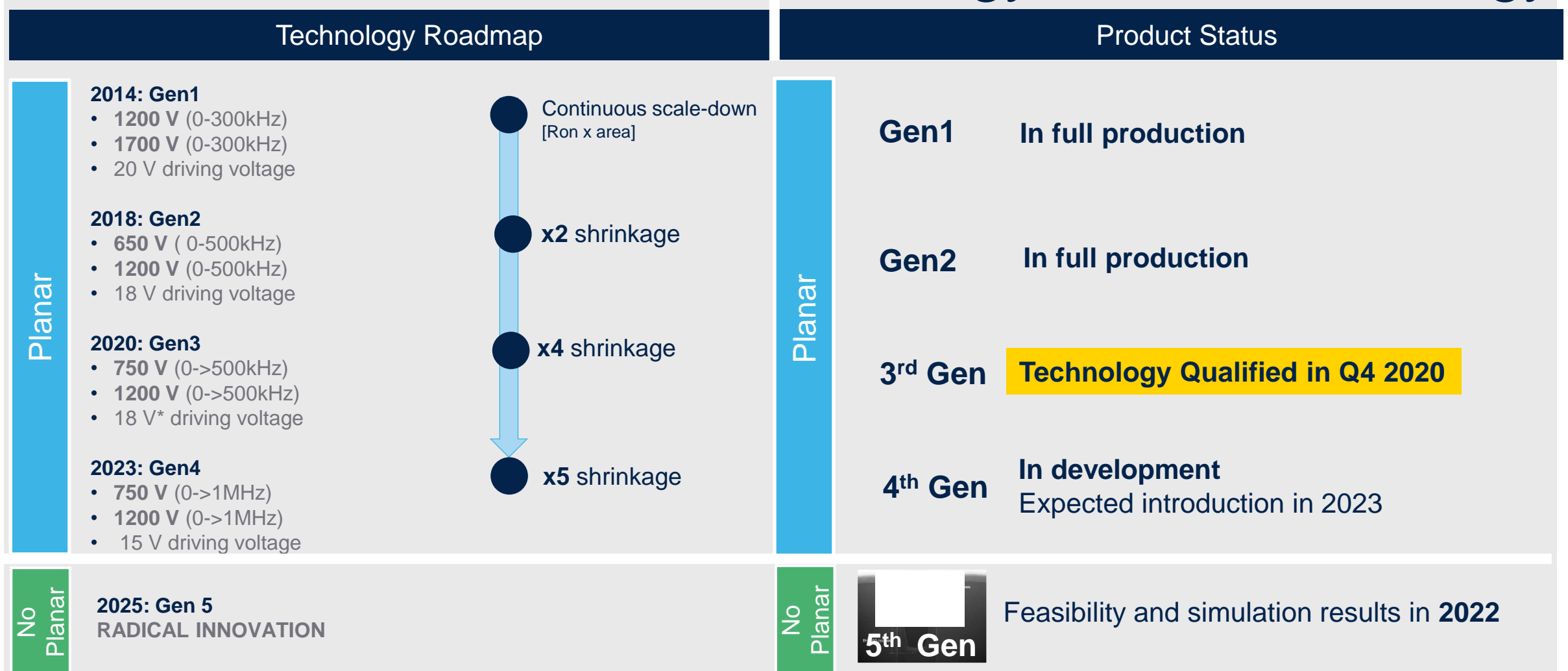
# ST SiC Diodes

## More than 12 years experience





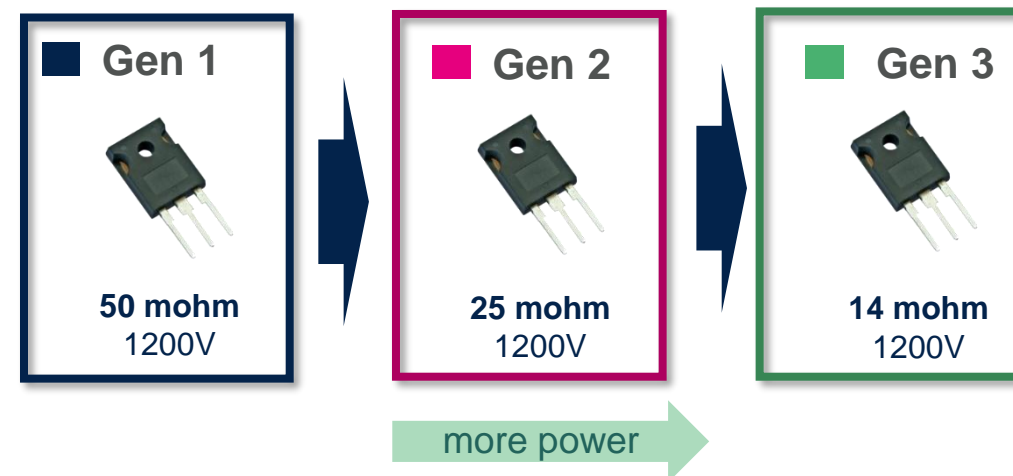
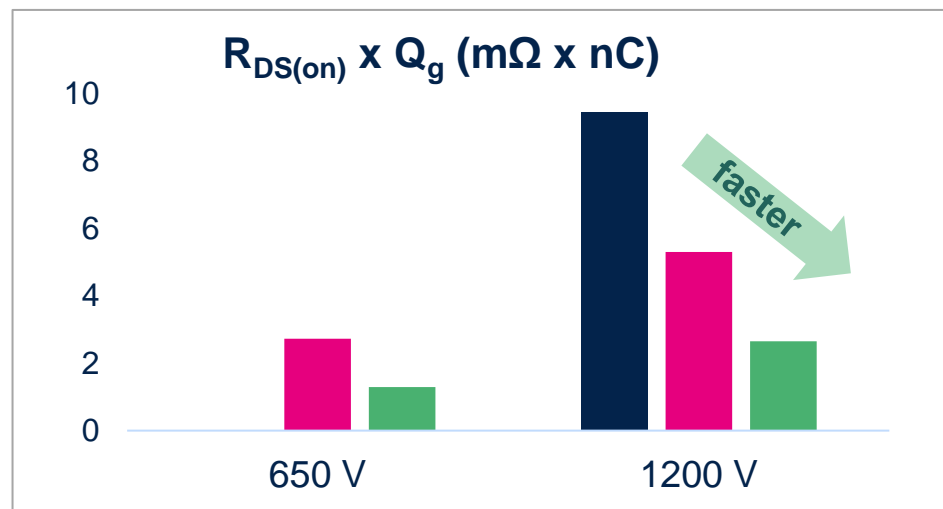
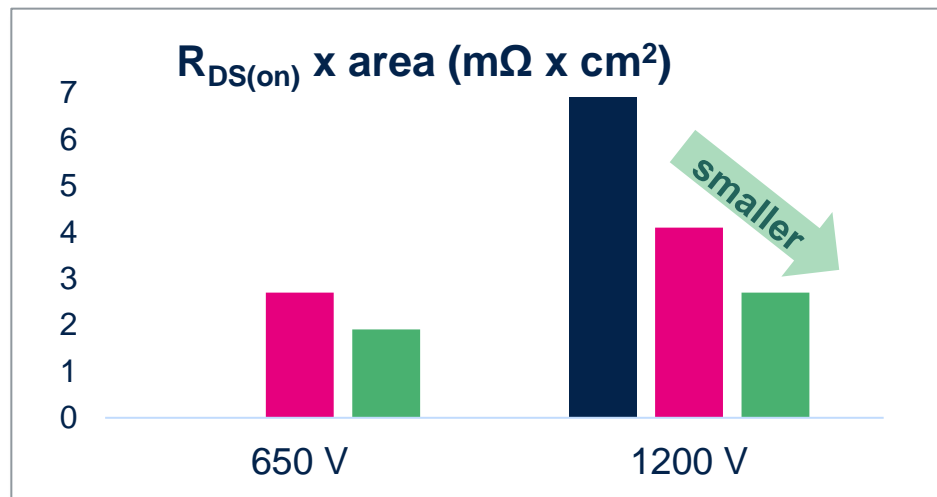
# Silicon Carbide MOSFET technology executive strategy



\* Suggested for best performance

# SiC MOSFET Advances in Technology

## Figure of Merits



### improvement in MOSFET generations

- Lower  $R_{on} \times \text{Area}$  → lower  $R_{on}$  for a given chip size or smaller chip size for a given  $R_{on}$ , higher current capability, lower conduction Losses → for a power module means higher power achievable with the same form factor
- Lower  $R_{on} \times Q_g$  → lower switching losses, higher frequency (reduced board)

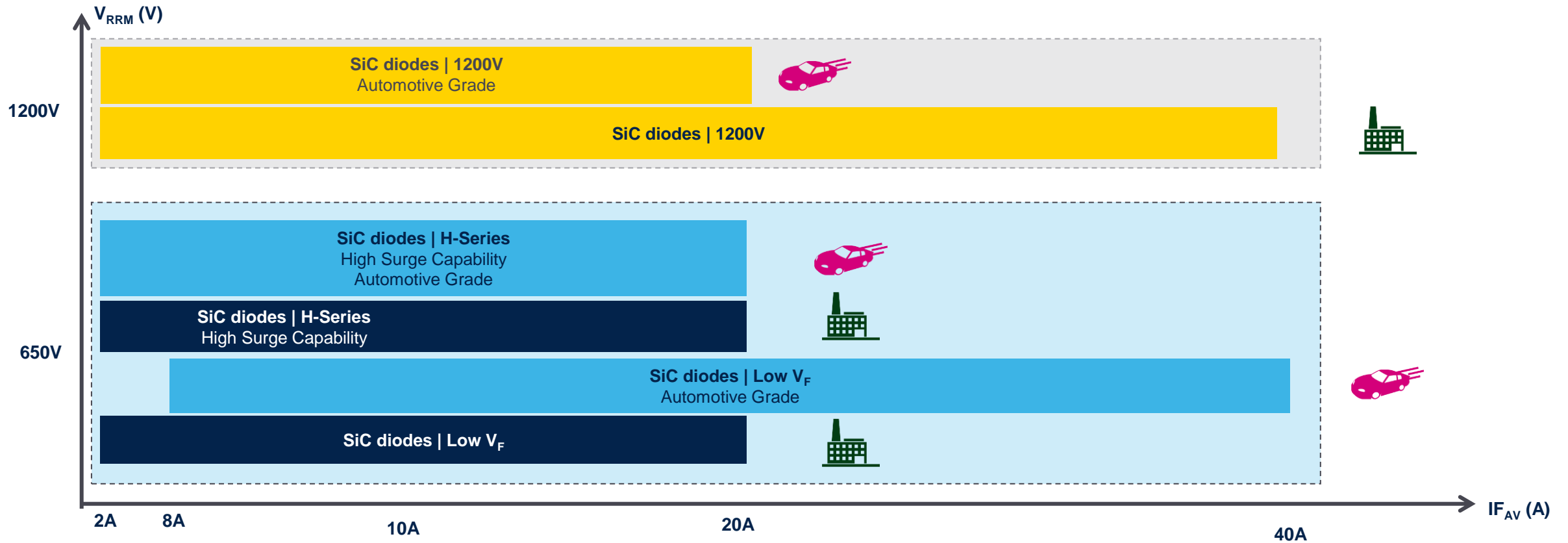
# ST SiC Product Profile



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# SiC Diode series overview

Extended range





# SiC MOSFET Portfolio

## Breakdown Voltage

650 V	750 V	1200 V	1700 V
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## Series

Gen 2	Gen 3	Gen 3	Gen 1	Gen 2	Gen 3	Gen 1
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## On-state resistance

18 mOhm to 55 mOhm	14 mOhm to 55 mOhm	11 mOhm	52 mOhm to 520 mOhm	21 mOhm to 75 mOhm	15 mOhm to 70 mOhm	1 Ohm and 65 mOhm
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## Packages



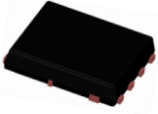



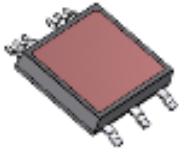
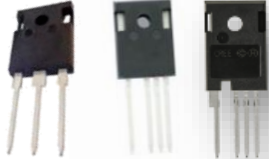
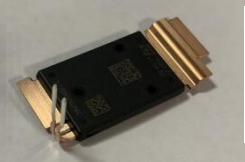
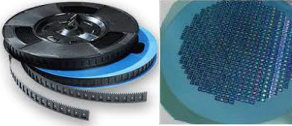
**HiP247**  
Rated at 200°C



Premier pilot product at 2200V under test



# SiC MOSFET Package Roadmap

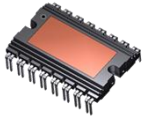
Package	Power FLAT 8x8 STD & DSC	TOLL	H2PAK-7L	HU3PAK	ACEPAK SMIT	HiP-247 3L 4L & 4L HC	STPAK	Bare Dice
								
	<b>Surface Mounting</b>					<b>Through-Hole</b>	<b>Special Package Solutions</b>	
<b>Characteristics</b>	<ul style="list-style-type: none"> <li>□ Very Thin (&lt; 1mm)</li> <li>□ Well accepted in power conversion</li> <li>□ Dual side cooling option</li> <li>□ Leadless</li> <li>□ <b>Industrial domain</b></li> </ul>	<ul style="list-style-type: none"> <li>□ Leadless</li> <li>□ Industrial domain</li> <li>□ Kelvin Source for optimized driving</li> <li>□ Good thermal dissipation</li> </ul>	<ul style="list-style-type: none"> <li>□ <b>AG qualified at 175dC</b></li> <li>□ Kelvin Source for optimized driving</li> <li>□ High runner for Automotive customers</li> </ul>	<ul style="list-style-type: none"> <li>□ <b>AG qualified at 175dC</b></li> <li>□ <b>Top side cooling</b></li> <li>□ Kelvin Source for optimized driving</li> <li>□ Very good thermal dissipation</li> </ul>	<ul style="list-style-type: none"> <li>□ <b>AG qualified at 175dC</b></li> <li>□ <b>Isolated Top side cooling</b></li> <li>□ Suitable for different configurations (HB, Dual die, etc.)</li> <li>□ High Power</li> <li>□ Modular Approach</li> </ul>	<ul style="list-style-type: none"> <li>□ <b>AG qualified at 200dC</b></li> <li>□ <b>Very common Industry standard</b></li> <li>□ Kelvin Source option for optimized driving</li> <li>□ High creepage version (1200V and 1700V) in development</li> </ul>	<ul style="list-style-type: none"> <li>□ <b>Unique Solution for traction Inverter</b></li> <li>□ <b>AG qualified at 200dC</b></li> <li>□ <b>Very High thermal dissipation efficiency</b></li> <li>□ Sense pin for optimized driving</li> <li>□ Multi-sintered package</li> </ul>	<ul style="list-style-type: none"> <li>□ <b>WLBI &amp; KGD</b></li> <li>□ <b>T&amp;R or RWF options</b></li> <li>□ <b>Compliant with the most stringent Automotive Quality Requirements</b></li> </ul>



# Power Module Solutions Overview for Industrial and Automotive

## ACEPACK Power Module

Silicon MOSFET & IGBT, Silicon Carbide MOSFET



ACEPACK  
DMT-32



ACEPACK  
SMIT



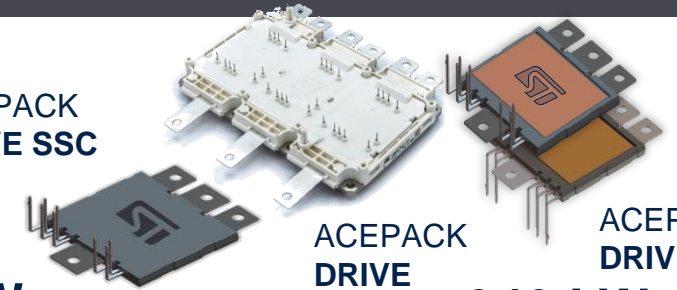
ACEPACK 1 & 2



30 kW

ACEPACK  
DRIVE SSC

150 kW



ACEPACK  
DRIVE

340 kW

ACEPACK  
DRIVE DSC

### Middle Power Applications



Solar Energy



Power Supply & UPS



HVAC



OBC



DC-DC Converter



Charging Station



Motor Drives



Grid and Infrastructure



Wind and renewable energy

### High Power Applications



Traction  
Inverter

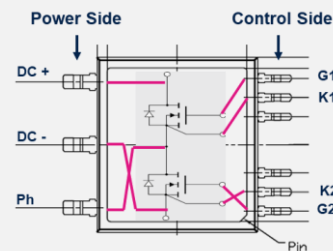
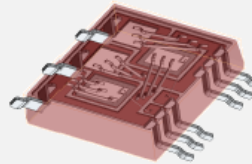
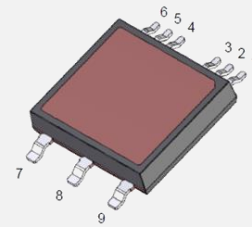
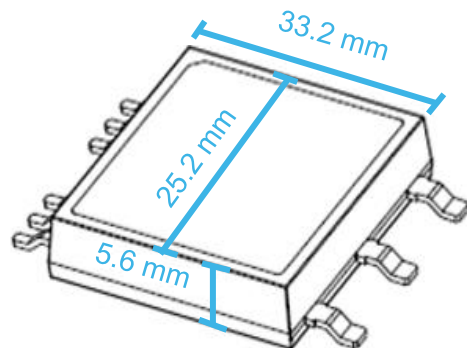


# ACEPACK SMIT features and benefits

## Surface Mounted Isolated Top-Side Cooled Package: ACEPACK SMIT

### Features

- Surface Mount Device for automatic stack assembly
- Up to six dies on Direct Bond Copper (DBC) substrate
- Top side cooling with low thermal resistance  $< 0.2^{\circ}\text{C/W}$
- Backside Insulated Ceramic, UL recognized,  $> 3400\text{VRMS}$
- Halogen free molding compound
- Improved creepage distances
- 6.6 mm minimum lead-to-lead

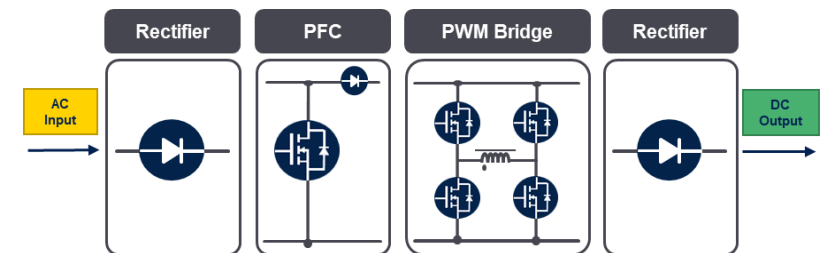


- This assembly is merely illustrative.
- Pin connections in real products may differ.

### Benefits

- Top side cooling package
- Molded and isolated thermal pad
- Very high thermal dissipation
- Kelvin source pin enables higher efficiency
- Suitable for several switch technology
- Several topologies can be realized
- Automotive graded
- Available in T&R\*

\* Tape and Reel



Ideal to realize a complete system

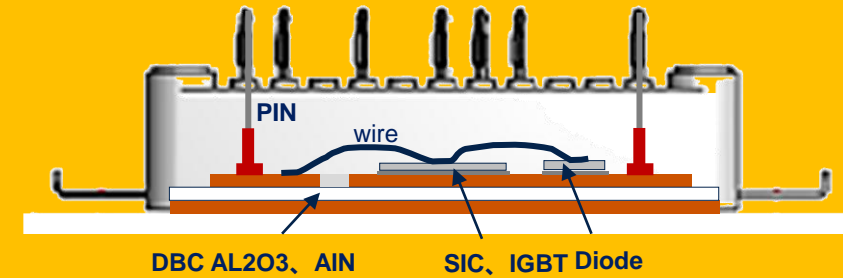
# ACEPACK A1 & A2 Plastic Package Power Modules



Package Dimension  
48x33.8



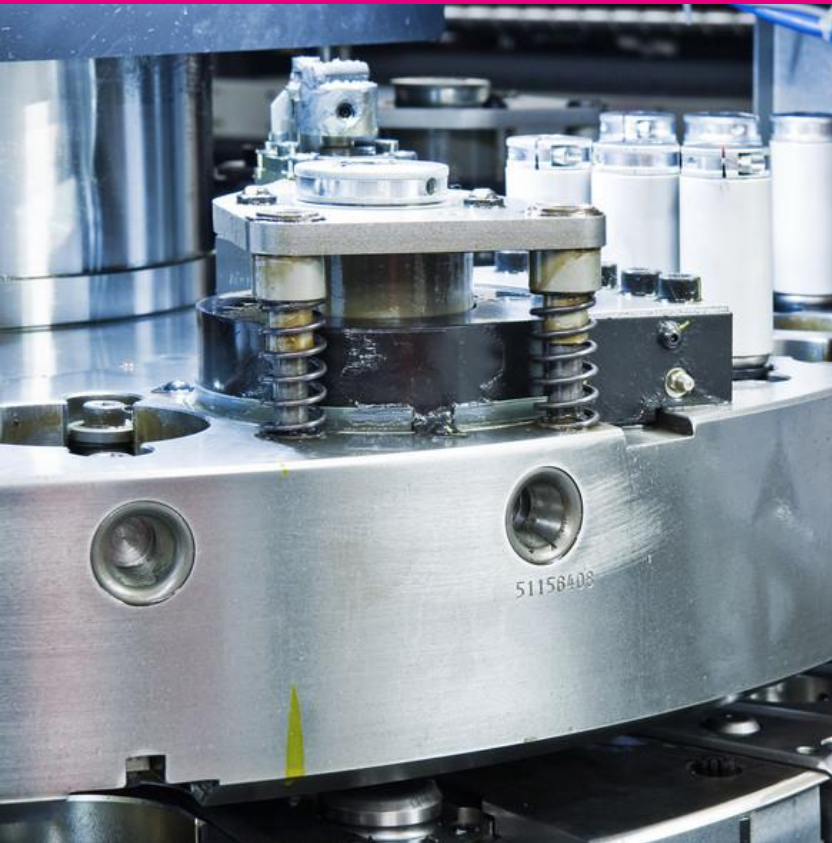
Package Dimension  
48x56.7



High flexibility in pin positioning for  
different pin out and configurations

# ACEPACK 1 and ACEPACK 2 key benefits

Industrial drives, motor control, UPS, and automotive EV ecosystems



## ACEPACK 1 & 2



33.8 x 48 mm



48 x 56.7 mm

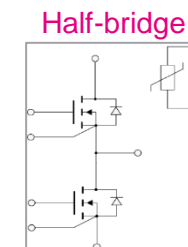
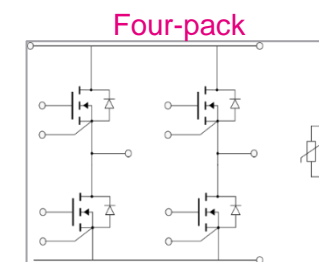
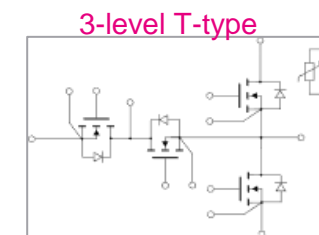
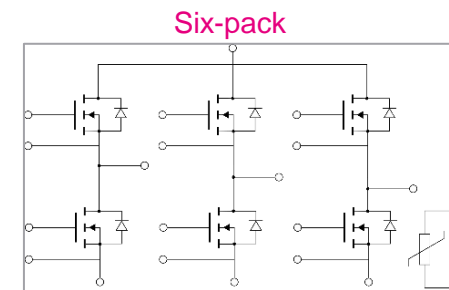
- Press fit and solder pin options for configuration flexibility
- Up to 1200V breakdown voltage
- Integrated screw clamps
- All power switches in a module including NTC
- Several current ratings available
- Several configuration options (CIB, six-pack, etc.)
- Low stray inductance
- High reliability and robustness, lower power-side board occupation
- Compact design and cost-effective system approach
- Very high power density

# ACEPACK 1 & 2

## Industrial-grade product developments

### New SiC-based power modules

Part number	Dice	Topology	BV	$R_{DS(on)typ}$ @ 25°C (per switch)	Package	Available within
A1P25M12W2-1	SiC	Six-pack	1200V	25mΩ	ACEPACK 1	Q1-2023
A2U12M12W2-F1C	SiC	3-level T-type S2S with integrated capacitance	1200V	12mΩ	ACEPACK 2	In production
A2U12M12W2-F2	SiC	3-level T-type D2D	1200V	12mΩ	ACEPACK 2	In production
A2F12M12W2-F1	SiC	Four-pack	1200V	12mΩ	ACEPACK 2	In production
A1F25M12W2-F1	SiC	Four-pack	1200V	25mΩ	ACEPACK 1	Q4-2022
A2H6M12W3	SiC	Half-bridge	1200V	6mΩ	ACEPACK 2	Q1-2023
A1H12M12W2-F	SiC	Half-bridge	1200V	12mΩ	ACEPACK 1	Q1-2023



Date: Sept 2022  
 Timeline and prototype availability may be subject to variation without notification

# ST SiC and module solution for new energy application

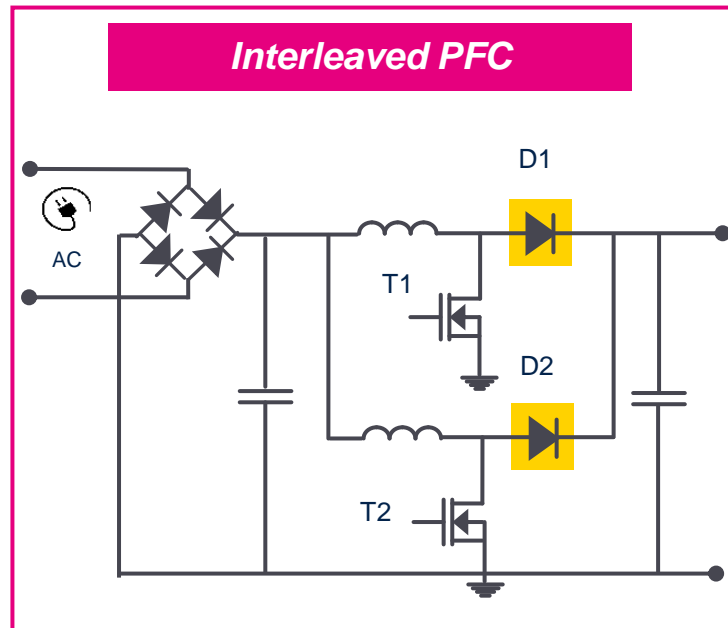


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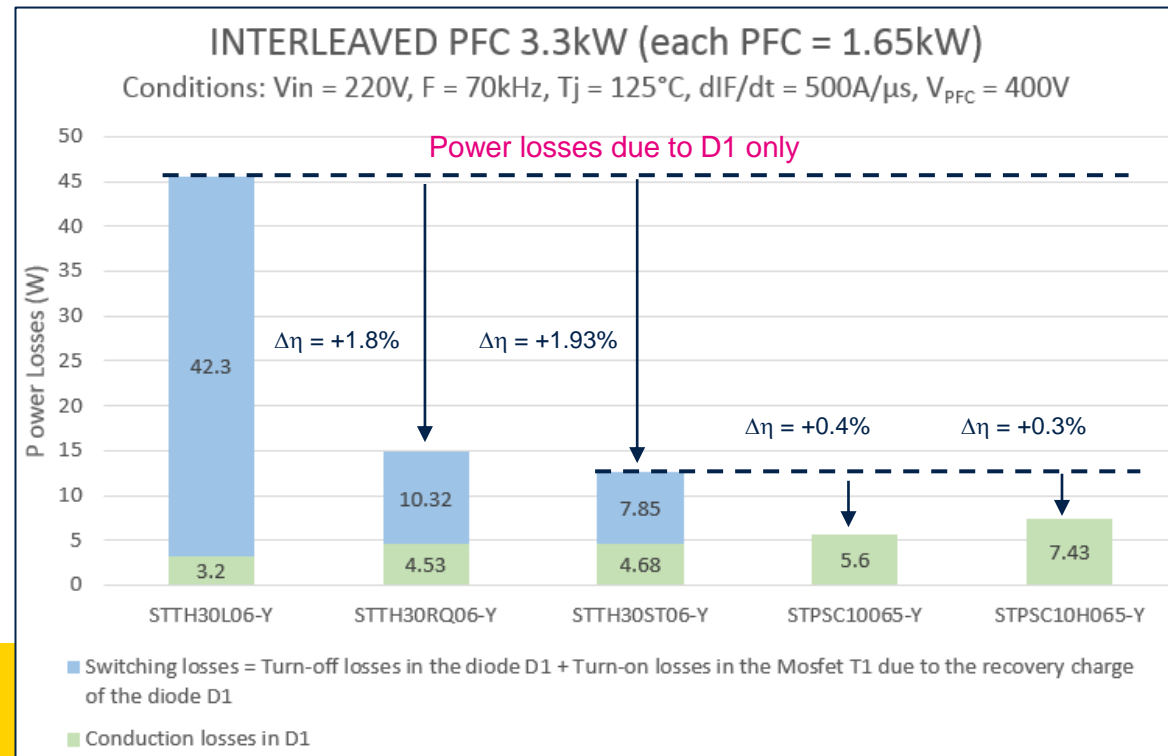
# 650V SiC diode performance vs Si

## Interleaved PFC 3.3kW: power losses sharing



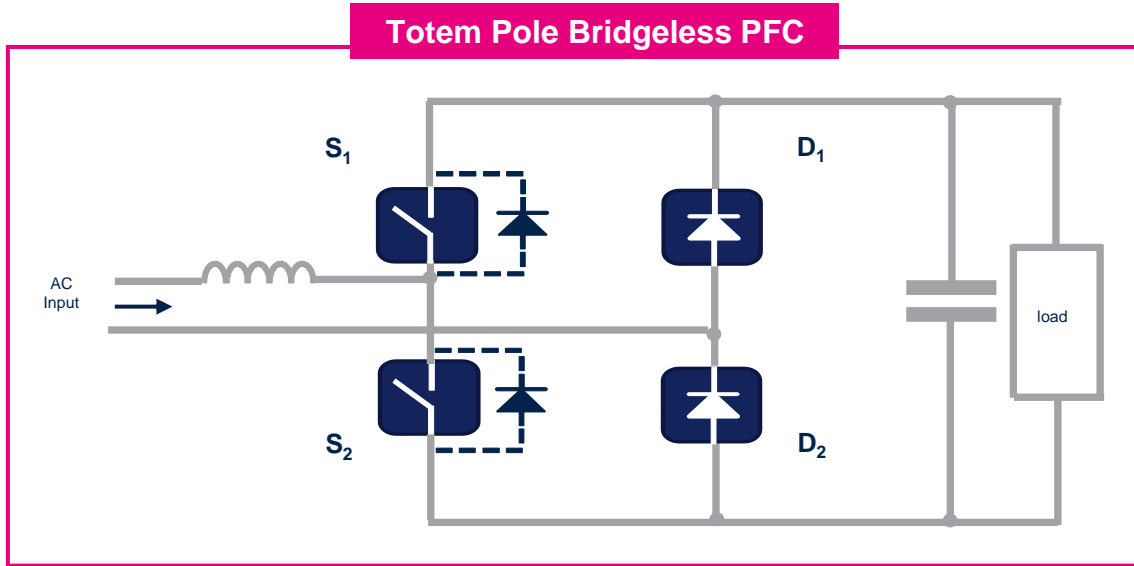
### Main advantages

- Best switching performance (fast and soft)
- Best efficiency in hard-switching applications thanks to best turn-off performance



# Totem-pole bridgeless PFC

## Ideal reverse recovery diode benefits



### Test conditions

- $P_{out} = 3.6 \text{ kW}$
- $I_{in \text{ rms}} = 16 \text{ A}$
- $V_{out} = 400 \text{ V}$
- $F_{sw} = 100 \text{ kHz}$
- $V_{in} = 220 \text{ V}_{ac}$

**SiC MOSFET  
outperforms both Si  
solutions**

### Typical losses at $V_{in} = 220 \text{ V}_{ac}$

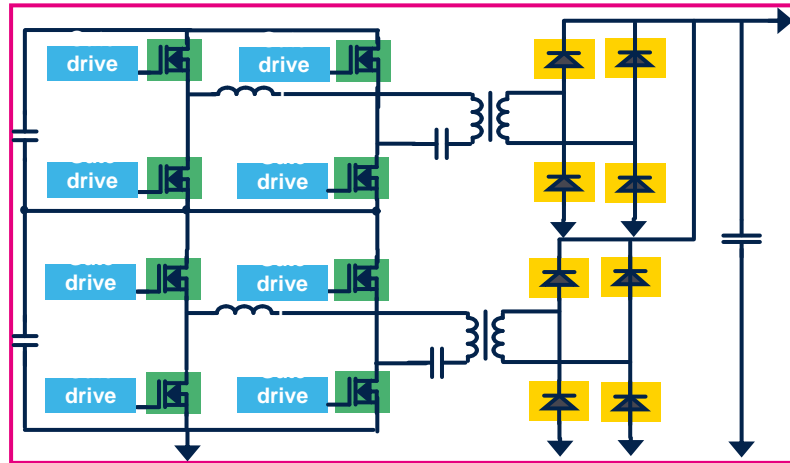
#### Devices

- $S_1 / S_2 = \text{ST SiC Mosfet } 650\text{V}/55\text{mohm}/\text{H2Pack}$
- $S_1 / S_2 = \text{ST Si Mosfet: } 650\text{V}/24\text{mohm}/\text{TO247}$
- $S_1 / S_2 = \text{ST } 650\text{V } 40\text{A IGBT}$
- $D_1/D_2 = 650\text{V } 30\text{A Si rectifiers}$

$S_1, S_2$ position	Conduction losses[W]	Switching losses[W]	$S_1 + S_2$ losses [W]	Efficiency*
SiC MOS	7.61	3.7	11.3	99.68%
Si MOS	12.56	34	46.56	98.7%
IGBT	15.6	40.7	56.1	98.44%

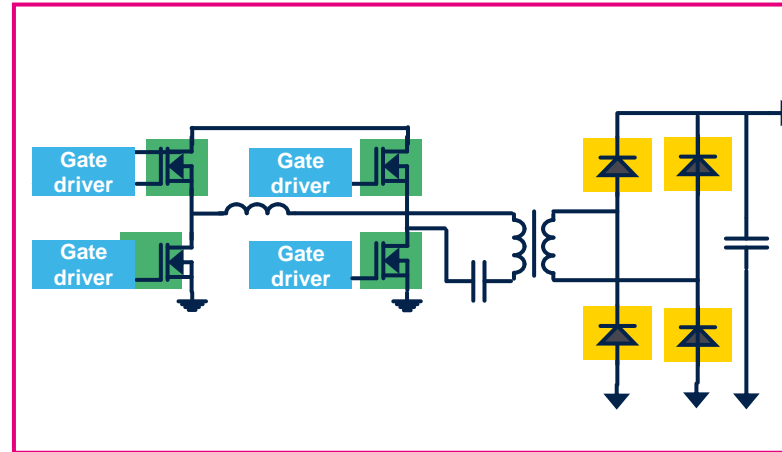
# Benefits in LLC DC/DC secondary stage

## Comparing efficiency in an 11 kW LLC DC/DC



**ST 600V 70mohm Si Mosfet \*8**

VS



**ST 1200V 62mohm SiC Mosfet \*4**

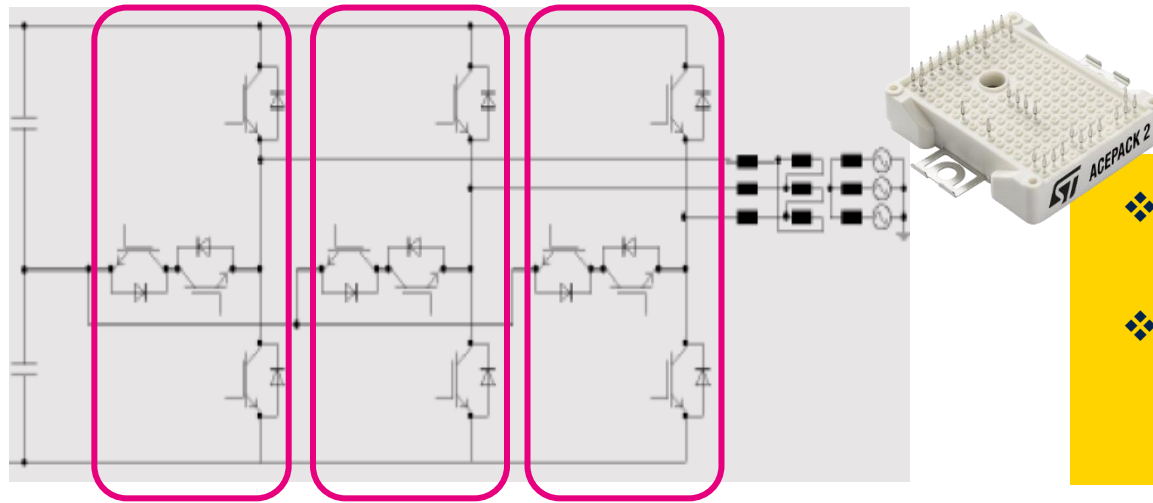
### Test conditions

- $P_{out} = 11kW$
- $I_{Mos-rms} = 12.7A$
- $V_{bus} = 800V$
- $F_{sw\_min} = 100kHz$  (at max load)

DUT	$P_{cond\_DC/DC}$ [W]	$P_{sw\_DC/DC}$ [W]	$P_{tot\_DC/DC}$ [W]	Efficiency
ST SIC MOS 1200V 70mohm	62.15	48	110.15	98.98
ST SI MOS 650V 80mohm	202.4	28	230.4	97.87

**1200V SiC MOSFET solution is more simplify and higher efficiency than 600V Si solution. 1200V Si would have much higher RDS(on) and not suite for this application.**

# Benefits in DC/AC Inverter



A2U12M12W2-F2

## Comparing efficiency in 40kW T-NPC DC/AC

- ❖ 50% power loss reduction for ST SiC module vs competitor IGBT module @19KHz
- ❖ 27% power loss reduction and same Tj level @48KHz, and higher Fsw would reduce output inductor size

### Test conditions

- Input 1000Vdc, output 380Vac
- $P_{out} = 40kW$
- $I_{c-rms} = 67A$
- $F_{sw\_min} = 19/48 kHz$
- $T_h = 85\text{ }^\circ C$
- Each module for one phase

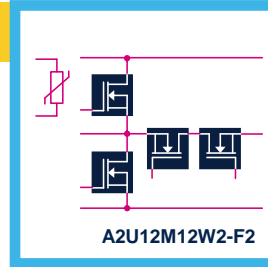
DUT	$P_{cond}$ [W]	$P_{sw}$ [W]	$P_{tot}$ [W]	Efficiency	Highest Tj, avg °C
IGBT Module 1200V/80A @19KHz	164.31	138.07	302.38	98.98	140
ST SiC Module 1200V/75A @19KHz	93.62	50.14	143.76	99.03	115
ST SiC Module 1200V/75A @48KHz	93.62	125.35	218.97	98.53	141.6



# Example of 50kW converter with SiC ACEPACK 2 power modules

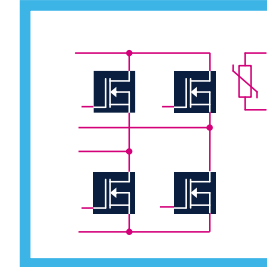
## A2U12M12W2

- SiC MOSFET
- 3 Level T-Type topology, NPC2
- 13mΩ of typical  $R_{DS(on)}$
- 1200V blocking voltage
- D2D (-F2) or S2S (-F1C) version available

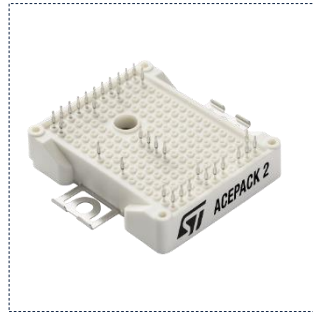


## A2F12M12W2-F1

- SiC MOSFET
- Full Bridge topology
- 13mΩ of typical  $R_{DS(on)}$
- 1200V blocking voltage



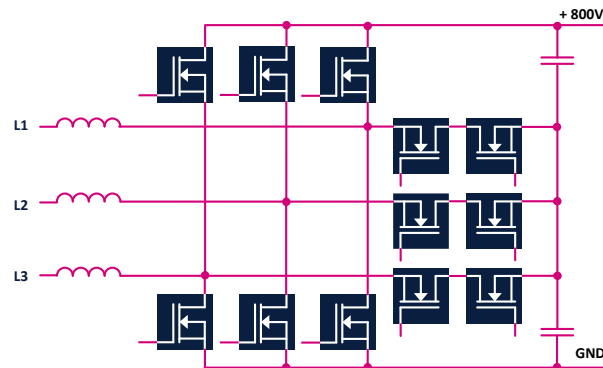
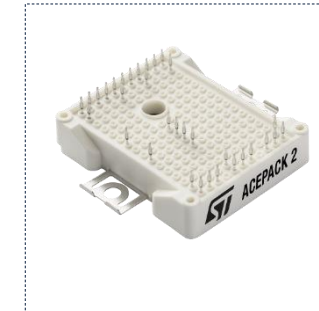
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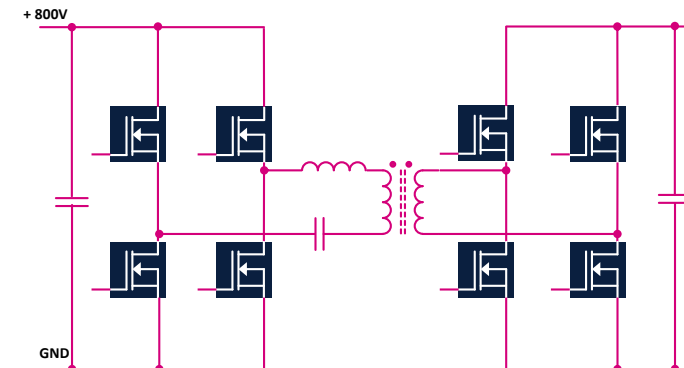
### Main applications

- DC charging station
- UPS
- Energy storage
- Solar

2 x



Active PFC stage



DC-DC isolated (DAB, LLC)

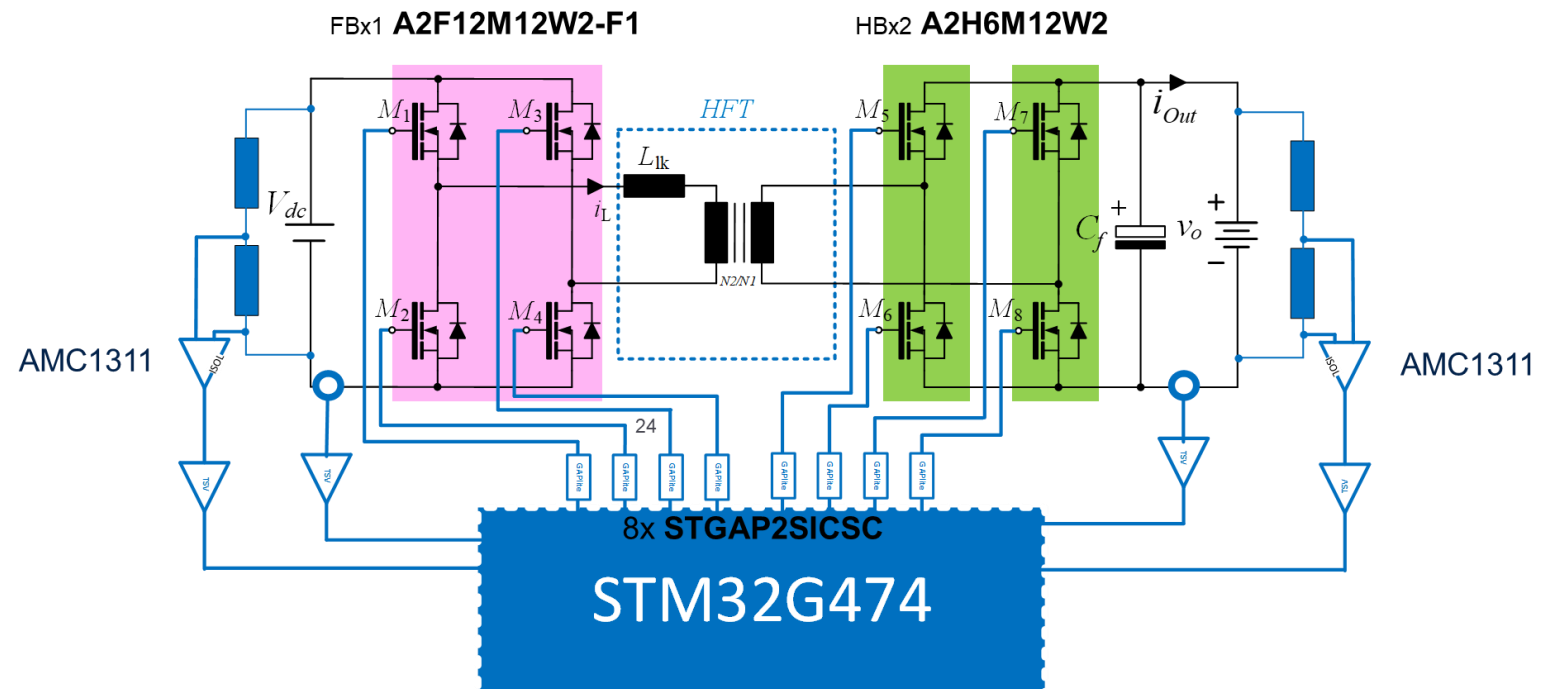
# Reference Design 25kW Bidirectional DC/DC DAB –Dual Active Bridge

## Project description

- The scope of this activity is to develop bidirectional DC/DC power platforms based on SiC based Power Modules and STM32G4.
- The selected topology is the «Dual Active Bridge – DAB».
- Main specifications:
  - $P_{out} = 25kW$
  - $V_{in} = 800V$
  - $V_{out} = \text{from } 250V \text{ up to } 650V$
  - $\eta > 98\% \text{ @ } 80\% \text{ load}$
  - Switching frequency = 100kHz

## Addressed Market

- Charging stations
- Power Supply
- Energy Storage Systems

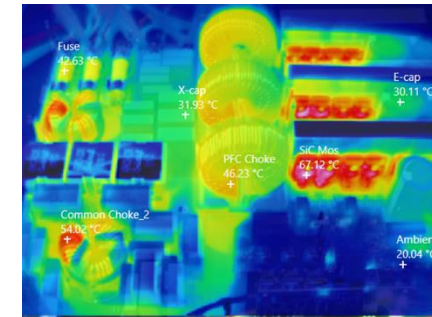
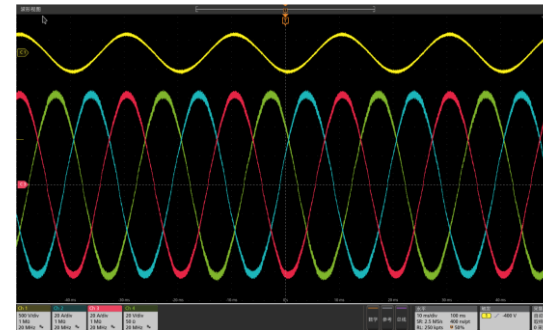
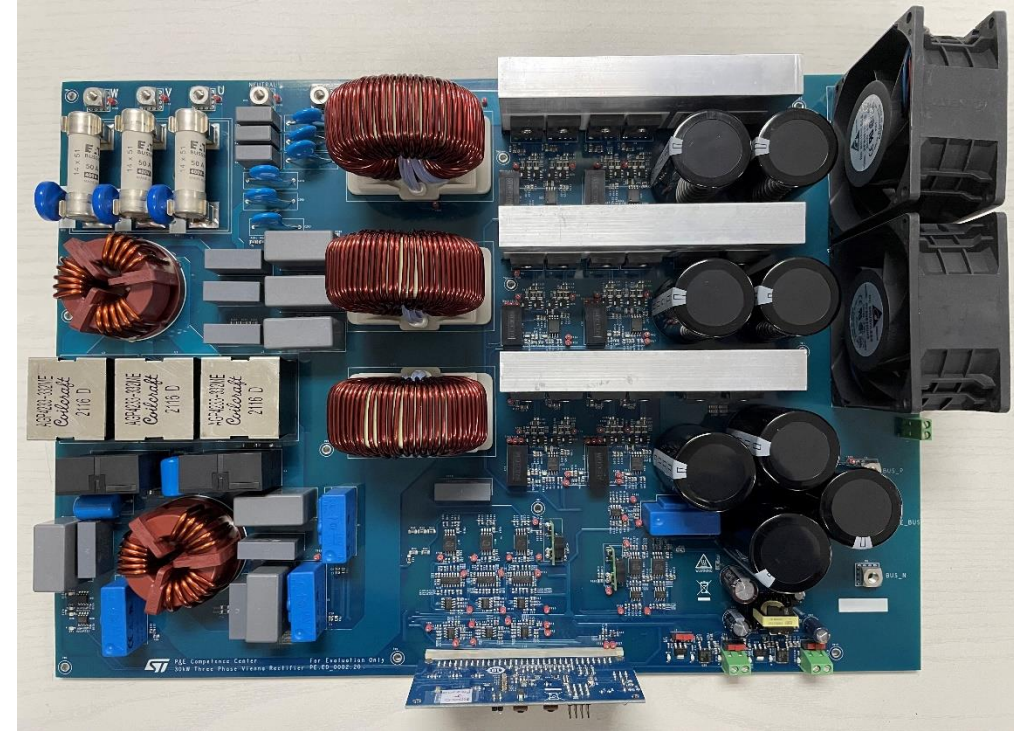
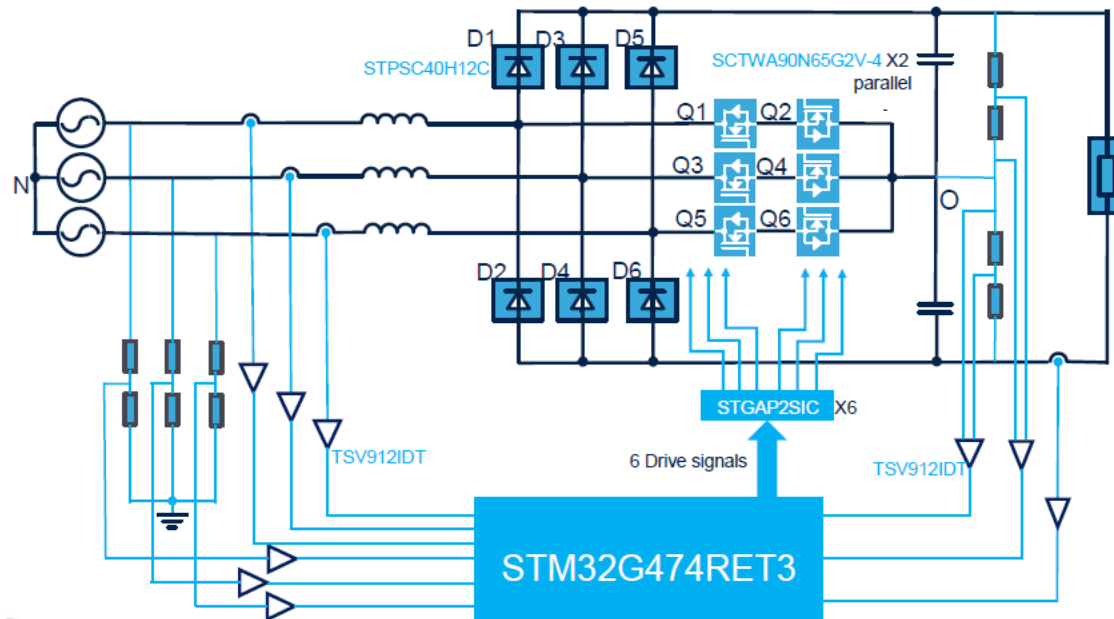




# Reference Design 30kW Three Phase Vienna Rectifier

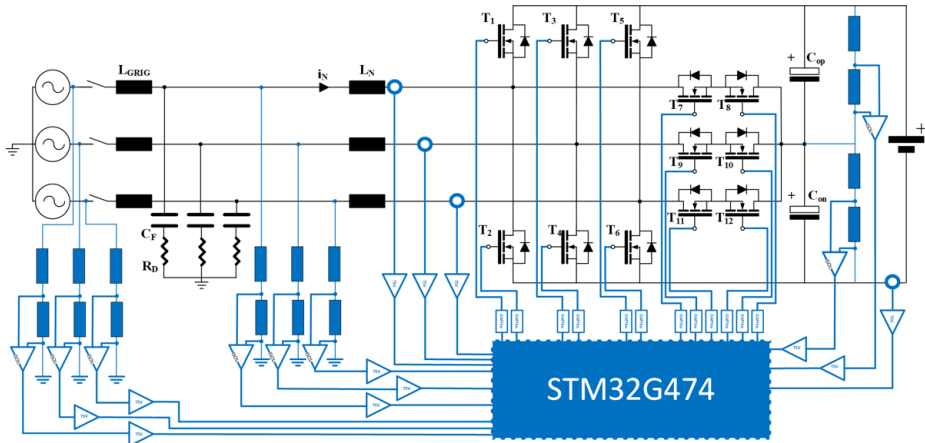
## Main Features:

- 345-460Vac input AC line voltage and 700-850Vdc output DC voltage
- SiC MOSFET and Diode based solution achieves high efficiency:  
Peak efficiency 98.56% @ 800Vdc output; 98.73% @ 700Vdc output
- Control section based on STM32G474 microcontroller
- PF > 0.99, THD < 2% @ 400Vac input; 800Vdc output; 30kW





# Reference design 15kW three phase AFE Bi-directional Converter



## Key Specs

$V_{in}$	380 Vac
$V_{out}$	800 Vdc
$P_{out}$	15 kW
$F_s$	70 kHz
$I_{ripple}$	2.5A
$V_{out\_ripple}$	10 Vpp
$PF_{@20\%}$	> 0.98
$THD_{@20\%}$	< 5%
$\eta_{@20\%}$	>97%

## Key ST Products

- **STM32G474** (32-bit MCU)
- **SCTW40N120G2V** (6x 70mΩ 1200V SiC)
- **SCTW35N65G2V** (6x 55mΩ 650V SiC)
- **STGAP2S** (Galvanic Isolated Gate Driver)
- **STPS1L30A, STPS2H100A, STTH1L06A, STPS1150A, STPS2L60A** (Schottky and Ultrafast diodes)
- **VIPer26HD** (High Voltage Converter)

- **Webpage:**
- <https://www.st.com/en/evaluation-tools/stdes-pfcbidir.html>



# Takeaways



ST is a leader in the SiC MOSFET industry through dedicated line. The **ramp up of SiC Technology** is faster than market expectation!



ST SiC technology innovation combined with the complete industrialization of **new power packages** leads to a strong product range for many power systems.



ST can offer a **broad product range**: discrete, bare dice, modules, and is investing continuously to expand capacity.



ST experts in new WBG materials and **power solutions can** replicate a new success story on GaN as already done with SiC



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