



1 Introduction

STM32F2x7xx microcontrollers features a high-quality 10/100 Mbit/s Ethernet peripheral that supports both the Media Independent Interface (MII) and Reduced Media Independent Interface (RMII) to interface with the Physical Layer (PHY).

When working with an Ethernet communication interface, a TCP/IP stack is usually used for communicating over a local or a wide area network.

This application note presents a demonstration package built on top of the lwIP (Lightweight IP) TCP/IP stack which is an open source stack intended for embedded devices.

This demonstration package contains nine applications running on top of the lwIP stack:

- Applications running in standalone mode (without an RTOS):
 - A Web server
 - A TFTP server
 - A TCP echo client application
 - A TCP echo server application
 - A UDP echo client application
 - A UDP echo server application
- Applications running with the FreeRTOS operating system:
 - A Web server based on netconn API
 - A Web server based on socket API
 - A TCP/UDP echo server application based on netconn API

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2 LwIP stack overview

2.1 Stack features

LwIP is a free TCP/IP stack developed by Adam Dunkels at the Swedish Institute of Computer Science (SICS) and licensed under a modified BSD license.

The focus of the lwIP TCP/IP implementation is to reduce RAM use while still having a full scale TCP/IP stack. This makes lwIP suitable for use in embedded systems.

LwIP comes with the following protocols:

- IPv4 and IPv6 (Internet Protocol v4 and v6)
- ICMP (Internet Control Message Protocol) for network maintenance and debugging
- IGMP (Internet Group Management Protocol) for multicast traffic management
- UDP (User Datagram Protocol)
- TCP (Transmission Control Protocol)
- DNS (Domain Name Server)
- SNMP (Simple Network Management Protocol)
- DHCP (Dynamic Host Configuration Protocol)
- PPP (Point to Point Protocol)
- ARP (Address Resolution Protocol)

LwIP has three application programming interface (API) sets:

- **Raw API** is the native API of lwIP. It enables the development of applications using event callbacks. This API provides the best performance and code size, but adds some complexity for application development.
- **Netconn API** is a high-level sequential API that requires the services of a real-time operating system (RTOS). The Netconn API enables multi-threaded operations.
- **BSD Socket API**: Berkeley-like Socket API (developed on top of the Netconn API)

The source code for the lwIP stack can be downloaded at the following link:

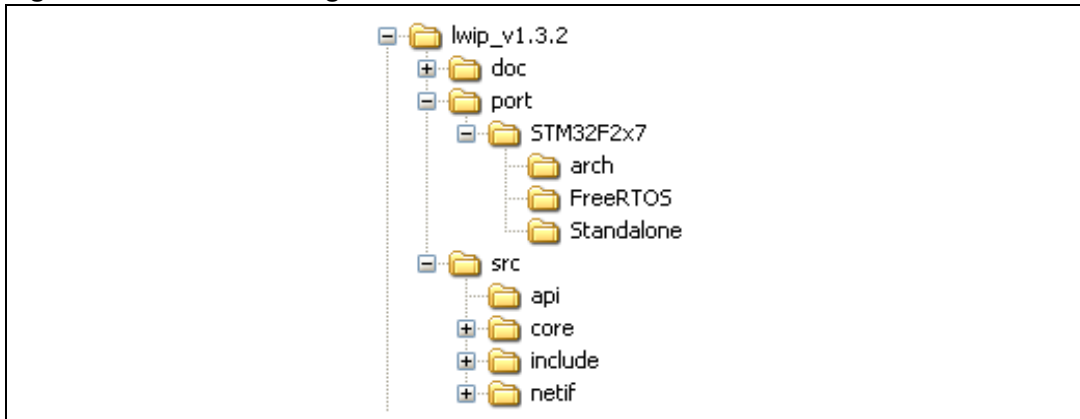
<http://savannah.nongnu.org/projects/lwip>

Note: This application note is based on lwIP v1.3.2

2.2 Folder organization of the lwIP stack

When unzipped, the lwIP stack files can be found under “\Utilities\Third_Party\lwip_v1.3.2” as shown in [Figure 1](#).

Figure 1. lwIP folder organization



- doc: documentation text files
- port/STM32F2x7: files implementing the lwIP port to STM32F2x7
 - arch: STM32 architecture port files (used data types,...)
 - FreeRTOS: lwIP port to STM32F2x7 using FreeRTOS
 - Standalone: lwIP port to STM32F2x7 in Standalone mode
- src: source files of the lwIP stack
 - api: Netconn and Socket API files
 - core: lwIP core files
 - include: lwIP include files
 - netif: Network interface files

2.3 LwIP API overview

As mentioned above, three types of APIs are offered by lwIP stack:

- RAW API
- Netconn API
- Socket API

2.3.1 Raw API

The RAW API is based on the native API of lwIP. It is used to develop callback-based applications.

When initializing the application, the user needs to register callback functions to different core events (such as, TCP_Sent, TCP_error,...). The callback functions will be called from the lwIP core layer when the corresponding event occurs.

[Table 1](#) provides a summary of the RAW API functions for TCP applications.

Table 1. TCP Raw API functions

	API function	Description
TCP connection setup	tcp_new	Creates a new TCP PCB (protocol control block).
	tcp_bind	Binds a TCP PCB to a local IP address and port.
	tcp_listen	Starts the listening process on the TCP PCB.
	tcp_accept	Assigns a callback function that will be called when a new TCP connection arrives.
	tcp_accepted	Informs the lwIP stack that an incoming TCP connection has been accepted.
	tcp_connect	Used to connect to a remote TCP host.
Sending TCP data	tcp_write	Queues up data to be sent.
	tcp_sent	Assigns a callback function that will be called when sent data is acknowledged by the remote host.
	tcp_output	Forces queued data to be sent.
Receiving TCP data	tcp_recv	Sets the callback function that will be called when new data arrives.
	tcp_recved	Must be called when the application has processed the incoming data packet (for TCP window management).
Application polling	tcp_poll	Assigns a callback functions that will be called periodically. It can be used by the application to check if there is remaining application data that needs to be sent or if there are connections that need to be closed.
Closing and aborting connections	tcp_close	Closes a TCP connection with a remote host.
	tcp_err	Assigns a callback function for handling connections aborted by the lwIP due to errors (such as memory shortage errors).
	tcp_abort	Aborts a TCP connection.

[Table 2](#) provides a summary of the RAW API functions for UDP applications.

Table 2. UDP Raw API functions

API function	Description
udp_new	Creates a new UDP PCB.
udp_remove	Removes and de-allocates a UDP PCB.
udp_bind	Binds a UDP PCB with a local IP address and port.
udp_connect	Sets up a UDP PCB remote IP address and port.
udp_disconnect	Removes a UDP PCB remote IP and port.
udp_send	Sends UDP data.
udp_recv	Specifies a callback function which is called when a datagram is received.

2.3.2 Netconn API

The netconn API is high-level sequential API which has a model of execution based on the blocking open-read-write-close paradigm.

To function correctly, this API must run in a multi-threaded operation mode where there is a separate thread for the lwIP TCP/IP stack and one or multiple threads for the application.

[Table 3](#) provides a summary of the netconn API functions.

Table 3. Netconn API functions

API function	Description
<code>netconn_new</code>	Creates a new connection.
<code>netconn_delete</code>	Deletes an existing connection.
<code>netconn_bind</code>	Binds a connection to a local IP address and port.
<code>netconn_connect</code>	Connects to a remote IP address and port.
<code>netconn_send</code>	Sends data to the currently connected remote IP/port (not applicable for TCP connections).
<code>netconn_recv</code>	Receives data from a netconn.
<code>netconn_listen</code>	Sets a TCP connection into a listening mode.
<code>netconn_accept</code>	Accepts an incoming connection on a listening TCP connection.
<code>netconn_write</code>	Sends data on a connected TCP netconn.
<code>netconn_close</code>	Closes a TCP connection without deleting it.

2.3.3 Socket API

lwIP offers the standard BSD socket API. This is a sequential API which is internally built on top of the netconn.

[Table 3](#) provides a summary of the main socket API functions.

Table 4. Socket API functions

API function	Description
<code>socket</code>	Creates an new socket.
<code>bind</code>	Binds a socket to an IP address and port.
<code>listen</code>	Listens for socket connections.
<code>connect</code>	Connects a socket to a remote host IP address and port.
<code>accept</code>	Accepts a new connection on a socket.
<code>read</code>	Reads data from a socket.
<code>write</code>	Writes data on a socket.
<code>close</code>	Closes a socket (socket is deleted).

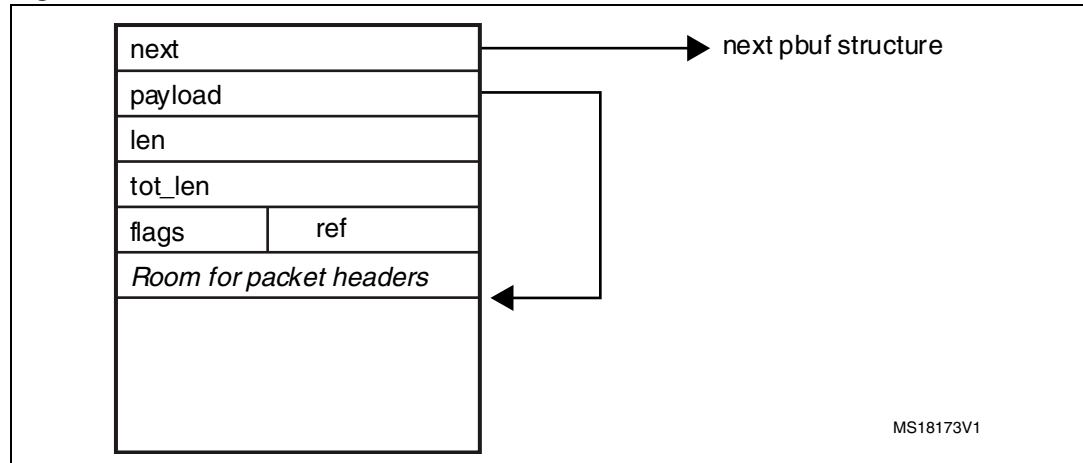
2.4 lwIP buffer management

2.4.1 Packet buffer structure

lwIP manages packet buffers using a data structure called pbuf. The pbuf structure enables both the allocation of dynamic memory to hold packet content and lets packets reside in the static memory.

Pbufs can be linked together in a chain. This enables packets to span over several pbufs.

Figure 2. Pbuf structure



- **next:** pointer to next pbuf in a pbuf chain
- **payload:** pointer to packet data payload
- **len:** length of the data content of the pbuf
- **tot_len:** the sum of pbuf len plus all the len fields of the next pbufs in the chain
- **ref** (on 4 bits) reference count: indicates the number of pointers that reference the pbuf. A pbuf can be released from memory only when its reference count is zero.
- **flags:** (on 4 bits) indicate the type of pbuf.

lwIP defines three types of pbufs, depending on the allocation type:

- **PBUF_POOL:** pbuf allocation is performed from a pool of statically pre-allocated pbufs that have a predefined size. Depending on the data size that needs to be allocated, one or multiple chained pbufs are allocated.
- **PBUF_RAM:** pbuf is dynamically allocated in memory (one contiguous chunk of memory for the full pbuf)
- **PBUF_ROM:** there is no allocation for memory space for user payload, the pbuf payload pointer points to data in the ROM memory (it can be used only for sending constant data).

For packet reception, the suitable pbuf type is PBUF_POOL, it allows to rapidly allocate memory for the received packet from the pool of pbufs. Depending on the size of the received packet, one or multiple chained pbufs are allocated. The PBUF_RAM is not suitable for packet reception because dynamic allocation takes some delay. It may also lead to memory fragmentation.

For packet transmission, depending on the data to be transmitted, the user can choose the most suitable pbuf type.

2.4.2 API for managing pbufs

Lwip has a specific API for working with pbufs. This API is implemented in the *pbuf.c* core file.

Table 5. Pbuf API functions

API function	Description
<code>pbuf_alloc</code>	Allocates a new pbuf.
<code>pbuf_realloc</code>	Resizes a pbuf (shrink size only).
<code>pbuf_ref</code>	Increments the reference count field of a pbuf.
<code>pbuf_free</code>	Decrements the pbuf reference count. If it reaches zero, the pbuf is de-allocated.
<code>pbuf_clen</code>	Returns the count number of pbufs in a pbuf chain.
<code>pbuf_cat</code>	Chains two pbufs together (but does not change the reference count of the tail pbuf chain).
<code>pbuf_chain</code>	Chains two pbufs together (tail chain reference count is incremented).
<code>pbuf_dechain</code>	Unchains the first pbuf from its succeeding pbufs in the chain.
<code>pbuf_copy_partial</code>	Copies (part of) the contents of a packet buffer to an application supplied buffer.
<code>pbuf_take</code>	Copies application supplied data into a pbuf.
<code>pbuf_coalesce</code>	Creates a single pbuf out of a queue of pbufs.

- Note: 1 “pbuf” can be a single pbuf or a chain of pbufs.
 2 When working with the Netconn API, netbufs (network buffers) are used for sending/receiving data.

A Netbuf is simply a wrapper for a pbuf structure. It can accommodate both allocated and referenced data.

A dedicated API (implemented in file *netbuf.c*) is provided for managing netbufs (allocating, freeing, chaining, extracting data,...).

2.5 Interfacing lwIP to STM32F2x7 Ethernet network interface

The port of lwIP stack to STM32F2x7 is found in folder “*/port/STM32F2x7*”.

This demonstration package provides two implementations:

- Implementation without RTOS (standalone)
- Implementation with an RTOS using FreeRTOS (<http://www.freertos.org/>)

For both implementations, the *ethernet_if.c* file is used to link the lwIP stack to the STM32F2x7 Ethernet network interface.

[Table 6](#) provides a summary of the *ethernet_if.c* functions.

Table 6. ethernet_if.c functions description

Function	Description
<code>low_level_init</code>	Calls the Ethernet driver functions to initialize the STM32F2x7 Ethernet peripheral.
<code>low_level_output</code>	Calls the Ethernet driver functions to send an Ethernet packet.
<code>low_level_input</code>	Calls the Ethernet driver functions to receive an Ethernet packet.
<code>ethernetif_init</code>	Calls <code>low_level_init</code> to initialize the Ethernet peripheral and network interface structure (<code>netif</code>).
<code>ethernet_input</code>	Calls <code>low_level_input</code> to receive a packet and provide it to the lwIP stack.

In the case of the RTOS implementation, an additional file is used (`sys_arch.c`). This file implements an emulation layer for the RTOS services (message passing through RTOS mailbox, semaphores,...). This file should be tailored according to the current RTOS, which is FreeRTOS in this package.

3 STM32F2x7 low level driver overview

The STM32F2x7 Ethernet low level driver is found in folder “\Libraries\STM32F2x7_ETH_Driver”.

The set of functions provided in the driver can be divided into the following categories:

- Global Ethernet MAC/DMA configuration/control functions
- DMA descriptors handling functions
- DMA configuration/control functions
- PHY control functions
- Power Management (PMT) functions
- MAC Management Counters (MMC) functions

3.1 Global Ethernet MAC/DMA functions

[Table 15](#) provides a summary of the Global Ethernet MAC/DMA functions used for the configuration of the media access control (MAC) and direct memory access (DMA) features.

Table 7. Global Ethernet MAC/DMA functions

Function	Description
ETH_DeInit	Resets the Ethernet peripheral.
ETH_StructInit	Fills a configuration structure for Ethernet peripheral with default config (see below).
ETH_Init	Initializes the Ethernet peripheral (MAC/DMA) registers with required configuration.
ETH_Start	Starts the Ethernet MAC/DMA operation.
ETH_MACTransmissionCmd	Enables or disables MAC transmission.
ETH_MACReceptionCmd	Enables or disables MAC reception.
ETH_GetFlowControlBusyStatus	Checks flow control Busy flag.
ETH_InitiatePauseControlFrame	Initiates a Pause frame (full-duplex only).
ETH_BackPressureActivationCmd	Enables or disables Back pressure mechanism (half duplex mode).
ETH_GetMACFlagStatus	Gets MAC flags status.
ETH_GetMACITStatus	Gets MAC interrupts status.
ETH_MACITConfig	Configures MAC interrupts.
ETH_MACAddressConfig	Configures a MAC address.
ETH_GetMACAddress	Gets configured MAC address.
ETH_MACAddressPerfectFilterCmd	Enables or disables MAC perfect filtering for a selected MAC address.
ETH_MACAddressFilterConfig	Configures the MAC address filtering mode.
ETH_MACAddressMaskBytesFilterConfig	Selects MAC address bytes on which filtering will be performed.

3.1.1 Ethernet MAC/DMA configuration parameters

The configuration structure for Ethernet MAC/DMA is of type `ETH_InitTypeDef`. This structure is composed of the following MAC and DMA configuration parameters.

Table 8. ETH_InitTypeDef structure MAC configuration parameters

Parameter	Description	Default value*
<code>ETH_AutoNegotiation</code>	Enables PHY Auto-Negotiation.	<code>ETH_AutoNegotiation_Enable</code>
<code>ETH_Watchdog</code>	Enables or disables Watchdog timer during frame reception. – When enabled, the MAC allows no more than 2048 bytes to be received. – When disabled, the MAC can receive up to 16384 bytes.	<code>ETH_Watchdog_Enable</code>
<code>ETH_Jabber</code>	– When enabled, the MAC allows no more than 2048 bytes to be sent. – When disabled, the MAC can send up to 16384 bytes.	<code>ETH_Jabber_Enable</code>
<code>ETH_InterFrameGap</code>	Selects the minimum IFG between frames during transmission.	<code>ETH_InterFrameGap_96Bit</code>
<code>ETH_CarrierSense</code>	Enables the Carrier Sense.	<code>ETH_CarrierSense_Enable</code>
<code>ETH_Speed</code>	Sets the Ethernet speed: 10/100 Mbps	<code>ETH_Speed_100M</code>
<code>ETH_ReceiveOwn</code>	Enables the ReceiveOwn. ReceiveOwn enables the reception of frames when the TX_EN signal is asserted in Half-Duplex mode.	<code>ETH_ReceiveOwn_Enable</code>
<code>ETH_LoopbackMode</code>	Enables the internal MAC MII Loopback mode.	<code>ETH_LoopbackMode_Disable</code>
<code>ETH_Mode</code>	Selects the MAC duplex mode: Half-Duplex or Full-Duplex mode	<code>ETH_Mode_FullDuplex</code>
<code>ETH_ChecksumOffload</code>	Enables the IPv4 checksum checking for received frame payloads for TCP/UDP/ICMP packets.	<code>ETH_ChecksumOffload_Disable</code>
<code>ETH_RetryTransmission</code>	Enables the MAC attempt retries transmission when a collision occurs (Half-Duplex mode).	<code>ETH_RetryTransmission_Enable</code>
<code>ETH_AutomaticPadCRCStrip</code>	Enables the Automatic MAC Pad/CRC Stripping.	<code>ETH_AutomaticPadCRCStrip_Disable</code>
<code>ETH_BackOffLimit</code>	Selects the BackOff limit value.	<code>ETH_BackOffLimit_10</code>
<code>ETH_DeferralCheck</code>	Enables the deferral check function (Half-Duplex mode).	<code>ETH_DeferralCheck_Disable</code>
<code>ETH_ReceiveAll</code>	Enables the reception of all frames by the MAC (No filtering).	<code>ETH_ReceiveAll_Disable</code>
<code>ETH_SourceAddrFilter</code>	Enables Source Address Filter mode.	<code>ETH_SourceAddrFilter_Disable</code>

Table 8. ETH_InitTypeDef structure MAC configuration parameters (continued)

Parameter	Description	Default value*
ETH_PassControlFrames	Sets the forwarding mode of the control frames (including unicast and multicast Pause frames).	ETH_PassControlFrames_BlockAll
ETH_BroadcastFramesReception	Enables the reception of Broadcast frames.	ETH_BroadcastFramesReception_Disable
ETH_DestinationAddrFilter	Sets the destination filter mode for both unicast and multicast frames.	ETH_DestinationAddrFilter_Normal
ETH_PromiscuousMode	Enables Promiscuous filtering mode.	ETH_PromiscuousMode_Disable
ETH_MulticastFramesFilter	Selects the Multicast frames filter mode: None/HashTableFilter/PerfectFilter/PerfectHashTableFilter.	ETH_MulticastFramesFilter_Perfect
ETH_UnicastFramesFilter	Selects the Unicast frames filter mode: HashTableFilter/PerfectFilter/PerfectHashTableFilter	ETH_UnicastFramesFilter_Perfect
ETH_HashTableHigh	This field holds the higher 32 bits of Hash table.	0x0
ETH_HashTableLow	This field holds the lower 32 bits of Hash table.	0x0
ETH_PauseTime	This field holds the value to be used in the Pause Time field in the transmit control frame.	0x0
ETH_ZeroQuantaPause	Enables the automatic generation of Zero-Quanta Pause control frames.	ETH_ZeroQuantaPause_Disable
ETH_PauseLowThreshold	This field configures the threshold of the Pause to be checked for automatic retransmission of Pause frame.	ETH_PauseLowThreshold_Minus4
ETH_UnicastPauseFrameDetect	Enables the MAC detection of the Pause frames (with MAC Address0 unicast address and unique multicast address).	ETH_UnicastPauseFrameDetect_Disable
ETH_ReceiveFlowControl	Enables the MAC to decode the received Pause frame and disable its transmitter for a specified time (Pause Time).	ETH_ReceiveFlowControl_Disable
ETH_TransmitFlowControl	Enables the MAC to transmit Pause frames (Full-Duplex mode) or the MAC back-pressure operation (Half-Duplex mode).	ETH_TransmitFlowControl_Disable
ETH_VLANTagComparison	Selects the 12-bit VLAN identifier or the complete 16-bit VLAN tag for comparison and filtering.	ETH_VLANTagComparison_16Bit
ETH_VLANTagIdentifier	Holds the VLAN tag identifier for receive frames.	0x0

Note: The Default Value is the value configured by calling the function `ETH_StructInit`.

Table 9. ETH_InitTypeDef Structure DMA configuration parameters

Parameter	Description	Default value
ETH_DropTCPIPChecksumErrorFrame	Enables the dropping of TCP/IP Checksum Error Frames.	ETH_DropTCPIPChecksumErrorFrame_Disable
ETH_ReceiveStoreForward	Enables the Receive store and forward mode.	ETH_ReceiveStoreForward_Enable
ETH_FlushReceivedFrame	Enables the flushing of received frames.	ETH_FlushReceivedFrame_Enable
ETH_TransmitStoreForward	Enables Transmit store and forward mode.	ETH_TransmitStoreForward_Enable
ETH_TransmitThresholdControl	Selection of the threshold level of the Transmit FIFO.	ETH_TransmitThresholdControl_64Bytes
ETH_ForwardErrorFrames	Enables the forward to the DMA of erroneous frames.	ETH_ForwardErrorFrames_Disable
ETH_ForwardUndersizedGoodFrames	Enables the Rx FIFO to forward Undersized frames (frames with no Error and length less than 64 bytes) including pad-bytes and CRC).	ETH_ForwardUndersizedGoodFrames_Disable
ETH_ReceiveThresholdControl	Selects the threshold level of the Receive FIFO.	ETH_ReceiveThresholdControl_64Bytes
ETH_SecondFrameOperate	Enables the Operate on second frame mode, which enables the DMA to process a second frame of Transmit data even before obtaining the status for the first frame.	ETH_SecondFrameOperate_Disable
ETH_AddressAlignedBeats	Enables address-aligned beats.	ETH_AddressAlignedBeats_Enable
ETH_FixedBurst	Enables AHB Master interface fixed burst transfers.	ETH_FixedBurst_Enable
ETH_RxDMABurstLength	Indicates the number of beats in a Rx DMA burst transfer.	ETH_RxDMABurstLength_32Beat
ETH_TxDMABurstLength	Indicates the number of beats in a Tx DMA burst transfer.	ETH_TxDMABurstLength_32Beat
ETH_DescriptorSkipLength	Specifies the number of words to skip between two unchained descriptors (Ring mode).	0x0
ETH_DMAArbitration	Selects the DMA Tx/Rx arbitration.	ETH_DMAArbitration_RoundRobin_RxTx_1_1

3.2 DMA descriptor handling

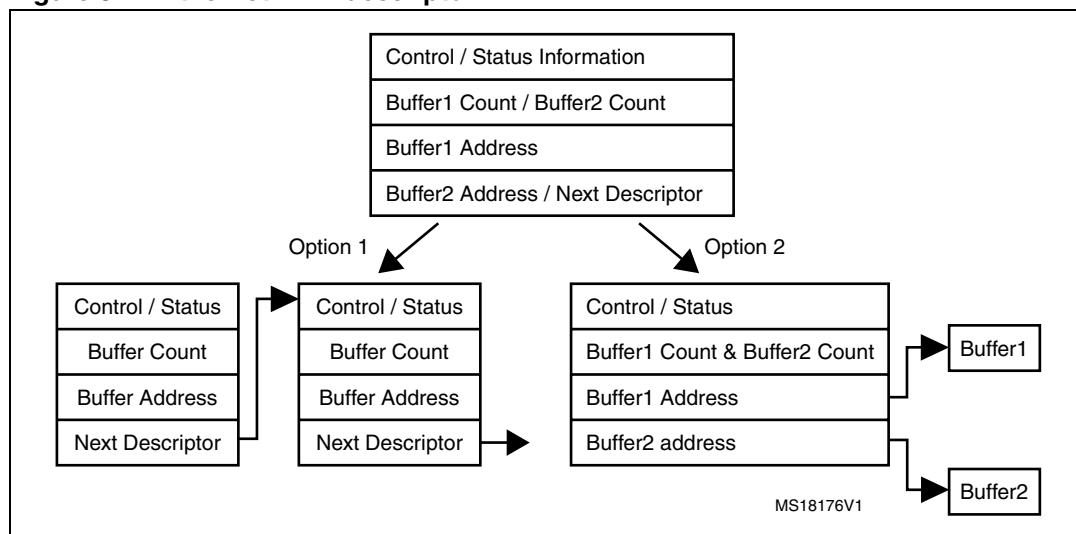
3.2.1 DMA descriptors

Transfer of Ethernet packets between Transmit/Receive FIFOs and memory is by performed by direct memory access (DMA) using transfer descriptors.

Figure 3 illustrates the format of an Ethernet DMA descriptor.

Note: The following description does not apply to enhanced DMA descriptors.

Figure 3. Ethernet DMA descriptor

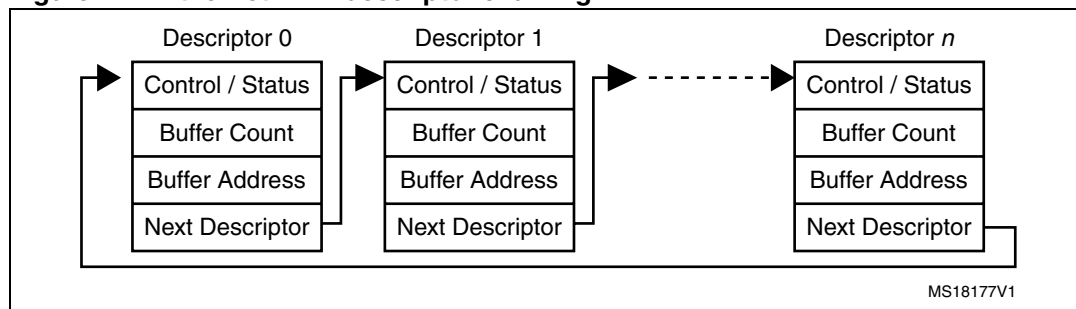


As shown in *Figure 3*, the DMA descriptor can have two formats:

- Descriptor points to one data buffer only and the Next Descriptor field points on next DMA descriptor for allowing descriptors chaining mechanism
- Descriptor can point to two data buffers Buffer1 and Buffer2

In the STM32F2x7 Ethernet driver, the selected DMA descriptor format is the one allowing descriptor chaining as shown in *Figure 4*.

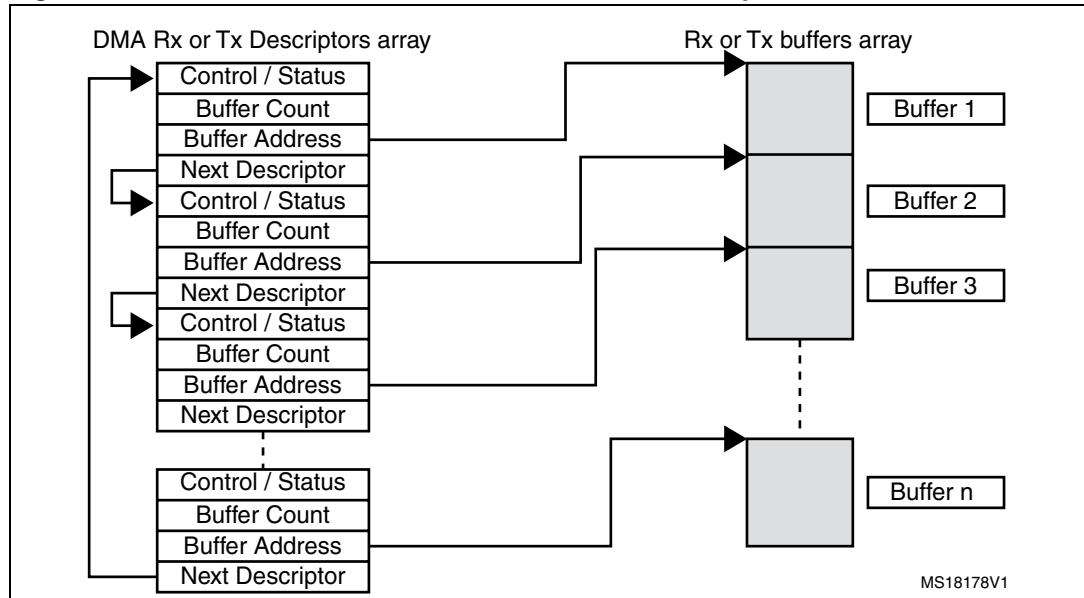
Figure 4. Ethernet DMA descriptor chaining



- Note:*
- 1 An Ethernet packet can span over one or multiple DMA descriptors.
 - 2 One DMA descriptor can be used for one Ethernet packet only.
 - 3 The last descriptor in the chain points to the first descriptor for forming a ring of descriptors.

Figure 5 illustrates the buffer and descriptor allocation model in memory for the STM32F2x7 Ethernet driver.

Figure 5. STM32F2x7 Ethernet driver buffers and descriptors



As shown in Figure 5, the following arrays are statically allocated in the STM32F2x7 Ethernet driver:

- Two arrays for the DMA descriptors, one for DMA Rx and another for DMA Tx:

```
ETH_DMADESCTypeDef  DMARxDscrTab[ETH_RXBUFNB] ,
DMATxDscrTab[ETH_TXBUFNB] ;
```

- Two arrays of driver buffers, one array for receive buffers and another array for transmit buffers:

```
uint8_t Rx_Buff[ETH_RXBUFNB][ETH_RX_BUF_SIZE] ;
uint8_t Tx_Buff[ETH_TXBUFNB][ETH_TX_BUF_SIZE] ;
```

where:

- ETH_RXBUFNB: number of driver receive buffers
- ETH_TXBUFNB: number of driver transmit buffers
- ETH_RX_BUF_SIZE: size in bytes of a receive buffer
- ETH_TX_BUF_SIZE: size in bytes of a transmit buffer

The default values for these parameters as defined in file *STM32F2x7_eth.h* are:

- ETH_RXBUFNB = 4
- ETH_TXBUFNB = 2
- ETH_RX_BUF_SIZE = 1524 (max size of Ethernet packet (1522) + 2 bytes for 32-bit alignment)
- ETH_TX_BUF_SIZE = 1524 (max size of Ethernet packet(1522) + 2 bytes for 32-bit alignment)

The above parameters values can be changed depending on user particular application needs. This can be done by enabling the define CUSTOM_DRIVER_BUFFERS_CONFIG and writing custom values in configuration file *stm32f2x7_eth_conf.h*

Note: The Ethernet driver allows to have buffer size (`ETH_RX_BUF_SIZE` or `ETH_TX_BUF_SIZE`) less than one maximum Ethernet packet size (1522 bytes). Ethernet packets (received or to be transmitted) exceeding the configured buffer size will span over multiple buffers (or descriptors).

3.2.2 DMA descriptor handling functions

[Table 10](#) provides a summary of the main driver functions used for handling DMA descriptors.

Table 10. DMA descriptor functions

Function	Description
<code>ETH_DMARxDescChainInit</code>	Initializes DMA Rx Descriptor chain (as shown in Figure 5).
<code>ETH_DMATxDescChainInit</code>	Initializes DMA Tx Descriptor chain (as shown in Figure 5).
<code>ETH_CheckFrameReceived</code>	Checks if frame is received (polling method on OWN bit and other flags of DMA RX descriptor).
<code>ETH_Get_Received_Frame</code>	Gets received frame (when using polling method).
<code>ETH_Get_Received_Frame_interrupt</code>	Gets received frame (when using interrupt method for detecting received packet).
<code>ETH_Prepare_Transmit_Descriptors</code>	Prepares DMA TX descriptors for transmitting a packet (data should already be copied in driver buffers).
<code>ETH_GetRxPktSize</code>	Gets size of received packet.
<code>ETH_GetDMATxDescFlagStatus</code>	Gets flag status of a DMA TX descriptor.
<code>ETH_GetDMARxDescFlagStatus</code>	Gets flag status of a DMA RX descriptor.
<code>ETH_DMATxDescTransmitITConfig</code>	Configures Interrupts for a DMA TX descriptor.
<code>ETH_DMARxDescReceiveITConfig</code>	Configures Interrupts for a DMA RX descriptor.
<code>ETH_EnhancedDescriptorCmd</code> ⁽¹⁾	Enables or disables the Enhanced descriptor structure.
<code>ETH_DMATxDescChecksumInsertionConfig</code>	Enables or disables TCP/UDP/ICMP checksum insertion for transmitted packet.
<code>ETH_DMATxDescCRCCmd</code>	Enables or disables CRC generation for transmitted packet.
<code>ETH_DMATxDescShortFramePaddingCmd</code>	Enables or disables adding padding to short frame to be transmitted.

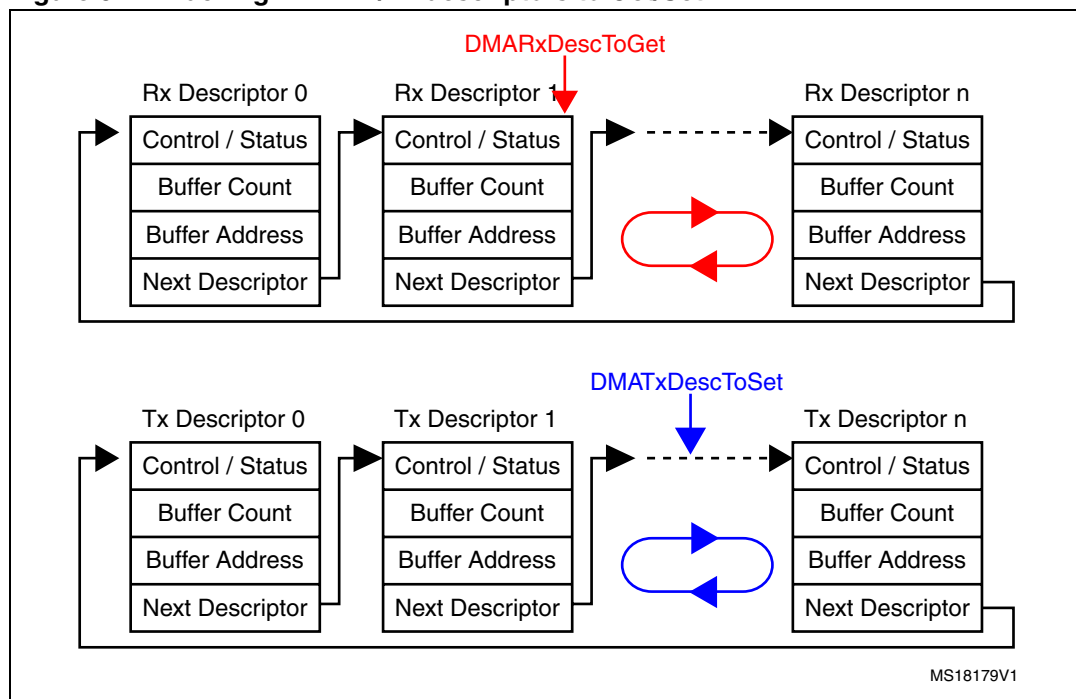
- Enhanced descriptors must be used if IPv4 checksum offload is activated. The enhanced descriptor format is enabled in one of two ways: 1) uncomment the define `USE_ENHANCED_DMA_DESCRIPTOR`s in `stm32f2x7_eth_conf.h` file; or, 2) by calling the `ETH_EnhancedDescriptorCmd()` function.

Note: The Ethernet driver maintains two global pointers for tracking the Rx/Tx DMA descriptor for next packet to be received or to be transmitted:

```

__IO ETH_DMADESCTypeDef *DMATxDescToSet;
__IO ETH_DMADESCTypeDef *DMARxDescToGet;
    
```

Figure 6. Tracking DMA Rx/Tx descriptors to Get/Set



3.3 PHY control functions

Table 11 provides a summary of the functions implemented for PHY control by the STM32F2x7 Ethernet driver.

Note: The PHY configuration options (Reset delay, Configuration delay, Status register Speed and Duplex mask values) are defined in the stm32f2x7_eth_conf.h configuration file. These values change from a PHY to another, so the user has to update this value depending on the used external PHY.

Table 11. PHY control functions

Function	Description
ETH_ReadPHYRegister	Reads a PHY register.
ETH_WritePHYRegister	Writes a data into a PHY register.
ETH_PHYLoopBackCmd	Enables or disables PHY loopback mode.

The PHY is mainly accessed during initialization time (by driver function *ETH_Init*) for:

- Resetting the PHY.
- Enabling PHY auto-negotiation mode or manually selecting the mode of operation (Full-speed/Low-speed, Half-duplex/Full-duplex).
- If PHY auto-negotiation mode is selected, the application needs to poll the PHY or use a PHY interrupt in order to obtain the result of auto-negotiation (speed, duplex mode).

3.4 Hardware checksum

The STM32F2x7 Ethernet controller has an embedded hardware checksum accelerator to off-load the CPU from generating, inserting and verifying the checksums of the IP, UDP, TCP and ICMP protocols.

The checksum for TCP, UDP or ICMP is calculated over a complete frame, and then inserted into its corresponding header field. Due to this requirement, this function is enabled only when the Transmit FIFO is configured for Store-and-Forward mode.

Note: By default, the hardware checksum is enabled. To disable this feature, “comment” the specific *CHECKSUM_BY_HARDWARE* define in the *lwipopts.h* file under the project *\inc* folder.

4 Developing applications with lwIP stack

4.1 Developing in standalone mode using the Raw API

4.1.1 Model of operation

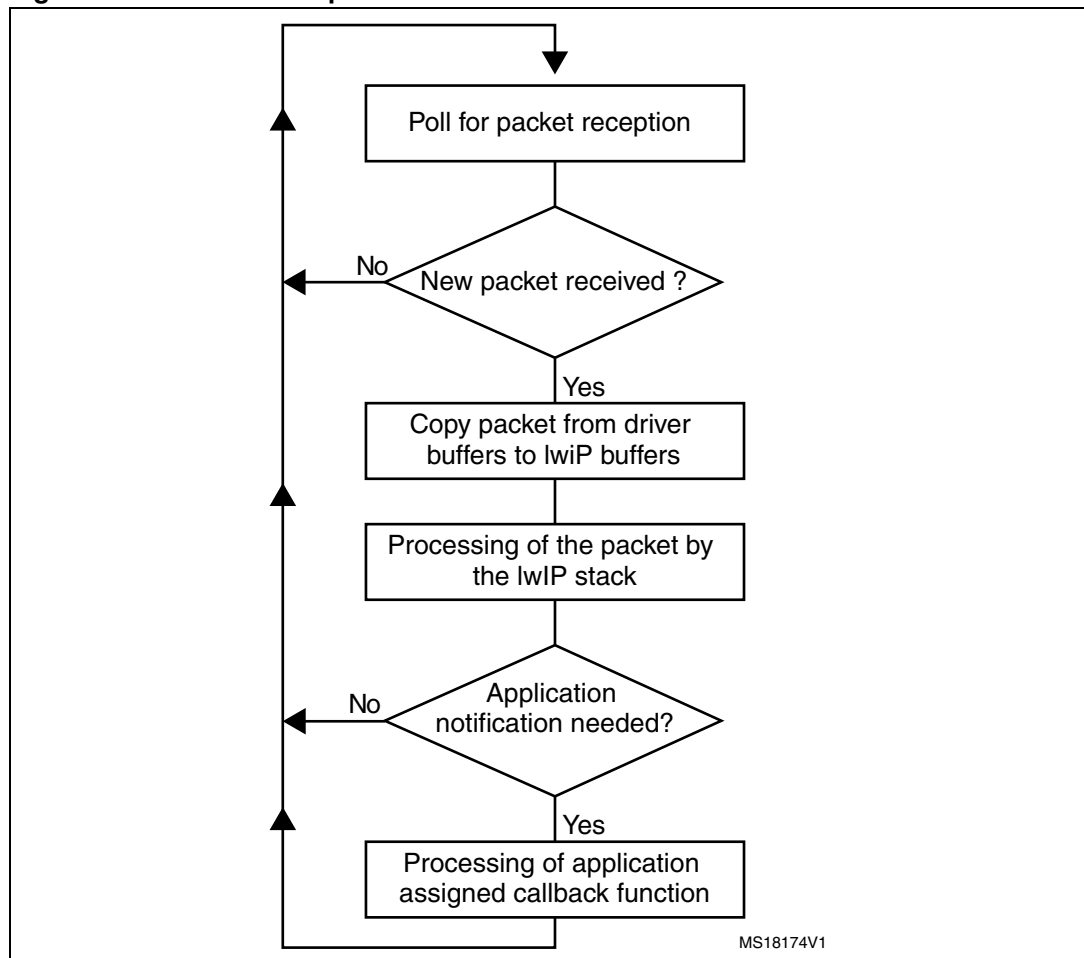
In standalone mode, the model of operation is based on continuous software polling to check if a packet is received.

When a packet is received, it is first copied from the Ethernet driver buffers into the lwIP buffers. In order to copy the packet as fast as possible, the lwIP buffers (pbufs) should be allocated from the pool of buffers (PBUF_POOL).

After the packet is copied, it is handed to the lwIP stack for processing. Depending on the received packet, the stack may or may not notify the application layer.

lwIP communicates with the application layer using event callback functions. These functions should be assigned before starting the communication process.

Figure 7. Standalone operation model



For TCP applications, the following common callback functions must be assigned:

- callback for incoming TCP connection event, assigned by API call *TCP_accept*
- callback for incoming TCP data packet event, assigned by API call *TCP_recev*
- callback for signalling successful data transmission, assigned by API call *TCP_sent*
- callback for signalling TCP error (after a TCP abort event), assigned by API call *TCP_err*
- Periodic callback (every 1 or 2 s) for polling the application, assigned by API call *TCP_poll*

4.1.2 Example of the TCP echo server demo

The TCP echo server example provided in folder “\Project\Standalone\tcp_echo_server” is a simple application that implements a TCP server which echoes any received TCP data packets coming from remote client.

To test the demo, use the PC client utility “echotool.exe”. This utility is found in folder “\Utilities\Third_Party\PC_Software”. (Refer to [Section 6.1.3: TCP echo server demo](#) for more details about testing the demo).

The following example provides a description about the firmware structure. It is an extract from the *main.c* file.

```
int main(void)
{
    ...

    /* configure Ethernet (GPIOs, clocks, MAC, DMA) */
    ETH_BSP_Config();

    /* Initilaize the LwIP stack */
    LwIP_Init();

    /* tcp echo server Init */
    tcp_echoserver_init();

    /* Infinite loop */
    while (1)
    {
        /* check if any packet received */
        if (ETH_CheckFrameReceived())
        {
            /* process received Ethernet packet */
            LwIP_Pkt_Handle();
        }
        /* handle periodic timers for LwIP */
        LwIP_Periodic_Handle(LocalTime);
    }
}
```

Example description:

- Function *ETH_BSP_Config* is called for initializing the Ethernet peripheral (GPIOs, Clocks, MAC and DMA options).
- Function *LwIP_Init* is called for initializing the lwIP stack internal structures and for starting stack operations.
- Function *tcp_echo_server_init* is called for initializing the TCP echo server application (see below).
- In the infinite *while* loop, software polls for packet reception using Ethernet driver function *ETH_CheckFrameReceived*. When a packet is received it should be handled by the lwIP stack using function *LwIP_Pkt_Handle*.
- Function *LwIP_Periodic_Handle* is called in order to handle certain LwIP internal periodic tasks (protocol timers, retransmission of TCP packets,...).

Function *tcp_echo_server_init* has the following code:

```
void tcp_echo_server_init(void)
{
    /* create new tcp pcb */
    tcp_echo_server_pcb = tcp_new();

    if (tcp_echo_server_pcb != NULL)
    {
        err_t err;

        /* bind echo_pcb to port 7 (ECHO protocol) */
        err = tcp_bind(tcp_echo_server_pcb, IP_ADDR_ANY, 7);

        if (err == ERR_OK)
        {
            /* start tcp listening for echo_pcb */
            tcp_echo_server_pcb = tcp_listen(tcp_echo_server_pcb);

            /* initialize LwIP tcp_accept callback function */
            tcp_accept(tcp_echo_server_pcb, tcp_echo_server_accept);
        }
        else
        {
            printf("Can not bind pcb\n");
        }
    }
    else
    {
        printf("Can not create new pcb\n");
    }
}
```


Example description:

- lwIP API calls *tcp_new* for allocating a new TCP protocol control block (PCB) (*tcp_echo_server_pcb*).
- The allocated TCP PCB is bound to a local IP address and port using function *tcp_bind*.
- After binding the TCP PCB, function *tcp_listen* is called in order to start TCP listening process on the TCP PCB.
- Finally a callback function *tcp_echo_server_accept* should be assigned for handling incoming TCP connections on the TCP PCB. This is done using lwIP API function *tcp_accept*.
- Starting from this point, the TCP server is ready to accept any incoming connection from remote clients.

The following example shows how incoming TCP connections are handled by the user callback function *tcp_echo_server_accept*. This is an extract from this function.

```
static err_t tcp_echo_server_accept(void *arg, struct tcp_pcb
*newpcb, err_t err)
{
    ...

    /* allocate structure es to maintain tcp connection infos */
    es = (struct tcp_echo_server_struct *)mem_malloc(sizeof(struct
tcp_echo_server_struct));
    if (es != NULL)
    {
        es->state = ES_ACCEPTED;
        es->pcb = newpcb;
        es->p = NULL;

        /* pass newly allocated es structure as argument to newpcb */
        tcp_arg(newpcb, es);

        /* initialize lwip tcp_recv callback function for newpcb */
        tcp_recv(newpcb, tcp_echo_server_recv);

        /* initialize lwip tcp_err callback function for newpcb */
        tcp_err(newpcb, tcp_echo_server_error);

        /* initialize lwip tcp_poll callback function for newpcb */
        tcp_poll(newpcb, tcp_echo_server_poll, 1);

        ret_err = ERR_OK;
        ...
    }
}
```

Example description:

- The new TCP connection is passed to *tcp_echoserver_accept* callback function through the parameter “newpcb”.
- A structure “es” is used to maintain application status. It is passed as an argument to the connection TCP PCB “newpcb” by calling lwIP API *tcp_arg*.
- A TCP receive callback function *tcp_echoserver_recv* is assigned by calling lwIP API *tcp_recv*. This callback will handle all the data traffic with the remote client.
- A TCP error callback function *tcp_echoserver_error* is assigned by calling lwIP API *tcp_err*, this callback will handle TCP errors.
- A TCP poll callback function *tcp_echoserver_poll* is assigned by calling lwIP API *tcp_poll* for handling periodic application tasks (such as checking if application data remains to be transmitted).

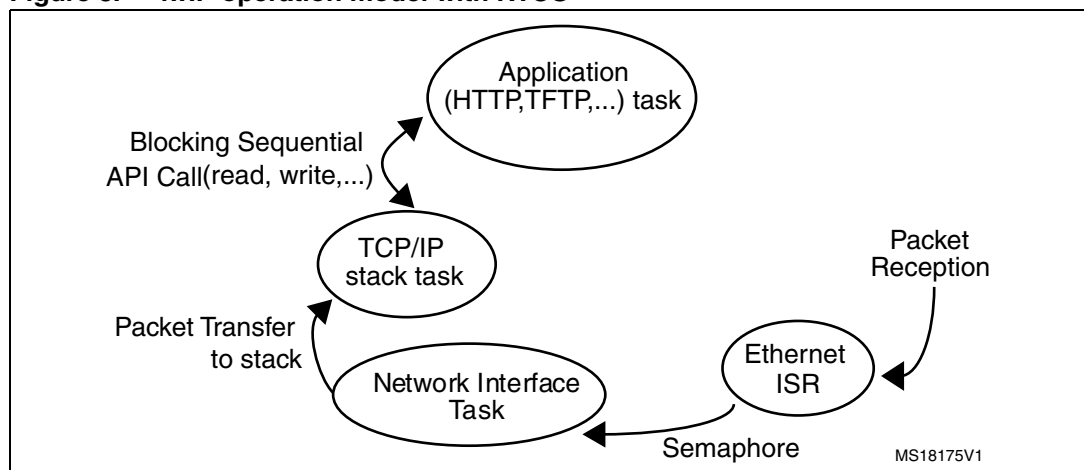
4.2 Developing with an RTOS using Netconn or Socket API

4.2.1 Model of operation

The model of operation when working with an RTOS has the following characteristics:

- The TCP/IP stack and the application run in separate tasks.
- The application communicates with the stack through sequential API calls that use the RTOS mailbox mechanism for inter-process communication. The API calls are blocking calls. This means that the application task will be blocked until a response is received from the stack.
- An additional task which is “the network interface task” is used for getting any received packets from driver buffers and providing them to the TCP/IP stack using the RTOS mailbox. This task is informed of a packet reception using the Ethernet receive interrupt service routine.

Figure 8. lwIP operation model with RTOS



4.2.2 Example of a TCP echoserver demo using the Netconn API

From the application point of view, the Netconn API offers a simpler way for developing TCP/IP applications than the raw API. This is because it has a more intuitive sequential API.

The following example shows a TCP echoserver demo developed with the Netconn API. It is an extract of the *main.c* file.

```
int main(void)
{
    ...

    /* configure Ethernet (GPIOs, clocks, MAC, DMA) */
    ETH_BSP_Config();

    /* Initilaize the LwIP stack */
    LwIP_Init();

    /* Initialize tcp echo server */
    tcpecho_init();

    ...

    /* Start scheduler */
    vTaskStartScheduler();

    /* We should never get here as control is now taken by the
    scheduler */
    for( ;; );
}
```

Example description:

- Function *LwIP_Init* initializes the lwIP stack. This includes the creation of the lwIP TCP/IP stack task.
- The TCP echo server task “tcpecho_thread” is created in function *tcpecho_init*.

```
void tcpecho_init(void)
{
    sys_thread_new("tcpecho_thread", tcpecho_thread, NULL, \
    DEFAULT_THREAD_STACKSIZE, TCPECHO_THREAD_PRIO);
}
```

The TCP echo server thread has the following code:

```
static void tcpecho_thread(void *arg)
{
    struct netconn *conn, *newconn;
    err_t err;

    LWIP_UNUSED_ARG(arg);

    /* Create a new connection identifier. */
    conn = netconn_new(NETCONN_TCP);
```

```
if (conn!=NULL)
{
    /* Bind connection to well known port number 7. */
    err = netconn_bind(conn, NULL, 7);

    if (err == ERR_OK)
    {
        /* Tell connection to go into listening mode. */
        netconn_listen(conn);

        while (1)
        {
            /* Grab new connection. */
            newconn = netconn_accept(conn);

            /* Process the new connection. */
            if (newconn)
            {
                struct netbuf *buf;
                void *data;
                u16_t len;

                while ((buf = netconn_recv(newconn)) != NULL)
                {
                    do
                    {
                        netbuf_data(buf, &data, &len);
                        netconn_write(newconn, data, len, NETCONN_COPY);
                    }
                    while (netbuf_next(buf) >= 0);

                    netbuf_delete(buf);
                }

                /* Close connection and discard connection identifier. */
                netconn_close(newconn);
                netconn_delete(newconn);
            }
        }
    }
    else
    {
        printf(" can not bind TCP netconn");
    }
}
else
{
    printf("can not create TCP netconn");
}
}
```

Example description:

- API function *Netconn_new* called with parameter `NETCONN_TCP` will create a new TCP connection.
- The newly created connection is then bound to port 7 (echo protocol) by calling the API function *Netconn_bind*.
- After binding connection, the application starts the listening process on the connection by calling the API function *Netconn_listen*.
- In the infinite *while(1)* loop, the application waits for a new connection by calling the API function *Netconn_accept*. This API call will block the application task when there is no incoming connection.
- When there is an incoming connection, the application can start receiving data by calling the API function *netconn_recv*. Incoming data are received in a netbuf.
- Application can get the received data by calling the Netbuf API function *netbuf_data*.
- The received data is sent back (echoed) to the remote TCP client by calling the API function *Netconn_write*.
- *Netconn_close* and *Netconn_delete* are used to respectively close and delete the Netconn connection.

4.3 lwIP memory configuration options

lwIP has several memory configurations options. These options allow the user to tune the allocated RAM memory usage depending on performance needs and also application memory constraints.

The user options for lwIP are changed in file *lwipopt.h*

[Table 12](#) provides a summary of the main options for RAM memory use.

Table 12. lwIP memory configuration options

lwIP memory option	Definition
<code>MEM_SIZE</code>	lwIP heap memory size: used for all lwIP dynamic memory allocations.
<code>MEMP_NUM_PBUF</code>	Total number of <code>MEM_REF</code> and <code>MEM_ROM</code> pbufs.
<code>MEMP_NUM_UDP_PCB</code>	Total number of UDP PCB structures.
<code>MEMP_NUM_TCP_PCB</code>	Total number of TCP PCB structures.
<code>MEMP_NUM_TCP_PCB_LISTEN</code>	Total number of listening TCP PCBs.
<code>MEMP_NUM_TCP_SEG</code>	The maximum number of simultaneously queued TCP segments.
<code>PBUF_POOL_SIZE</code>	The total number of pbufs of type <code>PBUF_POOL</code> .
<code>PBUF_POOL_BUFSIZE</code>	Size of a pbuf of type <code>PBUF_POOL</code> .
<code>TCP_MSS</code>	TCP maximum segment size.
<code>TCP_SND_BUF</code>	TCP send buffer space for a connection.
<code>TCP_SND_QUEUELEN</code>	Maximum number of pbufs in the TCP send queue.
<code>TCP_WND</code>	Advertised TCP receive window size.

As shown in [Table 12](#), lwIP memory has two main types:

- Heap memory for all dynamic allocations defined by `MEM_SIZE`.
- Pool memory for static pool structures defined by `MEMP_NUM_XX` and the `PBUF_POOL_XX`.

The allocation from these two types of memory will define the total size of memory allocated for lwIP. Below are some recommendations when setting these options:

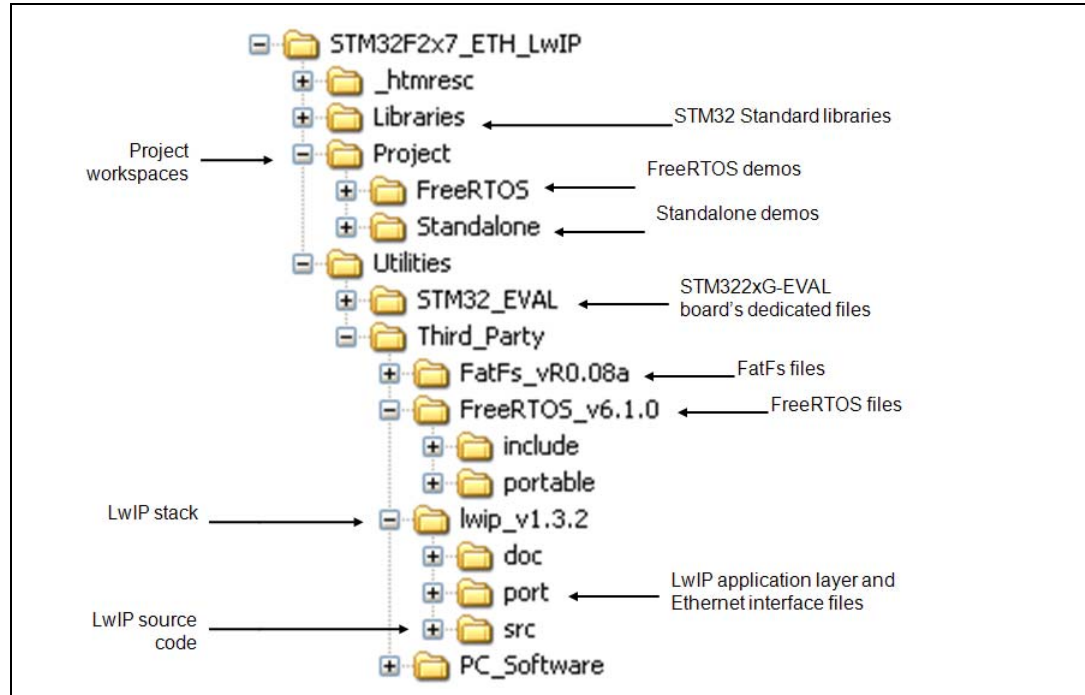
- `MEM_SIZE` should be set high when the application needs to send a lot of data that needs to be copied from application buffers to the lwip send buffer.
- `PBUF_POOL_BUFSIZE` should be set according to the average size of packets to be received.
- `PBUF_POOL_SIZE` should be tuned as high as possible in order to achieve the best receive data rate.
- `TCP_SND_BUF` limits the sender buffer space (data queued to be transmitted). For optimal performance, this parameter should be equal to the TCP window size of the remote host. Keep in mind that every active connection might buffer this amount of data, so make sure you have enough RAM (defined by `MEM_SIZE`) or limit the number of concurrently active connections.
- `TCP_WND` is the advertised receive window and should be tuned as high as possible in order to achieve the best performance.

5 Description of the demonstration package

5.1 Package directories

When unzipped, the package has the structure shown in [Figure 9](#).

Figure 9. Demonstration package structure



The demonstration package contains nine applications running on top of the lwIP stack.

- Standalone demos:
 - A Web server
 - A TFTP server
 - A TCP echo client application
 - A TCP echo server application
 - A UDP echo client application
 - A UDP echo server application
- FreeRTOS demos:
 - A Web server based on the netconn API
 - A Web server based on the socket API
 - A TCP/UDP echo server application based on the netconn API

5.2 Demonstration settings

5.2.1 PHY interface configuration

The demonstration firmware is used to interface the PHY with the two modes: MII and RMII. To select the required PHY interface mode, open the *main.h* file and choose one of the two defines:

```
#define MII_MODE
#define RMII_MODE
```

For MII mode, the PHY clock can be taken from the external crystal or from the STM32 via the MCO pin if both `MII_MODE` and `PHY_CLOCK_MCO` are defined in the *main.h* file.

Note: In RMII mode, it is not possible to use MCO to output the 50 MHz clock to PHY due to the PLL limitation explained in chapter 2.6.5 of STM32F20x & STM32F21x Errata sheet (ES0005). In such a case, it is possible to provide the 50 MHz clock by soldering a 50 MHz oscillator (ref SM7745HEV-50.0M or equivalent) on the U3 footprint located under CN3 and also by removing the jumper on JP5. This oscillator is not provided with the board. For more details, please refer to STM3220G-EVAL evaluation board User manual UM1057.

5.2.2 MAC and IP address settings

The default MAC address is set to: 00:00:00:00:00:02. To change this address, modify the six bytes defined in the *main.h* file.

The IP address can be set either as a static address, equal to 192.168.0.10, or as a dynamic address, assigned by a DHCP server.

The IP address configuration mode is selected in the *main.h* file:

- Uncomment `#define USE_DHCP` to configure the IP address by DHCP
- Comment `#define USE_DHCP` to use the static address (192.168.0.10)

Note: If you choose to configure the IP address by DHCP and the application does not find a DHCP server on the network to which it is already connected, the IP address is then automatically set to the static address (192.168.0.10).

5.2.3 STM322xG-EVAL settings

In order to run the software, configure the STM322xG-EVAL board as shown in [Table 13](#).

Note: Throughout this document, “STM322xG-EVAL board” refers to STM3220G-EVAL and STM3221G-EVAL boards.

Table 13. STM322xG-EVAL jumper configurations

Jumper	MII mode configuration	RMII mode configuration
JP5	1-2: 25 MHz clock driven by external crystal 2-3: 25 MHz clock driven by MCO at PA8	Not fitted
JP6	2-3: MII interface mode is enabled	1-2: RMII interface mode is enabled
JP8	Open: MII interface mode is selected	Closed: RMII interface mode is selected

6 Using the demos

The STM32F2x7 lwIP package comes with several demos that use the different API sets.

The examples come in two folders:

- Standalone: single-threaded application examples using the Raw API
- FreeRTOS: multi-threaded application using FreeRTOS with the Netconn or Socket API

6.1 Standalone demos

6.1.1 Httpserver demo

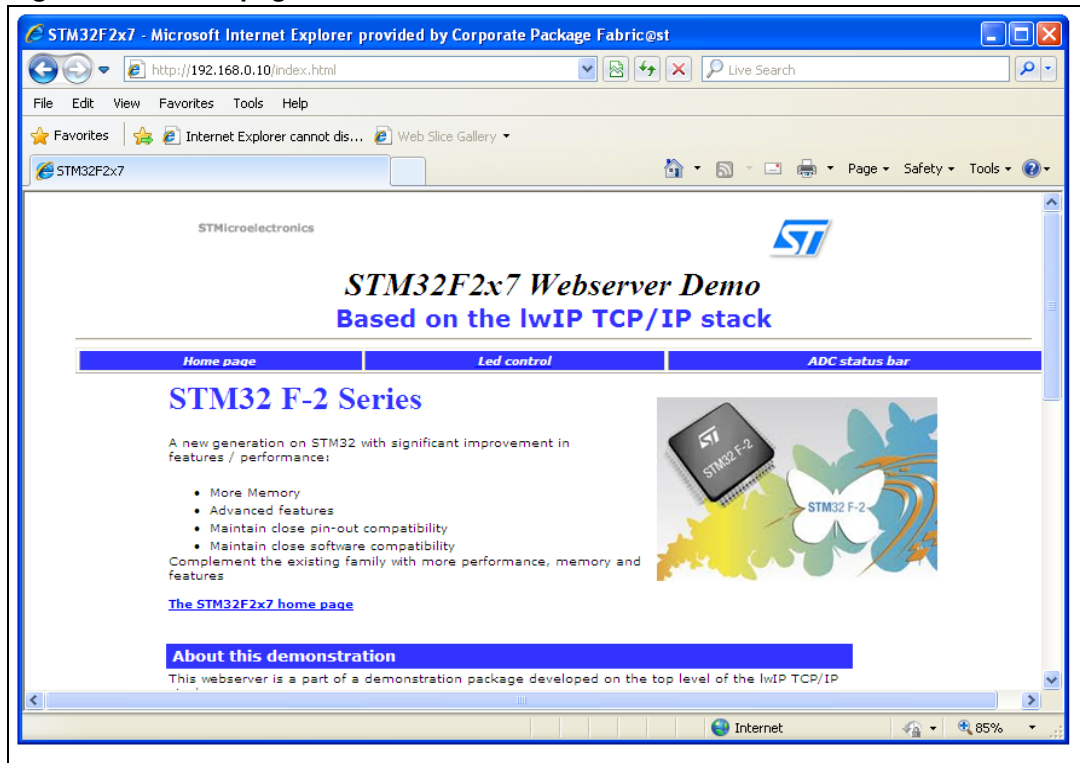
The HTTP server demo shows an implementation of a web server with the following features:

- URL parsing
- support of CGI (Common Gateway Interface)
- support of SSI (Server Side Includes)
- dynamic Header generation
- support of HTTP Post request

In order to test the HTTP server demo, follow these steps:

1. Be sure of the correct jumper settings in the STM322xG-EVAL board.
2. In the *main.h* file, uncomment option "USE_DHCP" to enable the DHCP client. Also depending on your needs, you can uncomment/comment other options such as "SERIAL_DEBUG" or "USE_LCD". The "SERIAL_DEBUG" option is used to retarget the printf function to serial port (COM1) for debug purposes.
3. Build and program the demo code into the STM32F2x7 Flash memory.
4. If "USE_DHCP" and "USE_LCD" are defined, a message is displayed on the LCD screen indicating the success or failure of the DHCP IP address allocation.
5. After IP address assignment (either static or dynamic address), the user can start the demo.
6. On the remote PC, open a web client (Mozilla Firefox or Internet Explorer) and type the board's IP address in a web browser. By default, the following static IP address is used: 192.168.0.10

Figure 10. Home page of the HTTP server demo



Server Side Includes (SSI)

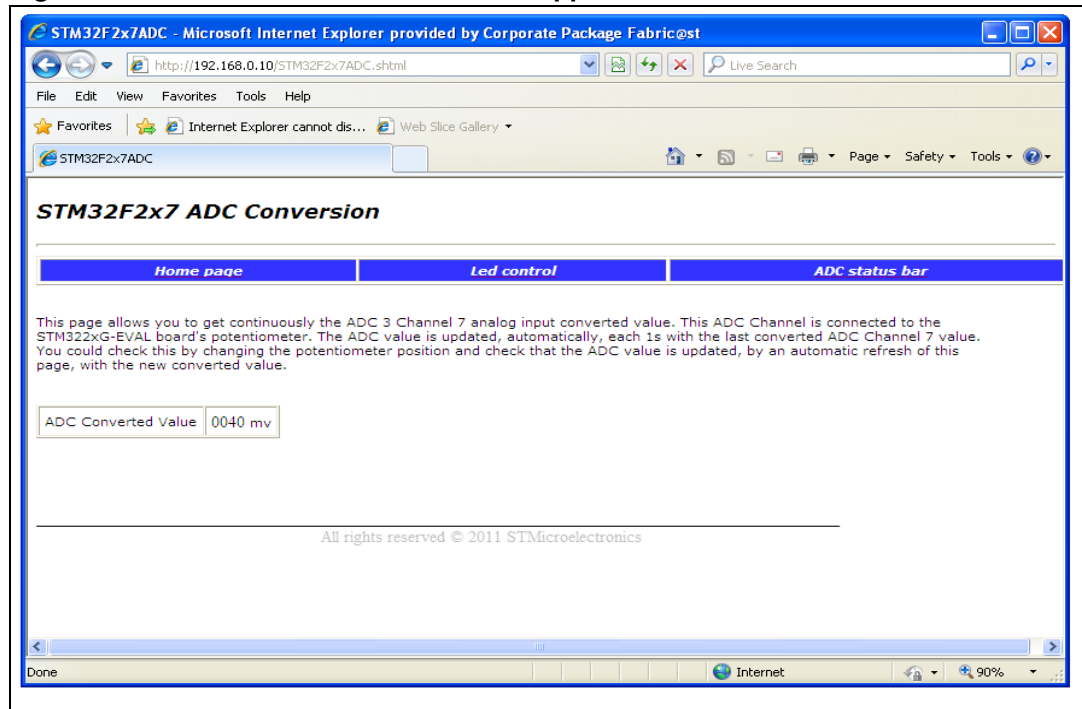
SSI is a method used to dynamically include dynamic data in HTML code.

This is done by placing a specific tag inside the HTML code of the web page. The tag should have the following format: `<!--#tag-->`

For the ADC conversion page, the following tag is used inside the HTML code: `<!--#t-->`

When there is a request for the ADC webpage (which has a ".shtml" extension), the server will parse the webpage and when the tag is found, it will be replaced by the ADC conversion value.

Figure 11. SSI use in HTTP server demo application



Common Gateway Interface (CGI)

The CGI is standard web technique used to execute a request coming from a client on the server side and then return a response to the client.

In LwIP, the simple CGI support offered works only with GET method requests and can handle up to 16 parameters encoded in the URI. The CGI handler function executed at the server side, returns a HTML file that the HTTP server sends to the client.

In the HTTP server demo, this method is used to control the four LEDs: LED1, LED2, LED3 and LED4 on the STM32F2x7 evaluation board.

6.1.2 TCP echo client demo

This demo is used to test a basic TCP connection. In this demo, the STM32 acts as a TCP client that connects to the TCP server. The client sends a string and the server echoes back the same string to the client.

In order to test the TCP echo client demo, follow these steps:

1. Be sure of the correct jumper settings in the STM322xG-EVAL board.
2. In the *main.h* file, uncomment option "USE_DHCP" to enable the DHCP client. Also depending on your needs, you can uncomment/comment other options such as "SERIAL_DEBUG" or "USE_LCD". The "SERIAL_DEBUG" option enables you to retarget the printf function to serial port (COM1) for debug purposes.
3. Build and program the demo code into the STM32F2x7 Flash memory.
4. If "USE_DHCP" and "USE_LCD" are defined, a message is displayed on the LCD screen indicating the success or failure of the DHCP IP address allocation.
5. After the IP address assignment (either static or dynamic address), the user can start the demo.

6. On the remote PC, open a command prompt window. (In Windows, select **Start > All Programs > Accessories > Command Prompt.**)
7. At the command prompt, enter:


```
C:\>echotool /p tcp /s
```

 where;
 - /p tcp is the TCP protocol (TCP protocol)
 - /s is the actual mode of connection (Server mode)
8. When you press the Key button on the STM322xG-EVAL board, the client sends a string and the server echoes back the same string to the client.

Figure 12 shows an example of this command string and the module's response.

Figure 12. TCP echo client demo

```

C:\>echotool /p tcp /s
Waiting for TCP connection on port 7. Press any key to exit.
Client 192.168.0.10:4097 accepted at 2:24:42 PM
2:24:42 PM received [sending tcp client message 0]
Session closed by peer.
Waiting for TCP connection on port 7. Press any key to exit.
Client 192.168.0.10:4098 accepted at 2:24:45 PM
2:24:45 PM received [sending tcp client message 1]
Session closed by peer.
Waiting for TCP connection on port 7. Press any key to exit.
Client 192.168.0.10:4099 accepted at 2:24:46 PM
2:24:46 PM received [sending tcp client message 2]
Session closed by peer.
Waiting for TCP connection on port 7. Press any key to exit.
  
```

Note: Please ensure that the remote PC IP address is the same IP address as the one defined in the main.h file (192.168.0.11).

6.1.3 TCP echo server demo

This demo is used to test a basic TCP connection. In this demo, the STM32 acts as a TCP server that waits for client requests. It simply echoes back whatever it is sent.

In order to test the TCP echo server demo, follow these steps:

1. Be sure of the correct jumper settings in the STM322xG-EVAL board.
2. In the *main.h* file, uncomment option "USE_DHCP" to enable the DHCP client. Also depending on your needs, you can uncomment/comment other options such as "SERIAL_DEBUG" or "USE_LCD". The "SERIAL_DEBUG" option is used to retarget the printf function to serial port (COM1) for debug purposes.
3. Build and program the demo code into the STM32F2x7 Flash memory.
4. If "USE_DHCP" and "USE_LCD" are defined, a message is displayed on the LCD screen indicating the success or failure of the DHCP IP address allocation.

5. After IP address assignment (either static or dynamic address), the user can start the demo.
6. On the remote PC, open a command prompt window. (In Windows, select **Start > All Programs > Accessories > Command Prompt.**)
7. At the command prompt, enter:

```
C:\>echotool IP_address /p tcp /r 7 /n 15 /t 2 /d Testing
LwIP TCP echo server
```

where;

- IP_address is the actual board's IP address; By default the following static IP address is used: 192.168.0.10
- /p tcp is the protocol (TCP protocol)
- /r is the actual remote port on the echo server (echo port)
- /n is the number of echo requests (for example, 15)
- /t is the connection timeout in seconds (for example, 2)
- /d is the message to be sent for echo (for example, "Testing LwIP TCP echo server")

Figure 13 shows an example of this command string and the module's response.

Figure 13. TCP echo server demo

```

Command Prompt
C:\>echotool 192.168.0.10 /p tcp /r 7 /n 15 /t 2 /d Testing LwIP TCP echo server
Hostname 192.168.0.10 resolved as 192.168.0.10
Reply from 192.168.0.10:7, time 0 ms OK
Reply from 192.168.0.10:7, time 0 ms OK
Reply from 192.168.0.10:7, time 0 ms OK
Reply from 192.168.0.10:7, time 0 ms OK
Reply from 192.168.0.10:7, time 0 ms OK
Reply from 192.168.0.10:7, time 0 ms OK
Reply from 192.168.0.10:7, time 0 ms OK
Reply from 192.168.0.10:7, time 0 ms OK
Reply from 192.168.0.10:7, time 0 ms OK
Reply from 192.168.0.10:7, time 0 ms OK
Reply from 192.168.0.10:7, time 0 ms OK
Reply from 192.168.0.10:7, time 0 ms OK
Reply from 192.168.0.10:7, time 0 ms OK
Reply from 192.168.0.10:7, time 0 ms OK
Reply from 192.168.0.10:7, time 0 ms OK
Statistics: Received=15, Corrupted=0
C:\>

```

Note: Statistics are given at the end of the test that provide the number of received and corrupted packets.

6.1.4 UDP echo client demo

This demo is used to test a basic UDP echo connection. In this demo the STM32 acts as a UDP client that connects to a UDP server.

In order to test the UDP echo client demo, follow these steps:

1. Be sure of the correct jumper settings in the STM32xG-EVAL board.
2. In the *main.h* file, uncomment option “USE_DHCP” to enable the DHCP client. Also depending on your needs, you can uncomment/comment other options such as “SERIAL_DEBUG” or “USE_LCD”. The “SERIAL_DEBUG” option is used to retarget the printf function to serial port (COM1) for debug purposes.
3. Build and program the demo code into the STM32F2x7 Flash memory.
4. If “USE_DHCP” and “USE_LCD” are defined, a message is displayed on the LCD screen indicating the success or failure of the DHCP IP address allocation.
5. After IP address assignment (either static or dynamic address), the user can start the demo.
6. On the remote PC, open a command prompt window. (In Windows, select **Start > All Programs > Accessories > Command Prompt.**)
7. At the command prompt, enter:


```
C:\>echotool /p udp /s
```

 where;
 - /p udp is the protocol (UDP protocol)
 - /s is the actual mode of connection (Server mode)
8. When you press the Key button on the STM32xG-EVAL board, the client sends a string and the server echoes back the same string to the client.

Figure 14 shows an example of this command string and the module’s response.

Figure 14. UDP echo client demo

```

C:\>echotool /p udp /s

Waiting for UDP connection on port 7. Press any key to exit.
2:46:10 PM from 192.168.0.10:4096 received [sending udp client message 0]
2:46:11 PM from 192.168.0.10:4096 received [sending udp client message 1]
2:46:11 PM from 192.168.0.10:4096 received [sending udp client message 2]
2:46:12 PM from 192.168.0.10:4096 received [sending udp client message 3]
2:46:12 PM from 192.168.0.10:4096 received [sending udp client message 4]
2:46:13 PM from 192.168.0.10:4096 received [sending udp client message 5]
2:46:13 PM from 192.168.0.10:4096 received [sending udp client message 6]
2:46:13 PM from 192.168.0.10:4096 received [sending udp client message 7]
2:46:13 PM from 192.168.0.10:4096 received [sending udp client message 8]
2:46:14 PM from 192.168.0.10:4096 received [sending udp client message 9]
2:46:14 PM from 192.168.0.10:4096 received [sending udp client message 10]
2:46:15 PM from 192.168.0.10:4096 received [sending udp client message 11]
2:46:19 PM from 192.168.0.10:4096 received [sending udp client message 12]
2:46:19 PM from 192.168.0.10:4096 received [sending udp client message 13]
2:46:19 PM from 192.168.0.10:4096 received [sending udp client message 14]
2:46:20 PM from 192.168.0.10:4096 received [sending udp client message 15]
  
```

Note: Please ensure that the remote PC IP address is the same IP address as the one defined in the *main.h* file (192.168.0.11).

6.1.5 UDP echo server demo

This demo is used to test a basic UDP connection. In this demo, the STM32 acts as a UDP server that waits for client requests.

In order to test the UDP echo server demo, follow these steps:

1. Be sure of the correct jumper settings in the STM322xG-EVAL board.
2. In the *main.h* file, uncomment option "USE_DHCP" to enable the DHCP client. Also depending on your needs, you can uncomment/comment other options such as "SERIAL_DEBUG" or "USE_LCD". The "SERIAL_DEBUG" option is used to retarget the printf function to serial port (COM1) for debug purposes.
3. Build and program the demo code into the STM32F2x7 Flash memory.
4. If "USE_DHCP" and "USE_LCD" are defined, a message is displayed on the LCD screen indicating the success or failure of the DHCP IP address allocation.
5. After IP address assignment (either static or dynamic address), the user can start the demo.
6. On the remote PC, open a command prompt window. (In Windows, select **Start > All Programs > Accessories > Command Prompt.**)
7. At the command prompt, enter:

```
C:\>echotool IP_address /p udp /r 7 l/ 7 /n 15 /t 2 /d
Testing LwIP UDP echo server
```

where;

- IP_address is the actual board's IP address; By default the following static IP address is used: 192.168.0.10
- /p udp is the protocol (UDP protocol)
- /r is the actual remote port on the echo server (echo port)
- /l is the actual local for the client (echo port)
- /n is the number of echo requests (for example, 15)
- /t is the connection timeout in seconds (for example, 2)
- /d is the message to be sent for echo (for example, "Testing LwIP UDP echo server")

Figure 15 shows an example of this command string and the module's response.

Figure 15. UDP echo server demon

```

C:\>echotool 192.168.0.10 /p udp /r 7 /l 7 /n 15 /t 2 /d Testing LwIP UDP echo server
Hostname 192.168.0.10 resolved as 192.168.0.10
Reply from 192.168.0.10:7. time 0 ms OK
Reply from 192.168.0.10:7. time 0 ms OK
Reply from 192.168.0.10:7. time 0 ms OK
Reply from 192.168.0.10:7. time 0 ms OK
Reply from 192.168.0.10:7. time 0 ms OK
Reply from 192.168.0.10:7. time 0 ms OK
Reply from 192.168.0.10:7. time 0 ms OK
Reply from 192.168.0.10:7. time 0 ms OK
Reply from 192.168.0.10:7. time 0 ms OK
Reply from 192.168.0.10:7. time 0 ms OK
Reply from 192.168.0.10:7. time 0 ms OK
Reply from 192.168.0.10:7. time 0 ms OK
Reply from 192.168.0.10:7. time 0 ms OK
Reply from 192.168.0.10:7. time 0 ms OK
Reply from 192.168.0.10:7. time 0 ms OK
Statistics: Received=15, Corrupted=0, Lost=0
C:\>

```

Note: Statistics are given at the end of the test that provide the number of received and corrupted packets.

6.1.6 TFTP server demo

The TFTP server is a file transfer application that needs a remote TFTP client. The files are transferred to and from the microSD card located on the STM322xG-EVAL board.

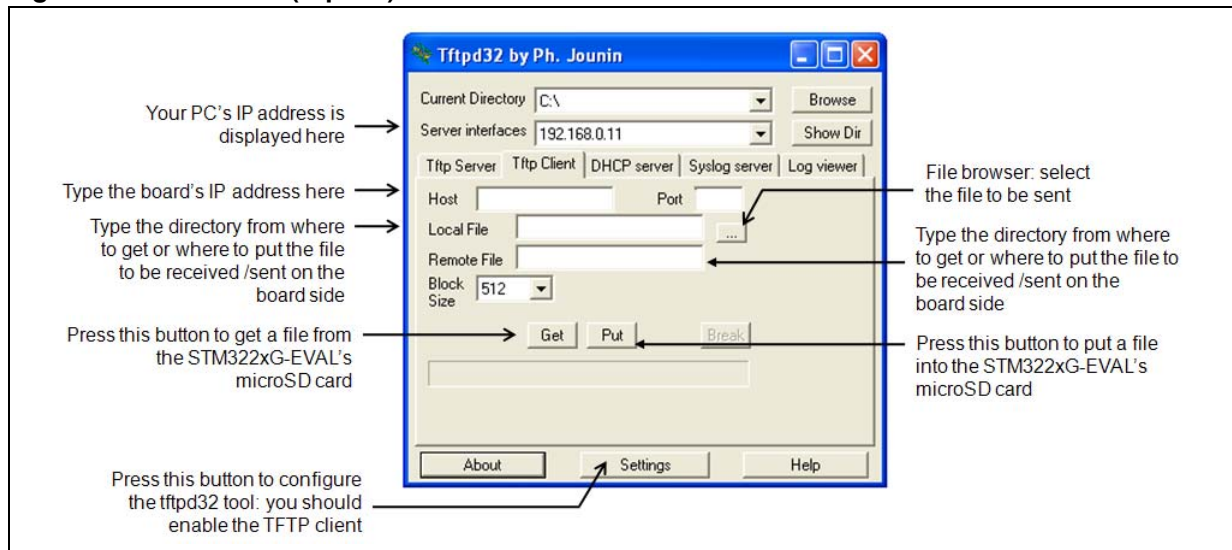
The TFTP server waits for a request from a remote TFTP client. The STM322xG-EVAL board must be connected through a remote PC to download or upload a file. To do this, a TFTP client must be installed on the remote PC. This can be done by using the tftpd32 tool, which can be found at <http://tftpd32.jounin.net>

In order to test the tftpserver demo, follow these steps:

1. Be sure of the correct jumper settings in the STM322xG-EVAL board.
2. In the main.h file, uncomment option "USE_DHCP" to enable the DHCP client. Also depending on your needs, you can uncomment/comment other options such as "SERIAL_DEBUG" or "USE_LCD". The "SERIAL_DEBUG" option is used to retarget the printf function to serial port (COM1) for debug purposes.
3. Build and program the demo code into the STM32F2x7 Flash memory.
4. If "USE_DHCP" and "USE_LCD" are defined, a message is displayed on the LCD screen indicating the success or failure of the DHCP IP address allocation.
5. After IP address assignment (either static or dynamic address), the user can start the demo.
6. On the remote PC, open the TFTP client (for example, TFTP32), and configure the TFTP server address (host address in TFTP32).
7. Start transferring files to and from the microSD card located on the STM322xG-EVAL board.

Figure 16 gives an overview of the tftpd32 tool.

Figure 16. TFTP tool (tftpd32)



Note: Make sure that the microSD™ card is plugged into the dedicated connector (CN6) prior to downloading/uploading a file from/to the STM32xG-EVAL board.

6.2 FreeRTOS demos

6.2.1 HTTP server netconn demo

The HTTP server netconn demo shows an implementation of a web server application based on the netconn API. This demo is used to connect to the STM32xG-EVAL board from web browser and to load HTML pages.

This demo has two HTML pages. The first one contains general information about STM32F2x7 microcontrollers, the demonstration package and the stack LwIP. The second one contains the list of running tasks and their status. This page is automatically updated every second.

In order to test the HTTP server netconn demo, follow these steps:

1. Be sure of the correct jumper settings in the STM32xG-EVAL board.
2. In the *main.h* file, uncomment option "USE_DHCP" to enable the DHCP client. Also depending on your needs, you can uncomment/comment other options such as "SERIAL_DEBUG" or "USE_LCD". The "SERIAL_DEBUG" option is used to retarget the printf function to serial port (COM1) for debug purposes.
3. Build and program the demo code into the STM32F2x7 Flash memory.
4. If "USE_DHCP" and "USE_LCD" are defined, a message is displayed on the LCD screen indicating the success or failure of the DHCP IP address allocation.
5. After IP address assignment (either static or dynamic address), the user can start the demo.
6. On the remote PC, open a web client (Mozilla Firefox or Internet Explorer) and type the board's IP address in a web browser. By default, the following static IP address is used: 192.168.0.10

6.2.2 HTTP server socket demo

The HTTP server socket demo shows an implementation of web server application based on the socket API. To test this demo, refer to [Section 6.2.1: HTTP server netconn demo](#).

6.2.3 UDP TCP echo server netconn demo

This demo provides the echo service application on both TCP and UDP protocols:

- To test the UDP TCP echo server netconn demo in TCP server mode, refer to [Section 6.1.3: TCP echo server demo](#).
- To test the UDP TCP echo server netconn demo in UDP server mode, refer to [Section 6.1.5: UDP echo server demo](#).

7 Footprint information

7.1 HTTP server demo

[Table 14](#) provides the HTTP server demonstration footprint, calculated with the following configuration:

- 12 buffers of 512 bytes constitute the lwIP pool of buffers. These parameters are defined in the *lwipopts.h* file by `PBUF_POOL_SIZE` and `PBUF_POOL_BUFSIZE`.
- 10 Kbytes dedicated to the lwIP's heap and defined in the *lwipopts.h* file by `MEM_SIZE`.
- 6 buffers of 1520 bytes dedicated to the Ethernet driver and defined in the *STM32F2x7_eth_conf.h* file.

Note: These values are provided for demonstration purposes only. So, if you want to port the current package and use it for your application, these parameters should be adjusted to your needs.

Table 14. HTTP server demo footprint

Modules	Flash memory (bytes)		SRAM (bytes)
	RO code	RO data	RW data
Ethernet driver and interface	2048	0	9360
lwIP memory management and IP modules	16006	44	21372
Application modules: Main and system initialization	4366	35112	1388
STM32F2xx standard peripherals library drivers	1908	16	16
STM322xC-EVAL board	1744	4592	32
Others (Stack, Heap, etc.)	12874	118	2285
Grand Total	38946	39882	34453

Note: The software is compiled using IAR EWARM v6.1, with high optimization for code size.

7.2 HTTP server netconn demo

[Table 15](#) provides the HTTP server demonstration footprint, calculated with the following configuration:

- 12 buffers of 512 bytes constitute the lwIP pool of buffers. These parameters are defined in the *lwipopts.h* file by `PBUF_POOL_SIZE` and `PBUF_POOL_BUFSIZE`.
- 5 Kbytes dedicated to the lwIP's heap and defined in the *lwipopts.h* file by `MEM_SIZE`.
- 8 buffers of 1520 bytes dedicated to the Ethernet driver and defined in the *STM32F2x7_eth_conf.h* file.

Note: These values are provided for demonstration purposes only. So, if you want to port the current package and use it for your application, these parameters should be adjusted to your needs.

Table 15. Httpserver netconn demo footprint

Modules	Flash memory (bytes)		SRAM (bytes)
	RO code	RO data	RW data
Ethernet driver and interface	2372	0	12484
lwIP memory management and IP modules	21518	18/	16638
FreeRTOS	3924	91	15736
Application modules: Main and system initialization	4014	26000	300
STM32F2xx standard peripherals library drivers	1716	4	16
STM322xC-EVAL board	1748	4569	32
Total	35292	30682	45206
Others (Stack, Heap, etc.)	15036	134	2810
Grand Total	50328	30816	48016

Note: The software is compiled using IAR EWARM v6.1, with high optimization for code size.

8 Conclusion

The aim of this application note is to show the use of the lwIP TCP/IP stack with the STM32F2x7xx family. This open source stack offers the services of a full-scale TCP/IP stack while keeping relatively low RAM/ROM usage.

The application note also shows two approaches for developing TCP/IP applications either in Standalone mode or using a real-time operating system (RTOS) for multi-threaded operations.

9 Revision history

Table 16. Document revision history

Date	Revision	Changes
27-May-2011	1	Initial release.
19-Oct-2011	2	Updated Section 5.2.1: PHY interface configuration and Table 13: STM322xG-EVAL jumper configurations .

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