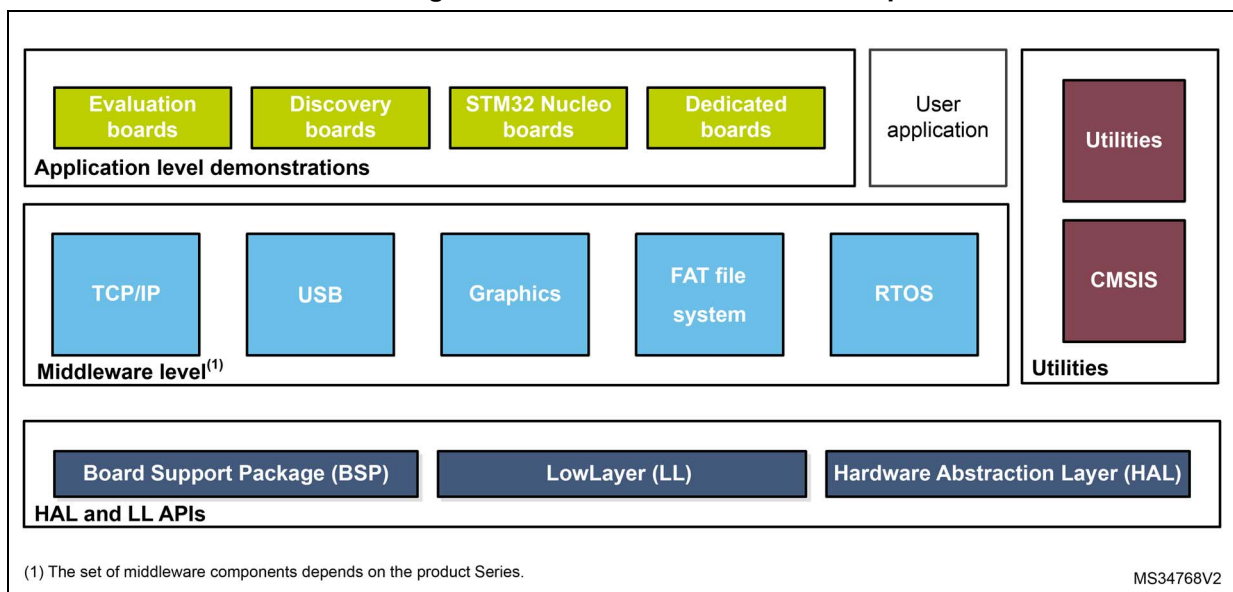


STM32Cube firmware examples for STM32F2 Series

Introduction

The STM32CubeF2 firmware package comes with a rich set of examples running on STMicroelectronics boards. The examples are organized by board and provided with preconfigured projects for the main supported toolchains (see [Figure 1](#)).

Figure 1. STM32CubeF2 firmware components



Reference documents

The reference documents are available on www.st.com/stm32cubefw:

- Latest release of STM32CubeF2 firmware package
- *Getting started with the STM32CubeF2 firmware package for STM32F2 Series* user manual (UM1739)
- *STM32Cube USB Device library* user manual (UM1734)
- *STM32Cube USB host library* user manual (UM1720)
- *Developing Applications on STM32Cube with FatFs* user manual (UM1721)
- *Developing Applications on STM32Cube with RTOS* user manual (UM1722)
- *Developing applications on STM32Cube with LwIP TCP/IP stack* user manual (UM1713)
- *STM32Cube Ethernet IAP example* user manual (UM1709)

STM32CubeF2 examples

The examples are classified depending on the STM32Cube level they apply to. They are named as follows:

- **Examples:** the examples use only the HAL and BSP drivers (middleware not used). Their objective is to demonstrate the product/peripherals features and usage. They are organized per peripheral (one folder per peripheral, for example TIM). Their complexity level ranges from the basic usage of a given peripheral (for example PWM generation using timer) to the integration of several peripherals (for example how to use DAC for signal generation with synchronization from TIM6 and DMA). The usage of the board resources is reduced to the strict minimum.
- **Examples_LL**
These examples use only the LL drivers (HAL drivers and middleware components not used). They offer an optimum implementation of typical use cases of the peripheral features and configuration sequences. The examples are organized per peripheral (one folder for each peripheral, for example TIM) and run exclusively on Nucleo board.
- **Examples_MIX**
These examples use only the HAL, BSP and LL drivers (middleware components not used). They aim at demonstrating how to use both HAL and LL APIs in the same application to combine the advantages of both APIs:
 - The HAL offers high-level function-oriented APIs with high portability level by hiding the product/IPs complexity for end users.
 - The LL provides low-level APIs at register level with better optimization.The examples are organized per peripheral (one folder for each peripheral, for example TIM) and run exclusively on Nucleo board.
- **Applications:** the applications demonstrate the product performance and how to use the available middleware stacks. They are organized either by middleware (a folder per middleware, for example USB Host) or by product feature that require high-level firmware bricks (for example Audio). The integration of applications that use several middleware stacks is also supported.
- **Demonstrations:** the demonstrations aim at integrating and running the maximum number of peripherals and middleware stacks to showcase the product features and performance.
- **Template project:** the template project is provided to allow quickly building a firmware application on a given board.

The examples are located under *STM32Cube_FW_STM32CubeF2_VX.Y.Z\Projects*. They all have the same structure:

- *\Inc* folder containing all header files
- *\Src* folder containing the source code
- *\EWARM*, *\MDK-ARM*, *\SW4STM32* and *\TrueSTUDIO* folders containing the preconfigured project for each toolchain.
- *readme.txt* file describing the example behavior and the environment required to run the example.

To run the example, proceed as follows:

1. Open the example using the preferred toolchain.
2. Rebuild all files and load the image into target memory
3. Run the example by following the readme.txt instructions

Note: Refer to "Development toolchains and compilers" and "Supported devices and evaluation boards" sections of the firmware package release notes to know more about the software/hardware environment used for the firmware development and validation. The correct operation of the provided examples is not guaranteed in other environments, for example when using different compiler or board versions.

The examples can be tailored to run on any compatible hardware: simply update the BSP drivers for your board, provided it has the same hardware functions (LED, LCD display, pushbuttons, etc.). The BSP is based on a modular architecture that can be easily ported to any hardware by implementing the low-level routines.

[Table 1](#) contains the list of examples provided within the STM32CubeF2 firmware package.

The total numbers of templates, templates_LL, examples, examples_LL, examples_MIX applications and demonstrations are highlighted in gray in the table.

Table 1. STM32CubeF2 firmware examples

| Level | Module Name | Project Name | Description | STM32F207ZG -Nucleo | STM322xG -EVAL |
|----------------|-------------------------------------|--|--|------------------------|-------------------|
| Templates_LL | - | Starter project | This directory provides a reference template through the LL API that can be used to build any firmware application. | X | X |
| | Total number of templates: 2 | | | 1 | 1 |
| Templates | - | Starter project | This directory provides a reference template project that can be used to build any firmware application. | X | X |
| | Total number of templates: 2 | | | 1 | 1 |
| Examples | - | BSP | The BSP examples detects the presence of Adafruit 1.8" TFT shield with joystick and uSD. | X | X |
| | ADC | ADC_DualModeInterleaved | This example provides a short description of how to use two ADC peripherals to perform conversions in interleaved dual-mode. | - | X |
| | | ADC_InjectedConversion_Interrupt | This example describes how to use the ADC in interrupt mode to convert data through the HAL API. | - | X |
| | | ADC_RegularConversion_DMA | This example describes how to use the ADC1 and DMA to transfer continuously converted data from ADC1 to memory. | X | X |
| | | ADC_RegularConversion_Interrupt | This example describes how to use the ADC in interrupt mode to convert data through the HAL API. | X | X |
| | | ADC_RegularConversion_Polling | This example describes how to use the ADC in Polling mode to convert data through the HAL API. | - | X |
| | | ADC_TriggerMode | This example describes how to use the ADC and TIM8 to convert continuously data from ADC channel. Each time an external trigger is generated by TIM2 a new conversion is started by ADC. | - | X |
| | | ADC_TripleModeInterleaved | This example provides a short description of how to use the ADC peripheral to convert a regular channel in Triple interleaved mode. | - | X |
| | CAN | CAN_LoopBack | This example provides a description of how to set a communication with the CAN in loopback mode. | - | X |
| CAN_Networking | | This example shows how to configure the CAN peripheral to send and receive CAN frames in the normal mode. The sent frames are used to control LEDs by pressing key pushbutton. | - | X | |



Table 1. STM32CubeF2 firmware examples (continued)

| Level | Module Name | Project Name | Description | STM32F207ZG -Nucleo | STM322xG -EVAL |
|----------|-------------|-----------------------|--|------------------------|-------------------|
| Examples | CRC | CRC_Example | This example guides the user through the different configuration steps by means of the HAL API. The CRC (Cyclic Redundancy Check) calculation unit computes the CRC code of a given buffer of 32-bit data words, using a fixed generator polynomial (0x4C11DB7). | X | X |
| | CRYP | CRYP_AESModes | This example provides a short description of how to use the CRYPTO peripheral to encrypt and decrypt data using AES in chaining modes (ECB, CBC, CTR) and all key size (128, 192, 256) algorithm. | - | X |
| | | CRYP_AES_DMA | This example provides a short description of how to use the CRYPTO peripheral to encrypt and decrypt data using AES-128 Algorithm with ECB chaining mode. | - | X |
| | | CRYP_DESDESmodes | This example provides a short description of how to use the CRYPTO peripheral to encrypt and decrypt data using DES and TDES in all mode (ECB, CBC) algorithm. | - | X |
| | | CRYP_TDES_DMA | This example provides a short description of how to use the CRYPTO peripheral to encrypt data using TDES Algorithm. | - | X |
| | CORTEX | CORTEXM_MPU | This example presents the MPU feature. The example purpose is to configure a memory region as privileged read only region and tries to perform read and write operation in different mode. | X | X |
| | | CORTEXM_ModePrivilege | This example shows how to modify Cortex-M3 Thread mode privilege access and stack. | - | X |
| | | CORTEXM_SysTick | This example shows how to use the default SysTick configuration with a 1 ms timebase to toggle LEDs. | X | X |
| | DAC | DAC_SignalsGeneration | This example provides a short description of how to use the DAC peripheral to generate several signals using DMA controller. | - | X |
| | | DAC_SimpleConversion | This example provides a short description of how to use the DAC peripheral to do a simple conversion. | - | X |
| | DCMI | DCMI_CaptureMode | This example provides a short description of how to use the DCMI to interface with camera module and display in continuous mode the picture on LCD. | - | X |
| | | DCMI_SnapshotMode | This example provides a short description of how to use the DCMI to interface with camera module and display in snapshot mode the picture on LCD. | - | X |

Table 1. STM32CubeF2 firmware examples (continued)

| Level | Module Name | Project Name | Description | STM32F207ZG -Nucleo | STM322xG -EVAL |
|----------|-------------|------------------------|---|------------------------|-------------------|
| Examples | DMA | DMA_FIFOMode | This example provides a description of how to use a DMA channel to transfer a word data buffer from the FLASH memory to an embedded SRAM memory with FIFO mode enabled through the STM32F2xx HAL API. | - | X |
| | | DMA_FLASHToRAM | This example provides a description of how to use a DMA channel to transfer a word data buffer from the Flash memory to an embedded SRAM memory through the HAL API. | X | X |
| | FLASH | FLASH_EraseProgram | This application describes how to configure and use the FLASH HAL API to erase and program the internal FLASH memory. | X | X |
| | | FLASH_WriteProtection | This example describes how to configure and use the FLASH HAL API to enable and disable the write protection of the internal FLASH memory. | - | X |
| | FSMC | FSMC_SRAM | This example describes how to configure the FSMC controller to access the SRAM memory. | - | X |
| | | FSMC_SRAM_DataMemory | This example describes how to configure the FSMC controller to access the SRAM memory including heap and stack. | - | X |
| | GPIO | GPIO_EXTI | This example shows how to configure external interrupt lines. | X | X |
| | | GPIO_IOToggle | This example describes how to configure and use GPIOs through the HAL API. | X | X |
| | HAL | HAL_TimeBase_RTC_ALARM | This example describes how to customize the HAL time base using the RTC alarm instead of SysTick as main source of time base. | X | X |
| | | HAL_TimeBase_RTC_WKUP | This example describes how to customize the HAL time base using the RTC wakeup instead of SysTick as main source of time base. | X | X |
| | | HAL_TimeBase_TIM | This example describes how to customize the HAL time base using a general purpose timer instead of SysTick as main source of time base. | X | X |
| | HASH | HASH_HMAC_SHA1MD5 | This example provides a short description of how to use the HASH peripheral to hash data using HMAC SHA-1 and HMAC MD5 Algorithms. | - | X |
| | | HASH_SHA1MD5 | This example provides a short description of how to use the HASH peripheral to hash data using SHA-1 and MD5 Algorithms. | - | X |
| | | HASH_SHA1MD5_DMA | This example provides a short description of how to use the HASH peripheral to hash data using SHA-1 and MD5 Algorithms. | - | X |


Table 1. STM32CubeF2 firmware examples (continued)

| Level | Module Name | Project Name | Description | STM32F207ZG -Nucleo | STM322xG -EVAL |
|----------|--------------|--|---|------------------------|-------------------|
| Examples | I2C | I2C_TwoBoards_AdvComIT | This example describes how to perform I2C data buffer transmission/reception between two boards, using an interrupt. | - | X |
| | I2S | I2S_Audio | This example provides a basic implementation of audio features. | - | X |
| | IWDG | IWDG_Example | This example describes how to reload the IWDG counter and to simulate a software fault by generating an MCU IWDG reset when a programmed time period has elapsed. | X | X |
| | PWR | PWR_BOR | This example shows how to configure the programmable BOR thresholds using the FLASH option bytes. | - | X |
| | | PWR_CurrentConsumption | This example shows how to configure the STM32F2xx system to measure the different Low-power mode current consumption. The Low-power modes are: - Sleep Mode - Stop mode with RTC - Standby mode without RTC and BKPSRAM - Standby mode with RTC - Standby mode with RTC and BKPSRAM. To run this example, the user has to follow this step: 1. Select the Low-power modes to be measured by uncommenting the corresponding line inside the stm32f2xx_lp_modes.h file. | X | X |
| | | PWR_PVD | This example shows how to configure the programmable voltage detector using an external interrupt line. In this example, EXTI line 16 is configured to generate an interrupt on each rising or falling edge of the PVD output signal (which indicates that the Vdd voltage is below the PVD threshold). | - | X |
| | | PWR_STANDBY | This example shows how to enter the system to Standby mode and wake-up from this mode using: external RESET, RTC Alarm A or WKUP pin. | - | X |
| | | PWR_STOP | This example shows how to enter Stop mode and wake up from this mode by using the RTC Wakeup timer event or an interrupt. | - | X |
| | RCC | RCC_ClockConfig | This example describes how to use the RCC HAL API to configure the system clock (SYSCLK) and modify the clock settings on run time. | X | X |
| RNG | RNG_MultiRNG | This example guides the user through the different configuration steps by means of the HAL API to ensure RNG random 32-bit numbers generation. | - | X | |

Table 1. STM32CubeF2 firmware examples (continued)

| Level | Module Name | Project Name | Description | STM32F207ZG -Nucleo | STM322xG -EVAL |
|----------|-------------|---------------------------|--|------------------------|-------------------|
| Examples | RTC | RTC_Alarm | This example guides the user through the different configuration steps by means of the HAL API to ensure Alarm configuration and generation using the RTC peripheral. | - | X |
| | | RTC_Calendar | This example guides the user through the different configuration steps by means of the HAL API to ensure Calendar configuration using the RTC peripheral. | X | X |
| | | RTC_Tamper | This example guides the user through the different configuration steps by means of the RTC HAL API to write/read data to/from RTC Backup registers and demonstrate the Tamper detection feature. | X | X |
| | | RTC_TimeStamp | This example guides the user through the different configuration steps by means of the HAL API to ensure Time Stamp configuration using the RTC peripheral. | - | X |
| | SMARTCARD | SMARTCARD_T0 | This example describes a firmware Smartcard Interface based on the USART peripheral. The main purpose of this firmware example is to provide resources facilitating the development of an application using the USART peripheral in smartcard mode. | - | X |
| | SPI | SPI_FullDuplex_AdvComIT | This example guides the user through the different configuration steps by means of the HAL API to ensure the SPI Data buffer transmission and reception using Interrupt, in an advance communication mode: the Master board is always sending a command to the slave before any transmission and the slave board is sending an acknowledge before going further. | - | X |
| | | SPI_FullDuplex_ComDMA | This example shows how to perform the SPI data buffer transmission/reception between two boards via DMA. | - | X |
| | | SPI_FullDuplex_ComIT | This example shows how to ensure SPI data buffer transmission/reception between two boards by using an interrupt. | - | X |
| | | SPI_FullDuplex_ComPolling | This example shows how to ensure SPI data buffer transmission/reception in Polling mode between two boards. | - | X |


Table 1. STM32CubeF2 firmware examples (continued)

| Level | Module Name | Project Name | Description | STM32F207ZG -Nucleo | STM322xG -EVAL |
|----------|-------------|--------------------------|---|------------------------|-------------------|
| Examples | TIM | TIM_6Steps | This example shows how to configure the TIM1 peripheral to generate 6 Steps. | - | X |
| | | TIM_7PWMOutput | This example shows how to configure the TIM1 peripheral to generate 7 PWM signals with 4 different duty cycles (50%, 37.5%, 25% and 12.5%). | - | X |
| | | TIM_CascadeSynchro | This example shows how to synchronize the TIM peripherals in cascade mode. | - | X |
| | | TIM_ComplementarySignals | This example shows how to configure the TIM1 peripheral to generate three complementary TIM1 signals, to insert a defined dead time value, to use the break feature and to lock the desired parameters. | - | X |
| | | TIM_DMA | This example provides a description of how to use DMA with TIMER update request to transfer data from memory to TIMER Capture Compare Register 3 (CCR3). | X | X |
| | | TIM_DMABurst | This example shows how to update the TIM1 channel1 period and the duty cycle using the TIM1 DMA burst feature. | - | X |
| | | TIM_Encoder | This example shows how to configure the TIM1 peripheral in encoder mode to determinate the rotation direction. | - | X |
| | | TIM_ExtTriggerSynchro | This example shows how to synchronize the TIM peripheral in cascade mode with an external trigger. | - | X |
| | | TIM_InputCapture | This example shows how to use the TIM peripheral to measure the frequency of an external signal. | X | X |
| | | TIM_OCActive | This example shows how to configure the TIM peripheral in Output Compare Active mode (when the counter matches the capture/compare register, the concerned output pin is set to its active state). | X | X |
| | | TIM_OCInactive | This example shows how to configure the TIM peripheral in Output Compare Inactive mode with the corresponding Interrupt requests for each channel. | - | X |

Table 1. STM32CubeF2 firmware examples (continued)

| Level | Module Name | Project Name | Description | STM32F207ZG -Nucleo | STM322xG -EVAL |
|--------------------------------------|--------------|--|---|------------------------|-------------------|
| Examples | TIM | TIM_OCToggle | This example shows how to configure the TIM peripheral to generate four different signals with four different frequencies. | X | X |
| | | TIM_OnePulse | This example shows how to use the TIM peripheral to generate a One pulse Mode after a rising edge of an external signal is received in timer input pin. | X | X |
| | | TIM_PWMInput | This example shows how to use the TIM peripheral to measure the frequency and duty cycle of an external signal. | X | X |
| | | TIM_PWMOutput | This example shows how to configure the TIM peripheral in PWM (Pulse Width Modulation) mode. | X | X |
| | | TIM_ParallelSynchro | This example shows how to synchronize TIM2 and the timers (TIM3 and TIM4) in parallel mode. | - | X |
| | | TIM_Synchronization | This example shows how to synchronize TIM1 and the timers (TIM3 and TIM4) in parallel mode. | - | X |
| | | TIM_TimeBase | This example shows how to configure the TIM peripheral to generate a time base of one second with the corresponding interrupt request. | - | X |
| | UART | UART_Hyperterminal_DMA | This example describes an UART transmission (transmit/receive) in DMA mode between a board and an Hyperterminal PC application. | - | X |
| | | UART_Hyperterminal_IT | This example describes an UART transmission (transmit/receive) between a board and an Hyperterminal PC application by using an interrupt. | - | X |
| | | UART_Printf | This example shows how to reroute the C library printf function to the UART. It outputs a message sent by the UART on the HyperTerminal. | X | X |
| WWDG | WWDG_Example | This example guides the user through the different configuration steps by means of the HAL API to perform periodic WWDG counter update and simulate a software fault that generates an MCU WWDG reset when a predefined time period has elapsed. | X | X | |
| Total number of examples: 104 | | | | 27 | 77 |


Table 1. STM32CubeF2 firmware examples (continued)

| Level | Module Name | Project Name | Description | STM32F207ZG -Nucleo | STM322xG -EVAL |
|-------------|-------------|---|--|------------------------|-------------------|
| Examples_LL | ADC | ADC_AnalogWatchdog | This example describes how to use a ADC peripheral with the ADC analog watchdog to monitor a channel and detect when the corresponding conversion data is out of window thresholds. This example is based on the STM32F2xx ADC LL API. The peripheral initialization is done using LL unitary service functions for optimization purpose (performance and size). | X | - |
| | | ADC_ContinuousConversion_TriggerSW | This example describes how to use a ADC peripheral to perform continuous ADC conversions of a channel, from a SW start. This example is based on the STM32F2xx ADC LL API. The peripheral initialization is done using LL unitary service functions for optimization purpose (performance and size). | X | - |
| | | ADC_ContinuousConversion_TriggerSW_Init | This example describes how to use a ADC peripheral to perform continuous ADC conversions of a channel, from a SW start. This example is based on the STM32F2xx ADC LL API. The peripheral initialization is done using LL unitary service functions for optimization purpose (performance and size). | X | - |
| | | ADC_GroupsRegularInjected | This example describes how to use a ADC peripheral with both ADC groups (ADC group regular and ADC group injected) in their intended use case. This example is based on the STM32F2xx ADC LL API. The peripheral initialization is done using LL unitary services functions for optimization purpose (performance and size). | X | - |
| | | ADC_MultiChannelSingleConversion | This example describes how to use a ADC peripheral to convert several channels, ADC conversions are performed successively in a scan sequence. This example is based on the STM32F2xx ADC LL API. The peripheral initialization is done using LL unitary services functions for optimization purpose (performance and size). | X | - |
| | | ADC_MultimodeDualInterleaved | This example describes how to use several ADC peripherals in multimode, mode interleaved. This example is based on the STM32F2xx ADC LL API. The peripheral initialization is done using LL unitary services functions for optimization purpose (performance and size). | X | - |
| | | ADC_SingleConversion_TriggerSW | This example describes how to use a ADC peripheral to perform a single ADC conversion of a channel, at each software start. The example is using the programming model: polling (for programming models interrupt or DMA transfer, refer to other examples). This example is based on the STM32F2xx ADC LL API. The peripheral initialization is done using LL unitary services functions for optimization purpose (performance and size). | X | - |

Table 1. STM32CubeF2 firmware examples (continued)

| Level | Module Name | Project Name | Description | STM32F207ZG -Nucleo | STM322xG -EVAL |
|-------------|-------------|---------------------------------------|--|------------------------|-------------------|
| Examples_LL | ADC | ADC_SingleConversion_TriggerSW_DMA | This example describes how to use a ADC peripheral to perform a single ADC conversion of a channel, at each software start. The example is using the programming model: DMA transfer (for programming models polling or interrupt, refer to other examples). This example is based on the STM32F2xx ADC LL API. The peripheral initialization is done using LL unitary services functions for optimization purpose (performance and size). | X | - |
| | | ADC_SingleConversion_TriggerSW_IT | This example describes how to use a ADC peripheral to perform a single ADC conversion of a channel, at each software start. The example is using the programming model: interrupt (for programming models polling or DMA transfer, refer to other examples). This example is based on the STM32F2xx ADC LL API. The peripheral initialization is done using LL unitary services functions for optimization purpose (performance and size). | X | - |
| | | ADC_SingleConversion_TriggerTimer_DMA | This example describes how to use a ADC peripheral to perform a single ADC conversion of a channel, at each trigger event from timer. The conversion data are transferred by DMA into a table, indefinitely (circular mode). This example is based on the STM32F2xx ADC LL API. The peripheral initialization is done using LL unitary services functions for optimization purpose (performance and size). | X | - |
| | | ADC_TemperatureSensor | This example describes how to use a ADC peripheral to perform a single ADC conversion of the internal temperature sensor and to calculate the temperature in Celsius degrees. The example is using the programming model: polling (for programming models interrupt or DMA transfer, refer to other examples). This example is based on the STM32F2xx ADC LL API. The peripheral initialization is done using LL unitary services functions for optimization purpose (performance and size). | X | - |
| | CORTEX | CORTEX_MPU | This example presents the MPU feature. Its purpose is to configure a memory area as privileged read-only area and attempt to perform read and write operations in different modes. | X | - |
| | CRC | CRC_CalculateAndCheck | This example shows how to configure the CRC calculation unit to get a CRC code of a given data buffer, based on a fixed generator polynomial (default value 0x4C11DB7). The peripheral initialization is done using LL unitary services functions for optimization purpose (performance and size). | X | - |


Table 1. STM32CubeF2 firmware examples (continued)

| Level | Module Name | Project Name | Description | STM32F207ZG -Nucleo | STM322xG -EVAL |
|-------------|-------------|--------------------------------------|--|------------------------|-------------------|
| Examples_LL | DAC | DAC_GenerateConstantSignal_TriggerSW | This example describes how to use the DAC peripheral to generate a constant voltage signal; This example is based on the STM32F2xx DAC LL API. The peripheral initialization is done using LL unitary services functions for optimization purpose (performance and size). | X | - |
| | | DAC_GenerateWaveform_TriggerHW | This example describes how to use the DAC peripheral to generate a waveform voltage from digital data stream transferred by DMA. This example is based on the STM32F2xx DAC LL API. The peripheral initialization is done using LL unitary services functions for optimization purpose (performance and size). | X | - |
| | | DAC_GenerateWaveform_TriggerHW_Init | This example describes how to use the DAC peripheral to generate a waveform voltage from digital data stream transferred by DMA. This example is based on the STM32F2xx DAC LL API. The peripheral initialization is done using LL initialization function to demonstrate LL init usage. | X | - |
| | DMA | DMA_CopyFromFlashToMemory | This example describes how to use a DMA to transfer a word data buffer from the Flash memory to embedded SRAM. The peripheral initialization is done using LL unitary services functions for optimization purpose (performance and size). | X | - |
| | | DMA_CopyFromFlashToMemory_Init | This example describes how to use a DMA to transfer a word data buffer from the Flash memory to embedded SRAM. The peripheral initialization is done using LL initialization function to demonstrate LL init usage. | X | - |
| | EXTI | EXTI_ToggleLedOnIT | This example describes how to configure the EXTI and use GPIOs to toggle the user LEDs available on the board when a user button is pressed. It is based on the STM32F2xx LL API. The peripheral initialization is done using LL unitary services functions for optimization purpose (performance and size). | X | - |
| | | EXTI_ToggleLedOnIT_Init | This example describes how to configure the EXTI and use GPIOs to toggle the user LEDs available on the board when a user button is pressed. This example is based on the STM32F2xx LL API. The peripheral initialization is done using LL initialization function to demonstrate LL init usage. | X | - |

Table 1. STM32CubeF2 firmware examples (continued)

| Level | Module Name | Project Name | Description | STM32F207ZG -Nucleo | STM322xG -EVAL |
|-------------|-------------|---|--|------------------------|-------------------|
| Examples_LL | GPIO | GPIO_InfiniteLedToggling | This example describes how to configure and use GPIOs to toggle every 250 ms the user LEDs available on the board. This example is based on the STM32F2xx LL API. The peripheral initialization is done using LL unitary services functions for optimization purpose (performance and size). | X | - |
| | | GPIO_InfiniteLedToggling_Init | This example describes how to configure and use GPIOs to toggle every 250 ms the user LEDs available on the board. This example is based on the STM32F2xx LL API. The peripheral initialization is done using LL initialization function to demonstrate LL init usage. | X | - |
| | I2C | I2C_OneBoard_AdvCommunication_DMAAndIT | This example describes how to exchange data between an I2C Master device in DMA mode and an I2C Slave device in Interrupt mode. The peripheral initialization is done using LL unitary services functions for optimization purpose (performance and size). | X | - |
| | | I2C_OneBoard_Communication_DMAAndIT | This example describes how to transmit data bytes from an I2C Master device using DMA mode to an I2C Slave device using Interrupt mode. The peripheral initialization is done using LL unitary services functions for optimization purpose (performance and size). | X | - |
| | | I2C_OneBoard_Communication_IT | This example describes how to receive one data byte from an I2C Slave device to an I2C Master device. Both devices operate in interrupt mode. The peripheral initialization is done using LL unitary services functions for optimization purpose (performance and size). | X | - |
| | | I2C_OneBoard_Communication_IT_Init | This example describes how to receive one data byte from an I2C Slave device to an I2C Master device. Both devices operate in Interrupt mode. The peripheral initialization is done using LL initialization function to demonstrate LL init usage. | X | - |
| | | I2C_OneBoard_Communication_PollingAndIT | This example describes how to transmit data bytes from an I2C Master device using Polling mode to an I2C Slave device using Interrupt mode. The peripheral initialization is done using LL unitary services functions for optimization purpose (performance and size). | X | - |
| | | I2C_TwoBoards_MasterRx_SlaveTx_IT | This example describes how to receive one data byte from an I2C Slave device to an I2C Master device. Both devices operate in Interrupt mode. The peripheral initialization is done using LL unitary services functions for optimization purpose (performance and size). | X | - |


Table 1. STM32CubeF2 firmware examples (continued)

| Level | Module Name | Project Name | Description | STM32F207ZG -Nucleo | STM322xG -EVAL |
|-------------|-------------|------------------------------------|---|------------------------|-------------------|
| Examples_LL | I2C | I2C_TwoBoards_MasterTx_SlaveRx | This example describes how to transmit data bytes from an I2C Master device using Polling mode to an I2C Slave device using Interrupt mode. The peripheral initialization is done using LL unitary services functions for optimization purpose (performance and size). | X | - |
| | | I2C_TwoBoards_MasterTx_SlaveRx_DMA | This example describes how to transmit data bytes from an I2C Master device using DMA mode to an I2C Slave device using DMA mode. The peripheral initialization is done using LL unitary services functions for optimization purpose (performance and size). | X | - |
| | IWDG | IWDG_RefreshUntilUserEvent | This example describes how to configure the IWDG to ensure period counter update and generate an MCU IWDG reset when a user button is pressed. The peripheral initialization is done using LL unitary services functions for optimization purpose (performance and size). | X | - |
| | PWR | PWR_EnterStandbyMode | This example shows how to enter the system in Standby mode and wake up from this mode using external RESET or wakeup interrupt. | X | - |
| | | PWR_EnterStopMode | This example shows how to enter the system in STOP_MAINREGU mode. | X | - |
| | RCC | RCC_OutputSystemClockOnMCO | This example describes how to configure MCO pins (PA8 and PC9) to output the system clock. | X | - |
| | | RCC_UseHSEasSystemClock | This example describes how to use the RCC LL API how to start the HSE and use it as system clock. | X | - |
| | | RCC_UseHSI_PLLasSystemClock | This example shows how to modify the PLL parameters in run time. | X | - |
| | RNG | RNG_GenerateRandomNumbers | This example shows how to configure RNG peripheral to allow generation of 32-bit long Random Numbers. The peripheral initialization is done using LL unitary services functions for optimization purpose (performance and size). | X | - |
| | | RNG_GenerateRandomNumbers_I T | This example shows how to configure the RNG peripheral to allow generation of 32-bit long Random Numbers, using interrupts. The peripheral initialization is done using LL unitary services functions for optimization purpose (performance and size). | X | - |

Table 1. STM32CubeF2 firmware examples (continued)

| Level | Module Name | Project Name | Description | STM32F207ZG -Nucleo | STM322xG -EVAL |
|-------------|-------------|----------------------------------|---|------------------------|-------------------|
| Examples_LL | RTC | RTC_Alarm | This example guides the user through the different configuration steps by means of LL API to ensure Alarm configuration and generation using the RTC peripheral. The peripheral initialization is done using LL unitary services functions for optimization purpose (performance and size). | X | - |
| | | RTC_Alarm_Init | This example guides the user through the different configuration steps by means of LL API to ensure Alarm configuration and generation using the RTC peripheral. the peripheral initialization is done using LL initialization function to demonstrate LL init usage. | X | - |
| | | RTC_Calendar | This example guides the user through the different configuration steps by means of HAL API to configure the RTC calendar. The peripheral initialization is done using LL unitary services functions for optimization purpose (performance and size). | X | - |
| | | RTC_ExitStandbyWithWakeUpTimer | This example shows how to configure the RTC in order to wake up from Standby mode using RTC wakeup Timer. The peripheral initialization is done using LL unitary services functions for optimization purpose (performance and size). | X | - |
| | | RTC_Tamper | This example guides the user through the different configuration steps by mean of LL API to ensure Tamper configuration using the RTC peripheral. The peripheral initialization is done using LL unitary services functions for optimization purpose (performance and size). | X | - |
| | | RTC_TimeStamp | This example guides the user through the different configuration steps by means of LL API to ensure Time Stamp configuration using the RTC peripheral. The peripheral initialization is done using LL unitary services functions for optimization purpose (performance and size). | X | - |
| | SPI | SPI_OneBoard_HalfDuplex_DMA | This example shows how to configure GPIO and SPI peripherals for transmitting bytes from an SPI Master device to an SPI Slave device by using the DMA mode through the STM32F2xx SPI LL API. The peripheral initialization is done using LL unitary services functions for optimization purpose (performance and size). | X | - |
| | | SPI_OneBoard_HalfDuplex_DMA_Init | This example shows how to configure GPIO and SPI peripherals for transmitting bytes from an SPI Master device to an SPI Slave device by using the DMA mode through the STM32F2xx SPI LL API. The peripheral initialization is done using LL initialization function to demonstrate LL init usage. | X | - |



Table 1. STM32CubeF2 firmware examples (continued)

| Level | Module Name | Project Name | Description | STM32F207ZG -Nucleo | STM322xG -EVAL |
|-------------|-------------|------------------------------|--|------------------------|-------------------|
| Examples_LL | SPI | SPI_OneBoard_HalfDuplex_IT | This example shows how to configure GPIO and SPI peripherals for transmitting bytes from an SPI Master device to an SPI Slave device by using IT mode through the STM32F2xx SPI LL API. The peripheral initialization is done using LL unitary services functions for optimization purpose (performance and size). | X | - |
| | | SPI_TwoBoards_FullDuplex_DMA | This example shows how to ensure the SPI data buffer transmission and reception in DMA mode. The example is based on the STM32F2xx SPI LL API. The peripheral initialization is done using LL unitary services functions for optimization purpose (performance and size). | X | - |
| | | SPI_TwoBoards_FullDuplex_IT | This example shows how to ensure the SPI Data buffer transmission and reception in Interrupt mode. The example is based on the STM32F2xx SPI LL API. The peripheral initialization is done using LL unitary services functions for optimization purpose (performance and size). | X | - |
| | TIM | TIM_BreakAndDeadtime | This example shows how to configure the Timer to perform the following: to generate three center-aligned PWM and complementary PWM signals, to insert a defined dead time value, to use the break feature, to lock the desired parameters. This example is based on the STM32F2xx TIM LL API. The peripheral initialization is done using LL unitary services functions for optimization purpose (performance and size). | X | - |
| | | TIM_DMA | This example provides a description of how to use the DMA with TIMER update request to transfer Data from the memory to the TIMER Capture Compare Register 3 (TIMx_CCR3). The example is using the STM32F2xx TIM LL API. The peripheral initialization is done using LL unitary services functions for optimization purpose (performance and size). | X | - |
| | | TIM_InputCapture | This example shows how to use the TIM peripheral to measure the frequency of a periodic signal provided either by an external signal generator or by another timer instance. The example is using the STM32F2xx TIM LL API. The peripheral initialization is done using LL unitary services functions for optimization purpose (performance and size). | X | - |
| | | TIM_OnePulse | This example shows how to configure a timer to generate a positive pulse in output compare mode with a length of tPULSE and after a delay of tDELAY. This example is based on the STM32F2xx TIM LL API. The peripheral initialization is done using LL unitary services functions for optimization purpose (performance and size). | X | - |

Table 1. STM32CubeF2 firmware examples (continued)

| Level | Module Name | Project Name | Description | STM32F207ZG -Nucleo | STM322xG -EVAL |
|-------------|-------------|--------------------------------------|--|------------------------|-------------------|
| Examples_LL | TIM | TIM_OutputCompare | This example shows how to configure the TIM peripheral to generate an output waveform in different output compare modes. The example is using the STM32F2xx TIM LL API. The peripheral initialization is done using LL unitary services functions for optimization purpose (performance and size). | X | - |
| | | TIM_PWMOutput | This example describes how to use a timer peripheral to generate a PWM output signal and update the PWM duty cycle. The example is using the STM32F2xx TIM LL API. The peripheral initialization is done using LL unitary services functions for optimization purpose (performance and size). | X | - |
| | | TIM_PWMOutput_Init | This example describes how to use a timer peripheral to generate a PWM output signal and update the PWM duty cycle. The example is using the STM32F2xx TIM LL API. The peripheral initialization is done using LL initialization function to demonstrate LL init usage. | X | - |
| | | TIM_TimeBase | This example shows how to configure the TIM peripheral to generate a time base. The example is using the STM32F2xx TIM LL API. The peripheral initialization is done using LL unitary services functions for optimization purpose (performance and size). | X | - |
| | USART | USART_Communication_Rx_IT | This example shows how to configure the GPIO and USART peripherals for receiving characters from HyperTerminal (PC) in Asynchronous mode using Interrupt mode. The peripheral initialization is done using LL unitary services functions for optimization purpose (performance and size). | X | - |
| | | USART_Communication_Rx_IT_Continuous | This example shows how to configure the GPIO and USART peripherals for continuously receiving characters from HyperTerminal (PC) in Asynchronous mode using Interrupt mode. The peripheral initialization is done using LL unitary services functions for optimization purpose (performance and size). | X | - |
| | | USART_Communication_Rx_IT_Init | This example shows how to configure the GPIO and USART peripherals for receiving characters from HyperTerminal (PC) in Asynchronous mode using Interrupt mode. The peripheral initialization is done using LL initialization function to demonstrate LL init usage. | X | - |


Table 1. STM32CubeF2 firmware examples (continued)

| Level | Module Name | Project Name | Description | STM32F207ZG -Nucleo | STM322xG -EVAL |
|-------------|-------------|--|--|------------------------|-------------------|
| Examples_LL | USART | USART_Communication_Tx | This example shows how to configure the GPIO and USART peripherals to send characters asynchronously to an HyperTerminal (PC) in Polling mode. If the transfer could not be completed within the allocated time, a timeout allows to exit from the sequence with a Timeout error code. This example is based on STM32F2xx USART LL API. The peripheral initialization is done using LL unitary services functions for optimization purpose (performance and size). | X | - |
| | | USART_Communication_TxRx_DMA | This example shows how to configure the GPIO and USART peripherals to send characters asynchronously to/from an HyperTerminal (PC) in DMA mode. This example is based on STM32F2xx USART LL API. The peripheral initialization is done using LL unitary services functions for optimization purpose (performance and size). | X | - |
| | | USART_Communication_Tx_IT | This example shows how to configure the GPIO and USART peripherals to send characters asynchronously to HyperTerminal (PC) in Interrupt mode. This example is based on STM32F2xx USART LL API. The peripheral initialization is done using LL unitary services functions for optimization purpose (performance and size). | X | - |
| | | USART_HardwareFlowControl | This example shows how to configure the GPIO and USART peripherals to receive characters asynchronously from HyperTerminal (PC) in Interrupt mode with Hardware Flow Control feature enabled. This example is based on STM32F2xx USART LL API. The peripheral initialization is done using LL unitary services functions for optimization purpose (performance and size). | X | - |
| | | USART_SyncCommunication_FullDuplex_DMA | This example shows how to configure the GPIO, USART, DMA and SPI peripherals for transmitting bytes from/to an USART peripheral to/from an SPI peripheral (in slave mode) by using DMA mode through the STM32F2xx USART LL API. The peripheral initialization is done using LL unitary services functions for optimization purpose (performance and size). | X | - |
| | | USART_SyncCommunication_FullDuplex_IT | This example shows how to configure the GPIO, USART, DMA and SPI peripherals for transmitting bytes from/to an USART peripheral to/from an SPI peripheral (in slave mode) by using Interrupt mode through the STM32F2xx USART LL API (SPI is using DMA for receiving/transmitting characters sent from/received by USART). The peripheral initialization is done using LL unitary services functions for optimization purpose (performance and size). | X | - |

Table 1. STM32CubeF2 firmware examples (continued)

| Level | Module Name | Project Name | Description | STM32F207ZG -Nucleo | STM322xG -EVAL |
|--------------|--|-----------------------------------|---|------------------------|-------------------|
| Examples_LL | UTILS | UTILS_ConfigureSystemClock | This example describes how to use UTILS LL API to configure the system clock using PLL with HSI as source clock. The user application just needs to calculate PLL parameters using STM32CubeMX and to call the UTILS LL API. | X | - |
| | | UTILS_ReadDeviceInfo | This example describes how to read UID, Device ID and Revision ID and save them into a global information buffer. | X | - |
| | WWDG | WWDG_RefreshUntilUserEvent | This example describes how to configure the WWDG, periodically update the counter, and generate an MCU WWDG reset when a user button is pressed. The peripheral initialization is done using LL unitary services functions for optimization purpose (performance and size). | X | - |
| | Total number of examples_LL: 69 | | | | 69 |
| Examples_MIX | ADC | ADC_SingleConversion_TriggerSW_IT | This example describes how to use the ADC to perform a single ADC channel conversion, at each software start. This example uses the interrupt programming model (for programming models in Polling or DMA mode, refer to other examples). This example is based on the STM32F2xx ADC HAL and LL API (LL API usage for performance improvement). | X | - |
| | CRC | CRC_CalculateAndCheck | This example provides a description of how to use the CRC peripheral through the STM32F2xx CRC HAL and LL API (LL API used for performance improvement). The fixed generator polynomial used in CRC IP is CRC-32 (Ethernet) polynomial: 0x4C11DB7. | X | - |
| | DMA | DMA_FLASHToRAM | This example provides a description of how to use a DMA to transfer a word data buffer from Flash memory to embedded SRAM through the STM32F2xx DMA HAL and LL API (LL API used for performance improvement). | X | - |
| | I2C | I2C_OneBoard_ComSlave7_10bits_IT | This example describes how to perform I2C data buffer transmission/reception between one master and 2 slaves with different address sizes (7-bit or 10-bit) and different Max speed support (400Khz or 100 KHz). This example uses the STM32F2xx I2C HAL and LL API (LL API usage for performance improvement) and an interrupt. | X | - |



Table 1. STM32CubeF2 firmware examples (continued)

| Level | Module Name | Project Name | Description | STM32F207ZG -Nucleo | STM322xG -EVAL | |
|--------------|---|-----------------------------------|---|------------------------|-------------------|----------|
| Examples_MIX | PWR | PWR_STANDBY_RTC | This example shows how to enter Standby mode and wake up from this mode using an external RESET or the RTC wakeup Timer through the STM32F2xx RTC and RCC HAL and LL API (LL API usage for performance improvement). | X | - | |
| | | PWR_STOP | This example shows how to enter the system in STOP with Low-power regulator mode and wake up from this mode using external RESET or wakeup interrupt (all the RCC functions calls use RCC LL API for footprint and performance improvements). | X | - | |
| | SPI | SPI_FullDuplex_ComPolling | This example shows how to ensure SPI data buffer transmission/reception in Polling mode between two boards. | X | - | |
| | | SPI_HalfDuplex_ComPollingIT | This example shows how to ensure SPI data buffer transmission/reception between two boards by using Polling (LL Driver) an interrupt mode (HAL Driver). | X | - | |
| | TIM | TIM_6Steps | This example shows how to configure the TIM1 peripheral to generate 6 Steps PWM signal. The STM32F2xx TIM1 peripheral offers the possibility to program in advance the configuration for the next TIM1 outputs behavior (or step) and to change the configuration of all the channels at the same time. This operation is possible when the COM (commutation) event is used. This example is based on the STM32F2xx TIM HAL and LL API (LL API used for performance improvement). | X | - | |
| | | TIM_PWMInput | This example shows how to use the TIM peripheral to measure the frequency and duty cycle of an external signal. | X | - | |
| | UART | UART_HyperTerminal_IT | This example describes how to use an UART to transmit data (transmit/receive) between a board and an HyperTerminal PC application in Interrupt mode. This example provides a description of how to use USART peripheral through the STM32F2xx UART HAL and LL API (LL API used for performance improvement). | X | - | |
| | | UART_HyperTerminal_TxPolling_RxIT | This example describes how to use an UART to transmit data (transmit/receive) between a board and an HyperTerminal PC application both in Polling and Interrupt modes. This example provides a description of how to use USART peripheral through the STM32F2xx UART HAL and LL API (LL API used for performance improvement). | X | - | |
| | Total number of examples_mix: 12 | | | | 12 | 0 |

Table 1. STM32CubeF2 firmware examples (continued)

| Level | Module Name | Project Name | Description | STM32F207ZG -Nucleo | STM322xG -EVAL | |
|-----------------------------------|-------------|-----------------------------------|---|---|-------------------|---|
| Applications | Camera | Camera_To_USBDisk | This application provides a short description of how to use the DCMI to interface with camera module and display in continuous mode the picture on LCD and to save a picture in USB device. | - | X | |
| | Display | LCD_Paint | This application describes how to configure LCD touch screen and attribute an action related to configured touch zone and how to save BMP picture in SD Card. | - | X | |
| | EEPROM | EEPROM_Emulation | This application describes the software solution for substituting a standalone EEPROM by emulating the EEPROM mechanism using the on-chip Flash of STM32F207xx devices. | X | - | |
| | FatFs | FatFs_MultiDrives | FatFs_MultiDrives | This application provides a description on how to use STM32Cube firmware with FatFs middleware component as a generic FAT file system module, in order to develop an application exploiting FatFs offered features with multidrive (RAMDisk, uSD) configuration. | - | X |
| | | FatFs_RAMDisk | FatFs_RAMDisk | This application provides a description on how to use STM32Cube firmware with FatFs middleware component as a generic FAT file system module, in order to develop an application exploiting FatFs offered features with RAM disk (SRAM) drive configuration. | - | X |
| | | FatFs_RAMDisk_RTOS | FatFs_RAMDisk_RTOS | This application provides a description on how to use STM32Cube firmware with FatFs middleware component as a generic FAT file system module, in order to develop an application exploiting FatFs offered features with RAM disk (SRAM) drive in RTOS mode configuration. | - | X |
| | | FatFs_USBDisk | FatFs_USBDisk | This application provides a description on how to use STM32Cube firmware with FatFs middleware component as a generic FAT file system module and STM32 USB On-The-Go (OTG) host library, in High Speed (HS) modes (configured in FS), in order to develop an application exploiting FatFs offered features with USB disk drive configuration. | X | X |
| FatFs_USBDisk_MultipleAccess_RTOS | | FatFs_USBDisk_MultipleAccess_RTOS | This application provides a description on how to use STM32Cube firmware with FatFs middleware component as a generic FAT file system module, FreeRTOS as an RTOS module based on using CMSIS-OS wrapping layer common APIs, and also STM32 USB On-The-Go (OTG) host library, in both Full Speed (FS) and High Speed (HS) modes, in order to develop an application exploiting FatFs offered features with USB disk drive in RTOS mode configuration. | - | X | |



Table 1. STM32CubeF2 firmware examples (continued)

| Level | Module Name | Project Name | Description | STM32F207ZG -Nucleo | STM322xG -EVAL |
|--------------|-------------|---------------------------|---|------------------------|-------------------|
| Applications | FatFs | FatFs_USBDisk_RTOS | This application provides a description on how to use STM32Cube firmware with FatFs middleware component as a generic FAT file system module, FreeRTOS as an RTOS module based on using CMSIS-OS wrapping layer common APIs, and also STM32 USB On-The-Go (OTG) host library, in both Full Speed (FS) and High Speed (HS) modes, in order to develop an application exploiting FatFs offered features with USB disk drive in RTOS mode configuration. | - | X |
| | | FatFs_uSD | This application provides a description on how to use STM32Cube firmware with FatFs middleware component as a generic FAT file system module, in order to develop an application exploiting FatFs offered features with micro SD drive configuration. | - | X |
| | | FatFs_uSD_RTOS | This application provides a description on how to use STM32Cube firmware with FatFs middleware component as a generic FAT file system module, in order to develop an application exploiting FatFs offered features with micro SD drive in RTOS mode configuration. | - | X |
| | FreeRTOS | FreeRTOS_LowPower | This directory contains a set of source files that implement an application that uses message queues with CMSIS RTOS API. This application creates two threads. | - | X |
| | | FreeRTOS_Mutexes | This directory contains a set of source files that implement an application that uses mutexes with CMSIS RTOS API. This application creates three threads with different priorities and an access at the same mutex MutexHighPriorityThread() which has the highest priority, so executed first, then it grabs the mutex and sleeps for a short period to let the lower priority threads execution. When it has completed its demonstration functionality, it gives the mutex back before suspending itself. | - | X |
| | | FreeRTOS_Queues | This directory contains a set of source files that implement an application that uses message queues with CMSIS RTOS API. This application creates two threads that send and receive an incrementing number to/from a queue. | - | X |
| | | FreeRTOS_Semaphore | This directory contains a set of source files that implement an application that uses semaphores with CMSIS RTOS API. This application creates two threads that toggle LEDs through a shared semaphore. | - | X |
| | | FreeRTOS_SemaphoreFromISR | This directory contains a set of source files that implement an application that uses semaphore from ISR with CMSIS RTOS API. This application creates a thread that toggles LED through semaphore given from ISR. | - | X |
| | | | | | |

Table 1. STM32CubeF2 firmware examples (continued)

| Level | Module Name | Project Name | Description | STM32F207ZG -Nucleo | STM322xG -EVAL |
|------------------|---|-------------------------------|--|------------------------|-------------------|
| Applications | FreeRTOS | FreeRTOS_ThreadCreation | This directory contains a set of source files that implement a thread creation application using CMSIS RTOS API. This application creates two threads with the same priority, which executes in a periodic cycle of 15 seconds. | - | X |
| | | FreeRTOS_Timers | This directory contains a set of source files that implement an application that uses timers of CMSIS RTOS API. This application creates a thread that toggles LED2 every 400 ms, and a periodic timer that calls a callback function every 200 ms to toggle the LED1. | - | X |
| | LibJPEG | LibJPEG_Decoding | This application demonstrates how to read a jpeg file from the SDCard memory, decode it and display the final BMP image on the LCD. | - | X |
| | | LibJPEG_Encoding | This application demonstrates how to read BMP file from the micro SD, encode it, save the jpeg file in uSD Card then decode the jpeg file and display the final BMP image on the LCD. | - | X |
| | LwIP | LwIP_HTTP_Server_Netconn_RTOS | This application guides STM32Cube HAL API users to run a http server application based on Netconn API of LwIP TCP/IP stack. The communication is done with a web browser application in a remote PC. | X | X |
| | | LwIP_HTTP_Server_Raw | This application guides STM32Cube HAL API users to run a http server application based on Raw API of LwIP TCP/IP stack. The communication is done with a web browser application in a remote PC. | - | X |
| | | LwIP_HTTP_Server_Socket_RTOS | This application guides STM32Cube HAL API users to run a http server application based on Socket API of LwIP TCP/IP stack. The communication is done with a web browser application in a remote PC. | - | X |
| | | LwIP_IAP | This application guides STM32Cube HAL API users to run In-Application Programming (IAP) over Ethernet. | - | X |
| | | LwIP_TCP_Echo_Client | This application guides STM32Cube HAL API users to run TCP Echo Client application based on Raw API of LwIP TCP/IP stack. To run this application, on the remote PC, open a command prompt window. | - | X |
| | | LwIP_TCP_Echo_Server | This application guides STM32Cube HAL API users to run TCP Echo Server application based on Raw API of LwIP TCP/IP stack. To run this application, on the remote PC, open a command prompt window. | - | X |
| LwIP_TFTP_Server | This application guides STM32Cube HAL API users to run a tftp server demonstration for STM32F2xx devices. | - | X | | |


Table 1. STM32CubeF2 firmware examples (continued)

| Level | Module Name | Project Name | Description | STM32F207ZG -Nucleo | STM322xG -EVAL |
|--------------|-------------|--------------------------------------|--|------------------------|-------------------|
| Applications | LwIP | LwIP_UDPTCP_Echo_Server_Netconn_RTOS | This application guides STM32Cube HAL API users to run a UDP/TCP Echo Server application based on Netconn API of LwIP TCP/IP stack. To run this application, on the remote PC, open a command prompt window. | - | X |
| | | LwIP_UDP_Echo_Client | This application guides STM32Cube HAL API users to run a UDP Echo Client application based on Raw API of LwIP TCP/IP stack. To run this application, on the remote PC, open a command prompt window. | - | X |
| | | LwIP_UDP_Echo_Server | This application guides STM32Cube HAL API users to run UDP Echo Server application based on Raw API of LwIP TCP/IP stack. To run this application, on the remote PC, open a command prompt window. | - | X |
| | STemWin | STemWin>HelloWorld | This directory contains a set of source files that implements a simple "Hello World" application based on STemWin for STM32F2xx devices. | - | X |
| | | STemWin_SampleDemo | This directory contains a set of source files that implements a sample demonstration application allowing to show some of the STemWin Library capabilities on STM32F2xx devices. | - | X |
| | USB_Device | AUDIO_Standalone | This application is a part of the USB Device Library package using STM32Cube firmware. It describes how to use the USB device application based on the AUDIO class implementation of an audio streaming (Out: Speaker/Headset) capability on the STM32F2xx devices. | - | X |
| | | CDC_Standalone | This application is a part of the USB Device Library package using STM32Cube firmware. It describes how to use the USB device application based on the Device Communication class (CDC) following the PSTN sub protocol in the STM32F2xx devices using the OTG-USB and UART peripherals. | - | X |
| | | CustomHID_Standalone | This application is a part of the USB Device Library package using STM32Cube firmware. It describes how to use USB device application based on the Custom HID Class on the STM32F2xx devices. | - | X |
| | | DFU_Standalone | This application is a part of the USB Device Library package using STM32Cube firmware. It describes how to use the USB device application based on the Device Firmware Upgrade (DFU) on the STM32F2xx devices. | X | X |

Table 1. STM32CubeF2 firmware examples (continued)

| Level | Module Name | Project Name | Description | STM32F207ZG -Nucleo | STM322xG -EVAL |
|--------------|-------------|--------------------------|---|------------------------|-------------------|
| Applications | USB_Host | DualCore_Standalone | This application is a part of the USB Device Library package using STM32Cube firmware. It describes how to use USB device application based on the STM32F2xx multi core support feature integrating Mass Storage (MSC) and Human Interface (HID) in the same project. | - | X |
| | | HID_Standalone | This application is a part of the USB Device Library package using STM32Cube firmware. It describes how to use the USB device application based on the Human Interface (HID) on the STM32F2xx devices. | X | X |
| | | MSC_Standalone | This application is a part of the USB Device Library package using STM32Cube firmware. It describes how to use the USB device application based on the Mass Storage Class (MSC) on the STM32F2xx devices. | - | X |
| | | AUDIO_Standalone | This application is a part of the USB Host Library package using STM32Cube firmware. It describes how to use the USB host application based on the Audio OUT class on the STM32F2xx devices. | - | X |
| | | CDC_Standalone | This application is a part of the USB Host Library package using STM32Cube firmware. It describes how to use the USB host application based on the Communication Class (CDC) on the STM32F2xx devices. | - | X |
| | | DualCore_Standalone | This application is a part of the USB Host Library package using STM32Cube firmware. It describes how to use the USB host application based on the STM32F2xx multi core support feature integrating Mass Storage (MSC) and Human Interface (HID) in the same project. | - | X |
| | | DynamicSwitch_Standalone | This application is a part of the USB Host Library package using STM32Cube firmware. It describes how to use dynamically a switch, on the same port, between available USB host applications on the STM32F2xx devices. | - | X |
| | | FWUpgrade_Standalone | This application is a part of the USB Host Library package using STM32Cube firmware. It describes how to use the USB host application based on the In-Application programming (IAP) on the STM32F2xx devices. | - | X |



Table 1. STM32CubeF2 firmware examples (continued)

| Level | Module Name | Project Name | Description | STM32F207ZG -Nucleo | STM322xG -EVAL | |
|---|---|----------------|--|---|-------------------|-----------|
| Applications | USB_Host | HID_RTOS | This application is a part of the USB Host Library package using STM32Cube firmware. It describes how to use the USB host application based on the Human Interface Class (HID) on the STM32F2xx devices. | - | X | |
| | | HID_Standalone | This application is a part of the USB Host Library package using STM32Cube firmware. It describes how to use the USB host application based on the Human Interface Class (HID) on the STM32F2xx devices. | X | X | |
| | | MSC_RTOS | This application is a part of the USB Host Library package using STM32Cube firmware. It describes how to use the USB host application based on the Mass Storage Class (MSC) on the STM32F2xx devices in RTOS mode configuration. | - | X | |
| | | MSC_Standalone | This application is a part of the USB Host Library package using STM32Cube firmware. It describes how to use the USB host application based on the Mass Storage Class (MSC) on the STM32F2xx devices. | X | X | |
| | mbedTLS | SSL_Client | This application describes how to run an SSL client application based on mbedTLS crypto library and LwIP TCP/IP stack on STM32F2 family. | - | X | |
| | | SSL_Server | This application guides STM32Cube HAL API users to run an SSL Server application based on mbedTLS crypto library and LwIP TCP/IP stack. | - | X | |
| | Total number of applications: 56 | | | | 7 | 49 |
| | Demonstration | - | Demo | The provided demonstration firmware based on STM32Cube helps the user to discover STM32 Cortex-M devices that can be plugged on a STM32 Nucleo board. | X | - |
| Total number of demonstration: 1 | | | | 1 | 0 | |
| Total number of projects: 246 | | | | 118 | 128 | |

Revision history

Table 2. Document revision history

| Date | Revision | Changes |
|-------------|----------|--|
| 23-Jul-2015 | 1 | Initial release. |
| 26-Nov-2015 | 2 | Updated Table 1: STM32CubeF2 firmware examples adding the list of examples and applications provided with the STM32F207ZG-Nucleo board. |
| 16-Mar-2017 | 3 | Updated Figure 1: STM32CubeF2 firmware components . Updated STM32CubeF2 examples adding examples_LL and examples_MIX. Updated Table 1: STM32CubeF2 firmware examples adding the list of examples, examples_LL, examples_MIX provided with the STM32F207ZG-Nucleo and the STM322xG-EVAL boards. |

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