List of software parameters to check in case of switch:

From M24C64M-FCU to M24C64X-FCU

Information

- This document provides a list of software parameters to check in case of switch from M24C64M-FCU to M24C64X-FCU.

List of software parameters to check

Parameter	M24C64M-FCU	M24C64X-FCU	Action needed	Remarks
Datasheet	M24C64M-FCU	M24C64X rev 7	FYI	Available on ST.COM web site : - M24C64X-FCU - M24C64M-FCU
Max Operating Frequency	Fast Mode Plus	1MHz	YES	If the application is running with operating frequency > 400KHz, on M24C64X the max authorized Tlow is 700ns.
Chip enable address	Fixed : 100	Default : 000	YES	At factory delivery not compatible but M24C64X offers the configurable chip enable address C2,C1,C0 (factory delivery value = 000). The chip enable address (C2,C1,C0) can be modified in the chip enable register. The register can be written & read with specifics command “write chip enable register“ & “read chip enable register“.
Chip enable register management	Not Available	Available	Yes	With the M24C64X, the user write & read the *chip enable register* with the specific commands “write chip enable register” & ”read chip enable register”. Can be updated: - The C2,C1,C0 bits for the chip enable address - The SWP bit for the protection of the memory array
Software Write Protect	Not Available	Available (Protection of full plan)	Yes	On M24C64X,full protection of the memory array by software. The full protection of the memory can be activated with the SWP bit of the chip enable register. Once activated, the memory array is in read-only for ever. The register can be written & read with specifics command “write chip enable register“ & “read chip enable register“.

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