Features

- STM32F429ZIT6 microcontroller featuring 2 Mbytes of Flash memory, 256 Kbytes of RAM in an LQFP144 package
- 2.4" QVGA TFT LCD
- USB OTG with Micro-AB connector
- I3G4250D, ST MEMS motion sensor 3-axis digital output gyroscope
- Six LEDs:
  - LD1 (red/green) for USB communication
  - LD2 (red) for 3.3 V power-on
  - Two user LEDs: LD3 (green), LD4 (red)
  - Two USB OTG LEDs: LD5 (green) \( V_{BUS} \) and LD6 (red) OC (over-current)
- Two push-buttons (user and reset)
- 64-Mbit SDRAM
- Extension header for LQFP144 I/Os for a quick connection to the prototyping board and an easy probing
- On-board ST-LINK/V2-B
- USB functions:
  - Debug port
  - Virtual COM port
  - Mass storage
- Mbed Enabled™ (see http://mbed.org)
- Board power supply: through the USB bus or from an external 3 V or 5 V supply voltage
- Comprehensive free software including a variety of examples, part of STM32CubeF4 MCU Package or STSW-STM32138, for using legacy standard libraries

Description

The 32F429IDISCOVERY Discovery kit leverages the capabilities of the STM32F429 high-performance microcontrollers, to allow users to develop rich applications easily with advanced graphic user interfaces.
1 Ordering information

To order the Discovery kit with the STM32F429ZI microcontroller, refer to Table 1.

Table 1. Ordering information

<table>
<thead>
<tr>
<th>Order code</th>
<th>Board reference</th>
<th>User manual</th>
<th>Target STM32</th>
</tr>
</thead>
<tbody>
<tr>
<td>STM32F429I-DISC1&lt;sup&gt;(1)&lt;/sup&gt;</td>
<td>MB1075</td>
<td>UM1670</td>
<td>STM32F429ZIT6</td>
</tr>
</tbody>
</table>


1.1 Product marking

Evaluation tools marked as ‘ES’ or ‘E’ are not yet qualified and therefore they are not ready to be used as reference design or in production. Any consequences deriving from such usage will not be at ST charge. In no event, ST will be liable for any customer usage of these engineering sample tools as reference design or in production.

‘E’ or ‘ES’ marking examples of location:

- On the targeted STM32 that is soldered on the board (for illustration of STM32 marking, refer to the section “Package information” of the STM32 datasheet available at www.st.com).
- Next to the evaluation tool ordering part number that is stuck or silk-screen printed on the board.

1.2 Codification

The meaning of the codification is explained in Table 2.

Table 2. Codification explanation

<table>
<thead>
<tr>
<th>32XXYYZDISCOVERY</th>
<th>Description</th>
<th>Example: 32F429IDISCOVERY</th>
</tr>
</thead>
<tbody>
<tr>
<td>32XX</td>
<td>MCU series in STM32 32-bit Arm Cortex MCUs</td>
<td>STM32F4 Series</td>
</tr>
<tr>
<td>YY</td>
<td>MCU product line in the series</td>
<td>STM32F429</td>
</tr>
<tr>
<td>Z</td>
<td>STM32 Flash memory size: – I for 2 Mbytes</td>
<td>2 Mbytes</td>
</tr>
<tr>
<td>DISCOVERY</td>
<td>Discovery kit</td>
<td>Discovery kit</td>
</tr>
</tbody>
</table>
2 Development environment

2.1 System requirements

- Windows® OS (7, 8, or 10)
- USB Type-A to Mini-B cable

2.2 Development toolchains

- IAR™ - EWARM\textsuperscript{(a)}
- Keil® - MDK-ARM\textsuperscript{(a)}
- STMicroelectronics - STM32CubeIDE
- Arm® Mbed™\textsuperscript{(b)} online

2.3 Demonstration software

The demonstration software, included in the STM32Cube MCU Package, is preloaded in the STM32 Flash memory for easy demonstration of the device peripherals in standalone mode. The latest versions of the demonstration source code and associated documentation can be downloaded from the www.st.com/stm32f4-discovery web page.

\textsuperscript{a} On Windows® only.

\textsuperscript{b} Arm and Mbed are registered trademarks or trademarks of Arm Limited (or its subsidiaries) in the US and or elsewhere.
# Revision history

## Table 3. Document revision history

<table>
<thead>
<tr>
<th>Date</th>
<th>Revision</th>
<th>Changes</th>
</tr>
</thead>
<tbody>
<tr>
<td>06-Sep-2013</td>
<td>1</td>
<td>Initial version.</td>
</tr>
<tr>
<td>29-Sep-2014</td>
<td>2</td>
<td>Updated Features and Description to introduce STM32cubeF4 and STSW-STM32138. Updated ST MEMS feature. Updated System requirements and Development toolchains.</td>
</tr>
<tr>
<td>28-Oct-2016</td>
<td>4</td>
<td>Updated Features and Description to inform that the new STM32F429I-DISC1 order code has replaced the old STM32F429I-DISCO order code.</td>
</tr>
</tbody>
</table>
| 22-Apr-2020   | 5        | Removed all references to obsolete STM32F429I-DISCO. Updated ST MEMS details in Features. Reorganized the entire document:  
|               |          | – Updated Features, Description, Ordering information, and Development toolchains  
|               |          | – Added Codification                                                                                                                   |