STM3210E-EVAL evaluation board

Features

■ Three 5 V power supply options:
  – power jack
  – USB connector
  – daughterboard
■ Boot from user Flash, system memory or SRAM
■ I2S audio DAC, stereo audio jack
■ 128 Mbyte MicroSD Card™ or bigger
■ Both A and B type smartcard support
■ 64 or 128 Mbit serial Flash, 512 Kx16 SRAM, 512 Mbit or 1 Gbit NAND Flash and 128 Mbit NOR Flash
■ I2C/SMBus compatible serial interface temperature sensor
■ Two RS-232 channels with RTS/CTS handshake support on one channel
■ IrDA transceiver
■ USB 2.0 full speed connection
■ CAN 2.0A/B compliant connection
■ Inductor motor control connector
■ JTAG and trace debug support
■ 240x320 TFT color LCD
■ Joystick with 4-direction control and selector
■ Reset, wakeup, tamper and user buttons
■ 4 color LEDs
■ RTC with backup battery

Description

The STM3210E-EVAL evaluation board is a complete development platform for STMicroelecronic's ARM Cortex-M3 core-based STM32F103ZET6 or STM32F103ZGT6 microcontroller. The range of hardware features on the board help you to evaluate all peripherals (LCD, SPI Flash, USART, IrDA, USB, audio, CAN bus, smartcard, MicroSD Card, NOR Flash, NAND Flash, SRAM, temperature sensor, audio DAC and motor control) and develop your own applications.

Extension headers make it easy to connect a daughterboard or wrapping board for your specific application.

Table 1. Device summary

<table>
<thead>
<tr>
<th>Order code</th>
<th>Reference</th>
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<tbody>
<tr>
<td>STM3210E-EVAL</td>
<td>STM32F103ZET6 or STM32F103ZGT6 evaluation board</td>
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**STM3210E-EVAL hardware block diagram**

*Figure 1* is a block diagram of the STM3210E-EVAL hardware.

![Hardware block diagram](image)

**Demonstration software**

Demonstration software is preloaded in the board’s Flash memory for easy demonstration of the device peripherals in stand-alone mode. For more information and to download the latest version (version 1.1 or later is required), refer to www.st.com.

**Revision history**

<table>
<thead>
<tr>
<th>Date</th>
<th>Revision</th>
<th>Changes</th>
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<tbody>
<tr>
<td>06-Aug-2010</td>
<td>1</td>
<td>Initial release.</td>
</tr>
<tr>
<td>29-Oct-2010</td>
<td>2</td>
<td>Modified microcontroller.</td>
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</tbody>
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