Features

- Ultra-low-power wireless STM32WBA52CG microcontroller based on the Arm® Cortex®-M33 core, featuring 1 Mbyte of flash memory and 128 Kbytes of SRAM in a UFQFPN48 package
- MCU RF board (MB1863):
  - 2.4 GHz RF transceiver supporting Bluetooth® specification v5.3
  - Arm® Cortex® M33 CPU with TrustZone®, MPU, DSP, and FPU
  - Integrated PCB antenna
- Three user LEDs
- Three user and one reset push-buttons
- Board connectors:
  - USB Micro-B
  - ARDUINO® Uno V3 expansion connector
  - ST morpho headers for full access to all STM32 I/Os
- Flexible power-supply options: ST-LINK USB VBUS or external sources
- On-board STLINK-V3MODS debugger/programmer with USB re-enumeration capability: mass storage, Virtual COM port, and debug port
- Comprehensive free software libraries and examples available with the STM32CubeWB MCU Package
- Support of a wide choice of Integrated Development Environments (IDEs) including IAR Embedded Workbench®, MDK-ARM, and STM32CubeIDE

Description

NUCLEO-WBA52CG is a Bluetooth® Low Energy wireless and ultra-low-power board embedding a powerful and ultra-low-power radio compliant with the Bluetooth® Low Energy SIG specification v5.3.

The ARDUINO® Uno V3 connectivity support and the ST morpho headers allow the easy expansion of the functionality of the STM32 Nucleo open development platform with a wide choice of specialized shields.
1 Ordering information

To order the NUCLEO-WBA52CG board, refer to Table 1. For a detailed description, refer to its user manual on the product web page. Additional information is available from the datasheet and reference manual of the target microcontroller.

<table>
<thead>
<tr>
<th>Order code</th>
<th>Board reference</th>
<th>User manual</th>
<th>Target STM32</th>
</tr>
</thead>
<tbody>
<tr>
<td>NUCLEO-WBA52CG</td>
<td>• MB1801(1)</td>
<td>UM3103</td>
<td>STM32WBA52CG</td>
</tr>
<tr>
<td></td>
<td>• MB1863(2)</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

1. Mezzanine board
2. MCU RF board

1.1 Product marking

The stickers located on the top or bottom side of all PCBs provide product information:

- First sticker: product order code and product identification, generally placed on the main board featuring the target device.
  Example:
  
  ![Product order code
  Product identification]

- Second sticker: board reference with revision and serial number, available on each PCB.
  Example:
  MBxxxx-Variant-yzz
  syywwxxxxxx

On the first sticker, the first line provides the product order code, and the second line the product identification.

On the second sticker, the first line has the following format: “MBxxxx-Variant-yzz”, where “MBxxxx” is the board reference, “Variant” (optional) identifies the mounting variant when several exist, “y” is the PCB revision, and “zz” is the assembly revision, for example B01. The second line shows the board serial number used for traceability.

Parts marked as “ES” or “E” are not yet qualified and therefore not approved for use in production. ST is not responsible for any consequences resulting from such use. In no event will ST be liable for the customer using any of these engineering samples in production. ST’s Quality department must be contacted prior to any decision to use these engineering samples to run a qualification activity.

“ES” or “E” marking examples of location:

- On the targeted STM32 that is soldered on the board (for an illustration of STM32 marking, refer to the STM32 datasheet Package information paragraph at the www.st.com website).
- Next to the evaluation tool ordering part number that is stuck, or silk-screen printed on the board.

Some boards feature a specific STM32 device version, which allows the operation of any bundled commercial stack/library available. This STM32 device shows a “U” marking option at the end of the standard part number and is not available for sales.

To use the same commercial stack in their applications, the developers might need to purchase a part number specific to this stack/library. The price of those part numbers includes the stack/library royalties.
1.2 Codification

The meaning of the codification is explained in Table 2.

<table>
<thead>
<tr>
<th>NUCLEO-WBXXYYRZ</th>
<th>Description</th>
<th>Example: NUCLEO-WBA2CG</th>
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<tbody>
<tr>
<td>XX</td>
<td>MCU series in STM32 32-bit Arm Cortex MCUs</td>
<td>STM32WBA Series</td>
</tr>
<tr>
<td>YY</td>
<td>MCU product line in the series</td>
<td>STM32WBAx2 product line</td>
</tr>
</tbody>
</table>
| R               | STM32 package pin count:  
  • C for 48 pins | 48 pins |
| Z               | STM32 flash memory size:  
  • G for 1 Mbyte | 1 Mbyte |
2 Development environment

The NUCLEO-WBA52CG board runs with the STM32WBA52CG 32-bit microcontroller based on the Arm® Cortex®-M33 processor.

Note: Arm is a registered trademark of Arm Limited (or its subsidiaries) in the US and/or elsewhere.

2.1 System requirements

- Multi-OS support: Windows® 10, Linux® 64-bit, or macOS®
- USB Type-A or USB Type-C® to Micro-B cable

Note: macOS® is a trademark of Apple Inc., registered in the U.S. and other countries and regions.
Linux® is a registered trademark of Linus Torvalds.
Windows is a trademark of the Microsoft group of companies.

2.2 Development toolchains

- IAR Systems® - IAR Embedded Workbench®
- Keil® - MDK-ARM®
- STMicroelectronics - STM32CubeIDE

1. On Windows® only.
## Revision history

**Table 3. Document revision history**

<table>
<thead>
<tr>
<th>Date</th>
<th>Revision</th>
<th>Changes</th>
</tr>
</thead>
<tbody>
<tr>
<td>13-Jan-2023</td>
<td>1</td>
<td>Initial release.</td>
</tr>
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</table>

NUCLEO-WBA52CG

[43x729]Revision history

[252x705]Table 3. Document revision history

[147x687]Date

[216x694]Revision

[272x694]Changes

[133x670]13-Jan-2023

[216x677]1

[272x677]Initial release.