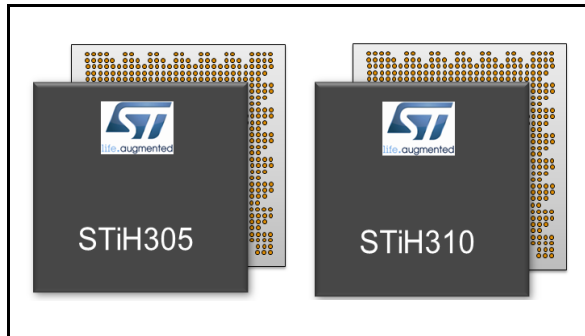


Cannes - ARM-based, hybrid set-top box platform

Data brief



Description

ST's STiH305 and STiH310 system-on-chips, belonging to its new 'Cannes' family, use leading application processor architecture to provide powerful set-top box and connected client platforms. These SoCs support integrated broadcast and broadband services, combined with the latest set-top box middleware and broadband software solutions.

The STiH310 supports HEVC decoding, and therefore permits IP service providers a means of offering HD content to a greater number of customers.

Features

- Dual core SMP ARM® applications CPU delivering 7500 DMIPS, plus a quad-core GPU for true 3D graphics
- Versatile full HD video decoding (including HEVC for the STiH310), combined with Faroudja® video processing technology
- Advanced security supporting concurrent conditional access and DRM, to protect premium broadcast content
- Wide connectivity, including USB 3.0, PCI-e, SATA and Gigabit Ethernet
- Dedicated interfaces to a range of companion front-end solutions, including MoCA 2.0, DOCSIS 3.0, satellite and 802.11ac WiFi devices
- 28 nm process technology chip

1 Revision history

Table 1. Document revision history

Date	Revision	Changes
18-Dec-2012	1	Initial revision.
30-Aug-2013	2	<ul style="list-style-type: none">– Updated front cover bullets (<i>Features</i>).– Updated the front cover description (<i>Description</i>).– Updated <i>Chapter 1</i>.– Updated the block diagram (<i>Figure 1</i>).
16-Dec-2013	3	<ul style="list-style-type: none">– Updated front cover bullets (<i>Features</i>).– Updated front cover description (<i>Description</i>).

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