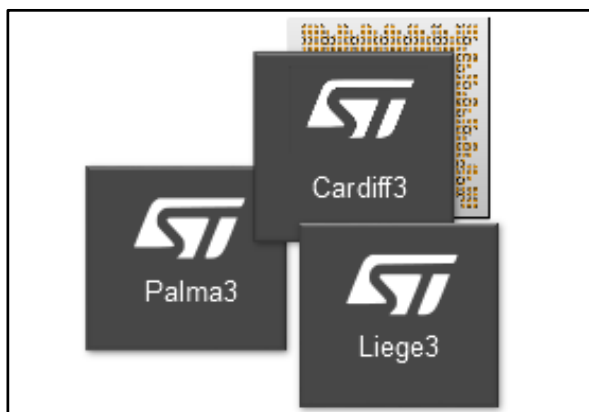


## ARM Cortex-based, HEVC 1080p, broadcast and interactive set-top box SoCs

Data brief



### Features

- Complete family with versions for IPTV markets (Liege3), satellite markets (Cardiff3) and cable markets (Palma3):
  - Integrated DVB-S2/S2X demodulator with DVB-S and DTV legacy support (STiH33x)
  - Integrated DVB-C demodulator (STiH37x)
- ARM® Cortex™ application CPU offering up to 5000 DMIPs
- High-performance GPU for fluid 3D graphics (ARM® Mali™-400)
- DDR3/3L 16-bit interface running at up to 1066 MHz (DDR3-2133)
- Video decoding:
  - HEVC Main 10 @ L4.1 (1080p60)
  - H.264 AVC, @ L4.2 (1080p60)
  - H.264 MVC and SHP @ L4.1 (1080p30L30R)
  - VC-1, MPEG4, MPEG2, AVS, AVS+
- Web-based content decoding: Flash, DivX, Xvid, MJPEG, WMV
- HDMI-TX 1.4b/2.0a @ 1080p60 with HDCP 1.4 and 2.2
- High-quality Faroudja video post-processing, including support for Blu-ray HDR10 content
- Generation 4 security for concurrent CA/DRM support, including schemes such as NOCS 3.0, NSK 2.1, SVP, DTCP-IP, PlayReady, DVB-CPCM, DivX, Marlin, and others
- Connectivity:
  - 1 x USB 2.0 port
  - 1 x USB 3.0 port
  - 1 x PCIe port
  - 1 x SD card
  - 1 x eMMC
  - 1 x Smartcard
  - 1 x Ethernet PHY
  - 1 x RGMII
  - 4 x input transport streams
  - 1 x DVB-CI+ 1.4 interface

### Description

The Liege3/Cardiff3/Palma3 family of system-on-chips (SoCs) support a wide variety of broadcast and interactive set-top box platforms for IPTV, satellite and cable markets.

These devices feature a common architecture based on a leading ARM® application processor and GPU. This architecture, employed across a range of ST SoCs (STiH3xx and STiH4xx series), enables scalable, reusable support of the latest middleware and software solutions built on Linux, RDK or Android operating systems.

The Liege3/Cardiff3/Palma3 devices support full HD, high-efficiency video coding (HEVC) reducing memory bandwidth for video distribution. In addition, the SoC family offers advanced interfaces that can add benefit to platforms, such as eMMC (for time-shifting), PCIe (for high-performance Wi-Fi), USB 3.0 (for high-speed storage solutions) and DVB-S2X (for greater satellite transponder bandwidth reduction).

## Revision history

**Table 1: Document revision history**

Date	Version	Changes
02-Sep-2015	1	Initial release.
04-Sep-2015	2	Minor grammatical changes to Features and Description sections.

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