

## Advanced HD application processor with 3D graphics acceleration and ARM Cortex-A9 SMP CPU

Data brief

### Features

- ARM Cortex-A9, dual core CPU, SMP NEON SIMD engine yielding up to 6000 DMIPS
- ARM Mali-400 quad core GPU yielding up to 1600 Mpixels/s fill rate and supporting up to 1080p60 or 1080p30 stereoscopic 3D graphics
- Dual HD H.264/VC-1/AVS/MPEG2 video decoder or SVC video decode
- MVC video decode for 3DTV: supports up to 1080p60L/60R, 3D video formats
- Dual HD internet video decoding: real video 8/9/10, DivX, Xvid, MP4p2, Adobe® Flash videos
- Single HD internet video decoding: WebM/VP8
- H.264 video encoder up to 1080p60 HD resolution with video preprocessing; deinterlacing, HQ resizing and TNR
- High-quality of image with Faroudja video
- ST proprietary multi-compartmental security IP and DRM processor

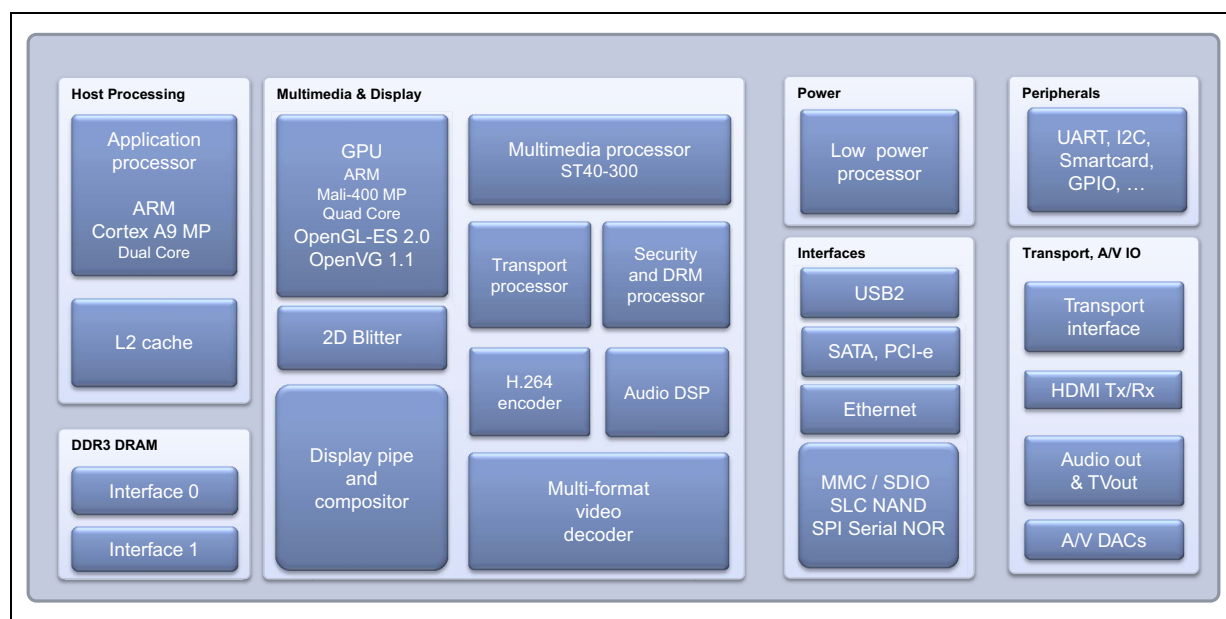
- Dual audio DSP: supports Dolby MS11
- Integrated low power processor: < 30 mW passive standby mode

### Description

The new STiH416 SoC combines the best of STMicroelectronics' leading set-top box system expertise and silicon process capability with ARM's exemplary core architecture.

The STiH416 SoC provides high security and system integration, functionalities tailored to both the broadcast and broadband ecosystems, accessibility for open platform software applications, eye-popping performance and unequalled power efficiency.

This combination of incredible performance, a future-proof architecture, and rich software ecosystems allow operators to offer a rich variety of innovative and value-added services to their subscribers.



# 1 Introduction

The STiH416 application processor integrates in a single IC, multi-stream transport demultiplexing, dual core SMP application CPUs, a real-time CPU, A/V decode, A/V encode/transcode, video processing, true 3D graphics and display, advanced security, peripherals, audio/video DACs, digital A/V outputs, HDMI output and input, dual eSATA2 ports, PCI-e, quadruple USB ports, dual Ethernet controllers (GbE capable), SLC NAND Flash controller and an SDIO/SD-MMC card controller.

| Features   | Benefits   |
|--|--|
| Dual-core, ARM Cortex-A9 applications CPU, ST40 multimedia real-time processor, ST231 and DRM processor.   | Brings full open Internet into the living room.<br>Very high host processor performance, extensive connectivity, supports wide software ecosystem.   |
| Integrated graphics processing unit (GPU); Programmable Vertex (geometry) processor, and quadruple fragment (pixel) processors, accelerated four times full scene anti-aliasing (4 × FSA).                           | High-performance 3D GPU supporting up to 1080p60 3D graphics or 1080p30L30R stereoscopic 3D graphics for blending with 3D stereoscopic video displays.<br>Advanced 3D user interface.<br>Drive 3D casual and semi core gaming. |
| Dual video decoder (latest generation) with an ST231-based multi-codec capable controller, coupled with a very high-quality video display pipeline and 1080p120 display compositor.                                  | Further extends broadcast services to support internet video formats.<br>Full HD Full Motion 1080p120 3DTV, MVC, SVC and so on.  |
| H.264 video encoder capable of encoding up to HD resolutions of 1080p60 (or 2 x 720p60 or 4 x 720p30); includes preprocessor video pipeline that can resize, deinterlace and noise filter the video before encoding. | High-quality transcoded video, whatever the input source resolution / format and transcoded output resolution, up to HD.<br>Adaptive streaming server in home network.   |
| HDMI 1.4b receiver input.  | Allows input of high-quality digital video from other sources to switch to the HDMI TV output or combine with application video / graphics to master final output.   |
| Latest generation transport/security subsystem, with enhanced performance for DVR, client/server-based, home networks up to HD 1080p60 video encoding.   | Distributes content securely around the home.<br>Audio and video transcoding.  |
| Dedicated security / DRM processor.  | Independent security / DRM processor allows easy integration of firmware based DRM schemes.  |
| Quadruple USB 2.0 hosts, dual e-SATA2, dual Gbit Ethernet MAC with MII/RMII/TMII/GMII/RGMII interfaces, PCI-e, SLC 1-bit and multi-bit ECC raw NAND Flash controller, SD-MMC/SDIO interface.                         | Extensive high-speed connectivity for the widest range of application peripherals, such as Flash drives, external HDDs, Gigabit Ethernet, home network controllers (such as MoCA®, Wi-Fi), DOCSIS® modem and memory cards.     |

## 2 Revision history

Table 1. Document revision history

| Date        | Revision | Changes  |
|-------------|----------|--|
| 03-Jan-2012 | 1        | Initial release.   |
| 25-Jul-2013 | 2        | <ul style="list-style-type: none"><li>– Updated the Introduction (<a href="#">Chapter 1</a>).</li><li>– Updated the Features and Benefits table <a href="#">on page 2</a>.</li></ul> |

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