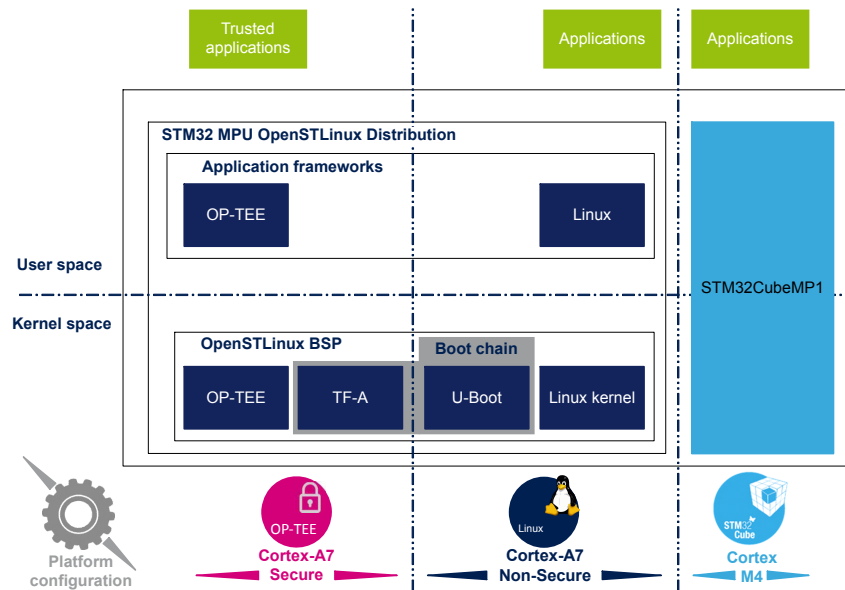


STM32CubeMP1 package



Product status link

[STM32CubeMP1](#)


Features

- STM32CubeMP1 consists of:
 - The hardware abstraction layer (HAL), enabling portability between different STM32 devices via standardized API calls
 - The low-layer API (LL), a light-weight, optimized, expert oriented set of APIs designed for runtime efficiency and so enhanced performance
 - A collection of middleware components (such as Freestones and Open Amp)
 - A collection of examples, running on STM32MP15xx-EV1 Evaluation boards and STM32MP15xx-DKx Discovery board and allowing demonstration of a basic implementation of features from a set of peripherals.

Description

The STM32CubeMP1 firmware provides BSP, HAL, middlewares and applications packages in source code for development on Arm® Cortex®-M processor.

1 General information

The STM32CubeMP1 package runs on STM32 MPUs based on Arm® Cortex® cores.

Note: Arm is a registered trademark of Arm Limited (or its subsidiaries) in the US and/or elsewhere.



1.1 Ordering information

STM32CubeMP1 is available for free download from the www.st.com website.

1.2 License

STM32CubeMP1 is delivered under the *Mix Ultimate Liberty+OSS+3rd-party V1* software license agreement (SLA0048).

The package contains a set of middleware components with the corresponding examples. They are delivered in very permissive license terms:

- CMSIS RTOS implementation with FreeRTOS™ open source solution
- OpenAMP™ open source solution is a library implementing the remote processor service framework (RPMsg) which is a virtio-based messaging bus that allows a local processor to communicate with remote processors available on the system.

Revision history

Table 1. Document revision history

Date	Version	Changes
14-Feb-2019	1	Initial release.

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