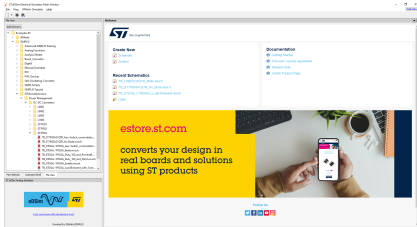


eDSim: fast and powerful electrical simulation software for SMPS and analog ICs



Features

- Built-in interface with [eDesignSuite](#)
- SMPS circuit full modeling using ST components
- Time domain simulation
- Control loop stability
- Predefined application use-case simulation
- Schematic editor for customization
- Waveform viewer with measurement capabilities

Description

eDSim is a fast and powerful electrical simulation tool for SMPS and analog ICs, integrated in the [eDesignSuite](#) tool. It features enhanced accuracy and higher convergence speed for SMPS, enabling a simulation time 10-50x faster than the classical analog SPICE simulators.

The tool is a specific version of the SIMPLIS/SIMetrix simulation environment paired with ST model components, for a full electrical simulation with no limitation of nodes and circuit size.

Design your analog circuit using the [eDesignSuite](#) engine, display a preview of it in few seconds with full annotated schematic and BOM, and then run the electrical simulation through [eDSim](#) to get fast and accurate simulations and reliable design validation, thus reducing the effort and risks related to hardware prototyping.

With the [eDSim](#) tool you can also create your schematic from scratch using ST models or simulate your SMPS and analog ICs from a list of predefined ST application schematics - test benches, that you can partially modify according to your needs.

Product summary	
Fast and powerful electrical simulation software for DC-DC switching converters	STSW-eDSim
Easy-to-use comprehensive software suite	eDesignSuite
Applications	DC-DC Converters

1 General information

Based on the SIMPLIS/SIMetrix AE version, the eDSim simulator software tool allows full simulations of all the ST encrypted models and applications supported, with the option of applying additional customizations as listed below.

For SIMPLIS:

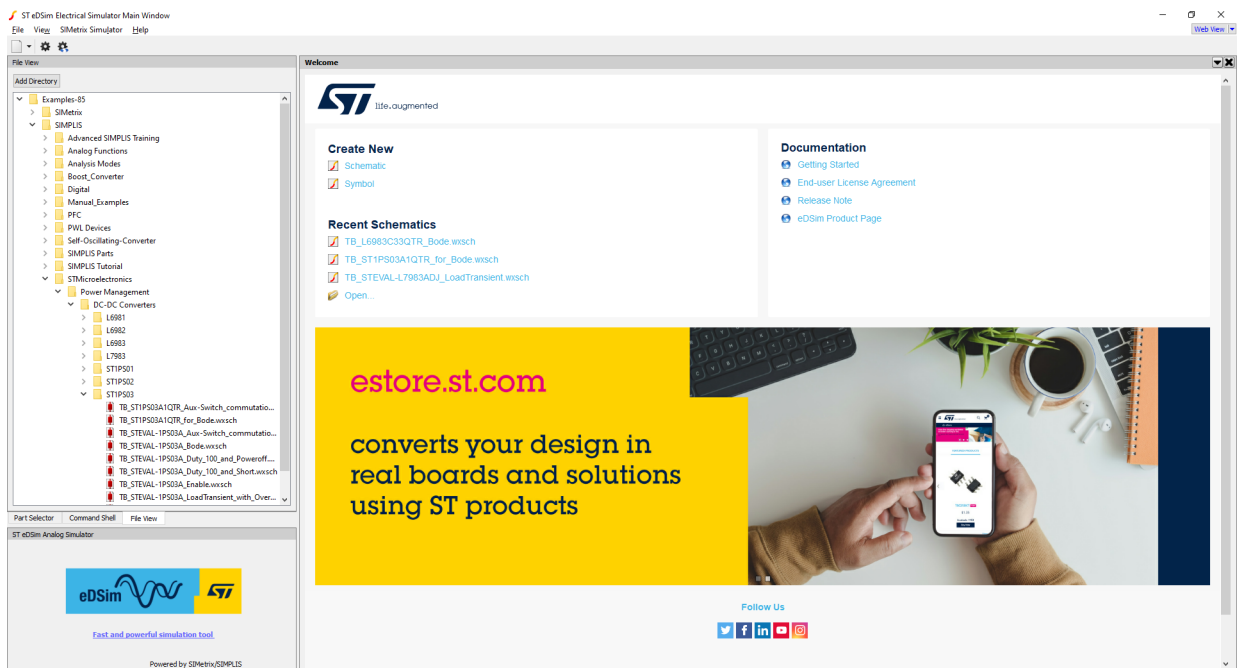
- A maximum of 15 state variables. Each capacitor or inductor requires one state variable. Each time-varying or small-signal AC source requires one state variable, except for sinusoidal or cosinusoidal sources, which require two state variables per source
- A maximum of 10 capacitors or inductors combined
- A maximum of 6 switches (simple or transistor)
- A maximum of 6 logic gates
- A maximum of 26 states. Each PWL element, switch, time-varying source, and logic gate requires one state
- A maximum of 100 new topologies, which are enough for simple switching circuits that use simple models only. More complex circuits or circuits that have more complicated models may exceed this limit

For SIMetrix:

- 140 analog nodes (internal and external)
- 360 digital nodes
- 720 digital ports
- 300 digital components
- 360 digital outputs

eDSim requires Windows® 10 or higher 64-bit edition (Home, Pro, or Enterprise).

Figure 1. eDSim main window



Revision history

Table 1. Document revision history

Date	Version	Changes
19-Apr-2022	1	Initial release.
02-May-2022	2	Updated title, cover page description, and Section 1 General information .

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