

Firmware for STSW-IFAPGUI and X-NUCLEO-OUT02A1 expansion board

Features

- Full control of the [X-NUCLEO-OUT02A1](#) expansion board via the [STSW-IFAPGUI](#)
- Control of single and dual (daisy chaining) applications
- Per-channel output switching frequency setting
- Per-channel switching duty cycle setting
- Per-channel thermal fault diagnostics
- Embedded RF communication fault diagnostics
- Process side UVLO diagnostics
- Process side power good diagnostics

Description

The [STSW-OUT02](#) is a binary file designed to run on STM324xx Nucleo boards and able to interface with the [X-NUCLEO-OUT02A1](#) expansion board via the [STSW-IFAPGUI](#)

The STSW-IFAPGUI software architecture is based on a common engine and different plug-ins.

Each plug-in is designed to communicate with the application layer running on the microcontroller connected to the shield board.

The STSW-OUT02 contains all the necessary software routines allowing the full control of the [X-NUCLEO-OUT02A1](#) expansion board via the [STSW-IFAPGUI](#).

The control a stack of two [X-NUCLEO-OUT02A1](#) expansion boards configured in daisy chaining mode is also allowed.

Product summary	
Firmware for STSW-IFAPGUI and X-NUCLEO-OUT02A1 expansion board	STSW-OUT02
Industrial digital output expansion board based on ISO8200AQ for STM32 Nucleo	X-NUCLEO-OUT02A1
Graphical user interface for the industrial IPS and IO-Link transceivers evaluation boards based on STM32 Nucleo	STSW-IFAPGUI

Revision history

Table 1. Document revision history

Date	Version	Changes
26-Feb-2019	1	Initial release.

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