



```

58  * two consecutive write instructions in order to
59  * and the second one is to reset the COVSEL bits.
60
61  INLINE void CLK_Reset(void)
62  {
63
64      CLK->ICSR = CLK_ICSR_RESET_VALUE;
65      CLK->MCCR = CLK_MCCR_RESET_VALUE;
66      CLK->SNR = CLK_SNR_RESET_VALUE;
67      CLK->SRR = CLK_SRR_RESET_VALUE;
68      CLK->CHDIRV = CLK_CHDIRV_RESET_VALUE;
69      CLK->PCRNEN1 = CLK_PCRNEN1_RESET_VALUE;
70      CLK->PCRNEN2 = CLK_PCRNEN2_RESET_VALUE;
71      CLK->CCSR = CLK_CCSR_RESET_VALUE;
72      CLK->CCOR = CLK_CCOR_RESET_VALUE;
73      while (CLK->CCOR & CLK_CCOR_COVSEL)
74      {
75      }
76      CLK->CCOR = CLK_CCOR_RESET_VALUE;
77      CLK->WRITEREN = CLK_WRITEREN_RESET_VALUE;
78      CLK->BWINMCCR = CLK_BWINMCCR_RESET_VALUE;
79
80
81  INLINE void Lock_esprom_data_area()
82  {
83      // unlock esprom data zone write operati
84      FLASH->DOVR = FLASH_DOVR_RESET_VALUE;
85  }
86
87  INLINE void Dump_data_to_esprom(ui6 ramaddr, ui6 e
88  {
89      u8 *ramtmp = (u8*)ramaddr;
90      u8 *esepm = (u8*)esepmaddr;
91      u8 val = *ramtmp;
92
93      // wait previous write operation, if pending
94      while(!(FLASH->IAPSR & FLASH_IAPSR_BV0FF))
95      {
96      }
97      // check if WS is enabled
98      if (FLASH->IAPSR & FLASH_IAPSR_DWL) {
99          *ramtmp = (u8) (ramaddr++);  *esepm++;
100      }
101      // wait previous write operation, if pending
102      while(!(FLASH->IAPSR & FLASH_IAPSR_BV0FF))
103      {
104      }
105  }

```

Features

- Fully documented APIs
- Peripheral drivers, open source

Description

This package includes a device library intended to be used by the STLUX™ / STNRG digital platform.

The library provides a set of more than 100 application programming interfaces (API). It is intended to be used by software developers in order to have simple access to the controller and its peripherals.

For any detail please refer to the UM2001 - “Standard peripheral library for STLUX™ and STNRG digital controllers”.

Revision history

Table 1. Document revision history

Date	Revision	Changes
17-Feb-2017	1	Initial release.

INACTIVE - INACTIVE - INACTIVE

IMPORTANT NOTICE – PLEASE READ CAREFULLY

STMicroelectronics NV and its subsidiaries ("ST") reserve the right to make changes, corrections, enhancements, modifications, and improvements to ST products and/or to this document at any time without notice. Purchasers should obtain the latest relevant information on ST products before placing orders. ST products are sold pursuant to ST's terms and conditions of sale in place at the time of order acknowledgement.

Purchasers are solely responsible for the choice, selection, and use of ST products and ST assumes no liability for application assistance or the design of Purchasers' products.

No license, express or implied, to any intellectual property right is granted by ST herein.

Resale of ST products with provisions different from the information set forth herein shall void any warranty granted by ST for such product.

ST and the ST logo are trademarks of ST. All other product or service names are the property of their respective owners.

Information in this document supersedes and replaces information previously supplied in any prior versions of this document.

© 2017 STMicroelectronics – All rights reserved