



```
58  * two consecutive write instructions in order to
59  * and the second one is to reset the COOSEL bits.
60  */
61  INLINE void CLK_Reset(void)
62  {
63
64  CLK->ICSR = CLK_ICSR_RESET_VALUE;
65  CLK->MCCR = CLK_MCCR_RESET_VALUE;
66  CLK->SNR = CLK_SNR_RESET_VALUE;
67  CLK->SRR = CLK_SRR_RESET_VALUE;
68  CLK->SRRDRVR = CLK_SRRDRVR_RESET_VALUE;
69  CLK->PCRENR1 = CLK_PCENR1_RESET_VALUE;
70  CLK->PCRENR2 = CLK_PCENR2_RESET_VALUE;
71  CLK->CCSR = CLK_CCSR_RESET_VALUE;
72  CLK->CCOR = CLK_CCOR_RESET_VALUE;
73  while (CLK->CCOR & CLK_CCOR_COOR0)
74  {
75  CLK->CCOR = CLK_CCOR_RESET_VALUE;
76  CLK->WRITERDR = CLK_WRITERDR_RESET_VALUE;
77  CLK->BWMHCCR = CLK_BWMHCCR_RESET_VALUE;
78
79
80  }
81
82  }
83
84  }
85
86  }
87
88  }
89
90  }
91
92  }
93
94  }
95
96  }
97
98  }
99
100 }
```

Features

- Fully documented APIs
- Peripheral drivers, open source

Description

This package includes a device library intended to be used by the STLUX™ / STNRG digital platform.

The library provides a set of more than 100 application programming interfaces (API). It is intended to be used by software developers in order to have simple access to the controller and its peripherals.

For any detail please refer to the UM2001 - “Standard peripheral library for STLUX™ and STNRG digital controllers”.

Revision history

Table 1. Document revision history

Date	Revision	Changes
17-Feb-2017	1	Initial release.

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