
Writing to non-volatile memory without disrupting code execution on STM32L0/L1 Series software expansion for STM32Cube

Data brief

Features

- Continuous EEPROM programming during communication
- Configuration of EEPROM timing

Description

In ultra-low-power devices the Flash program memory and the data EEPROM share the same memory controller. For this reason the Flash memory is not accessible during data EEPROM programming. In this firmware package (X-CUBE-NVMRWW) the implementation example provided for STM32L0 and STM32L1 microcontrollers, shows how to deal with delays in execution caused by the memory interface sharing.

Revision history

Table 1. Document revision history

Date	Revision	Changes
13-Jun-2016	1	Initial release.

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