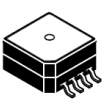
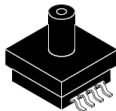


## 20 to 300 kPa, absolute, integrated, pressure sensor

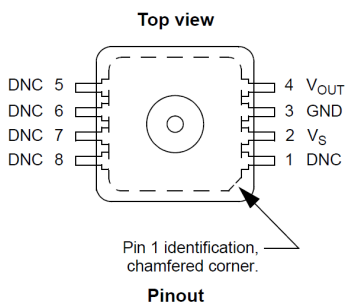
### Super small outline package



**MPXH6300A6U**  
Case 98ARH99066A



**MPXH6300AC6T1**  
Case 98ARH99089A



### Features

- Improved accuracy at high temperature
- Available in super small outline package
- 1.5% maximum error over 0 °C to 85 °C
- Fully calibrated and compensated
- Ideally suited for microprocessor or microcontroller-based systems
- Temperature compensated from -40 °C to +125 °C
- Durable thermoplastic surface mount package
- Package porting and mounting options enable tube attachment for liquefied natural gas (LPG) or remote sensing applications

### Applications

- Fuel injected car engines
- Vehicles powered by green gases (for example LPG and CNG)
- Small engines
- Industrial controls

### Description

ST's MPXH6300A series sensor integrates on-chip, bipolar op amp circuitry and thin film resistor networks to provide a high output signal and temperature compensation. The small form factor and high reliability of on-chip integration make the ST pressure sensor a logical and economical choice for the system designer.

The MPXH6300A series piezoresistive transducer is a state-of-the-art, monolithic, signal conditioned, silicon pressure sensor. This sensor combines advanced micromachining techniques, thin film metallization, and bipolar semiconductor processing to provide an accurate, high level analog output signal that is proportional to applied pressure.

# 1 Ordering information

**Table 1. Ordering information**

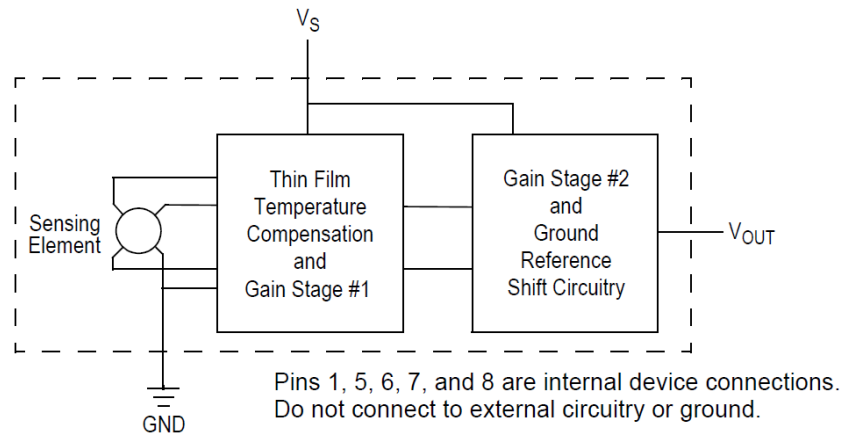
Part number	Shipping	Package	# of Ports				Pressure type		Device marking
			None	Single	Dual	Gauge	Differential	Absolute	
MPXH6300A6U	Rail	98ARH99066A	•					•	MPXH6300A
MPXH6300AC6T1	Tape and Reel	98ARH99089A		•				•	MPXH6300A

## 2 General description

### 2.1 Block diagram

Figure 1 shows a block diagram of the internal circuitry integrated on a pressure sensor chip.

Figure 1. Integrated pressure sensor block diagram



### 2.2 Pinout

Figure 2. Device pinout (top view)

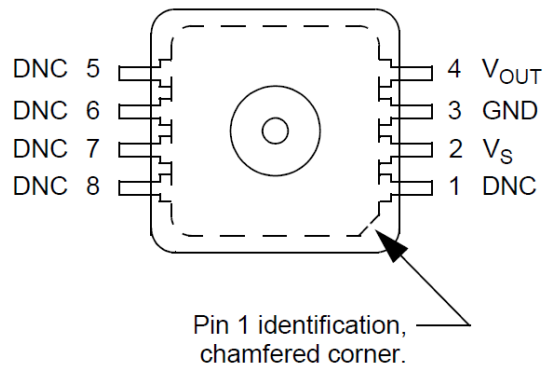


Table 2. Pin functions

Pin	Name	Function
1	DNC	Do not connect to external circuitry or ground. Pin 1 is notated by chamfered corner.
2	V <sub>S</sub>	Voltage supply
3	GND	Ground
4	V <sub>OUT</sub>	Output voltage
5	DNC	Do not connect to external circuitry or ground.
6	DNC	Do not connect to external circuitry or ground.
7	DNC	Do not connect to external circuitry or ground.
8	DNC	Do not connect to external circuitry or ground.

## 3 Mechanical and electrical specifications

### 3.1 Maximum ratings

**Table 3. Maximum ratings (1)**

Parametrics	Symbol	Value	Units
Maximum pressure (P1 > P2)	$P_{max}$	1200	kPa
Storage temperature	$T_{stg}$	-40 to +125	°C
Operating temperature	$T_A$	-40 to +125	°C
Output source current @ full-scale output(2)	$I_{o+}$	0.5	mAdc
Output sink current @ minimum pressure offset(2)	$I_{o-}$	-0.5	mAdc

- 1.Exposure beyond the specified limits may cause permanent damage or degradation to the device.
- 2.Maximum output current is controlled by effective impedance from  $V_{OUT}$  to GND or  $V_{OUT}$  to  $V_S$  in the application circuit.

### 3.2 Operating characteristics

**Table 4. Operating characteristics ( $V_S = 5.1 V_{DC}$ ,  $T_A = 25\text{ °C}$  unless otherwise noted, P1 > P2.)**

Characteristic	Symbol	Min	Typ	Max	Unit
Pressure range	$P_{OP}$	20	—	304	kPa
Supply voltage(1)	$V_S$	4.74	5.1	5.46	$V_{DC}$
Supply current	$I_O$	—	6.0	10	mAdc
Minimum pressure offset(2) (0 to 85 °C) @ $V_S = 5.1$ Volts	$V_{OFF}$	0.241	0.306	0.371	$V_{DC}$
Full-scale output(3) (0 to 85 °C) @ $V_S = 5.1$ Volts	$V_{FSO}$	4.847	4.912	4.977	$V_{DC}$
Full-scale span(4) (0 to 85 °C) @ $V_S = 5.1$ Volts	$V_{FSS}$	4.476	4.606	4.736	$V_{DC}$
Accuracy(5) (0 to 85 °C)	—	—	—	±1.5	% $V_{FSS}$
Sensitivity	V/P	—	16.2	—	mV/kPa
Response time(6)	$t_R$	—	1.0	—	ms
Warm-up time(7)	—	—	20	—	ms
Offset stability(8)	—	—	±0.25	—	% $V_{FSS}$

- 1.Device is ratiometric within this specified excitation range.
- 2.Offset ( $V_{OFF}$ ) is defined as the output voltage at the minimum rated pressure.
- 3.Full-scale output ( $V_{FSO}$ ) is defined as the output voltage at the maximum or full-rated pressure.
- 4.Full-scale span ( $V_{FSS}$ ) is defined as the algebraic difference between the output voltage at full-rated pressure and the output voltage at the minimum rated pressures
- 5.Accuracy is the deviation in actual output from nominal output over the entire pressure range and temperature range as a percent of span at 25 °C due to all sources of error including the following:  
 Linearity: Output deviation from a straight line relationship with pressure over the specified pressure range.  
 Temperature hysteresis:Output deviation at any temperature within the operating temperature range, after the temperature is cycled to and from the minimum or maximum operating temperature points, with zero differential pressure applied.

Pressure hysteresis: Output deviation at any pressure within the specified range, when this pressure is cycled to and from minimum or maximum rated pressure at 25 °C

TcSpan: Output deviation over the temperature range of 0 to 85 °C, relative to 25 °C.

TcOffset: Output deviation with minimum rated pressure applied, over the temperature range of 0 to 85 °C, relative to 25 °C. Variation from Nominal: The variation from nominal values, for offset or full-scale span, as a percent of  $V_{FSS}$ , at 25 °C.

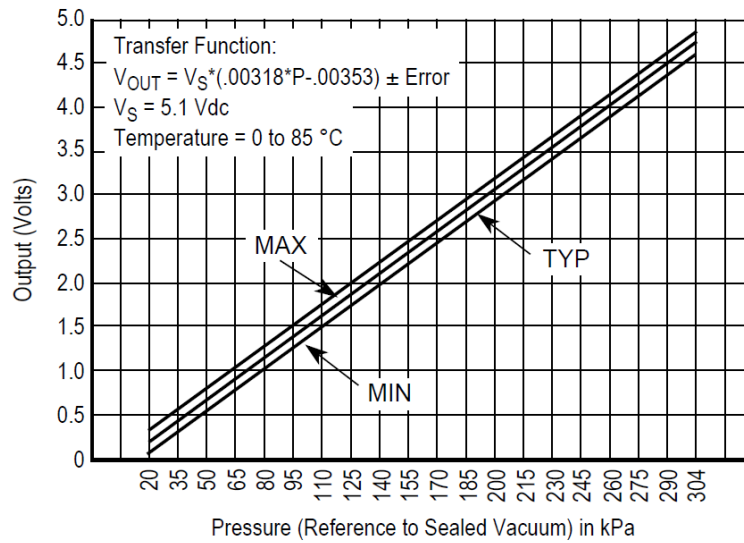
6. Response time is defined as the time for the incremental change in the output to go from 10% to 90% of its final value when subjected to a specified step change in pressure.

7. Warm-up time is defined as the time required for the product to meet the specified output voltage after the pressure has been stabilized.

8. Offset stability is the product's output deviation when subjected to 1000 cycles of pulsed pressure, temperature cycling with bias test.



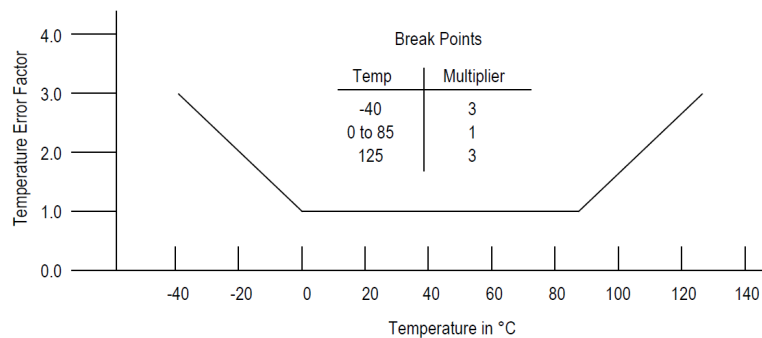
**Figure 5. Output vs. absolute pressure**



**Figure 6. Transfer function**

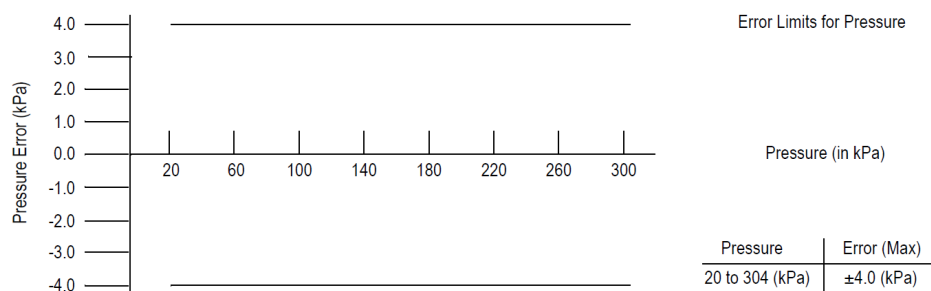
**Nominal Transfer Value:**  $V_{out} = V_S \times (0.00318 \times P - 0.00353)$   
 $\pm (\text{Pressure Error} \times \text{Temp Factor} \times 0.00318 \times V_S)$   
 $V_S = 5.1 \pm 0.36 \text{ Vdc}$

**Figure 7. Temperature error band**



NOTE: The Temperature Multiplier is a linear response from 0 °C to -40 °C and from 85 °C to 125 °C.

**Figure 8. Pressure error band**

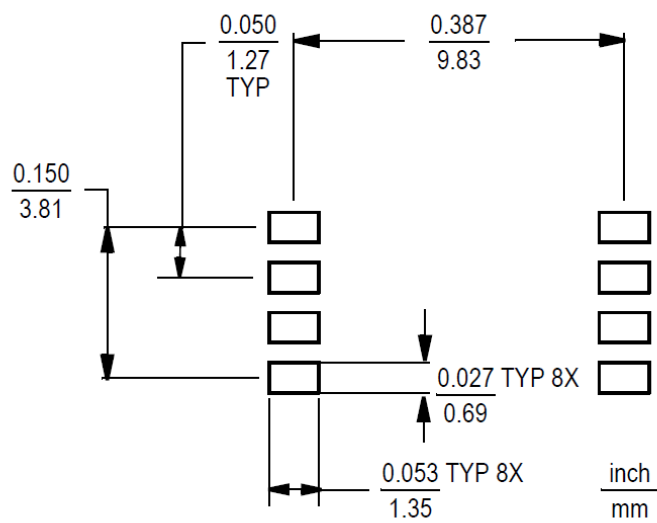


## 5 Package information

### 5.1 Minimum recommended footprint for surface mounted applications

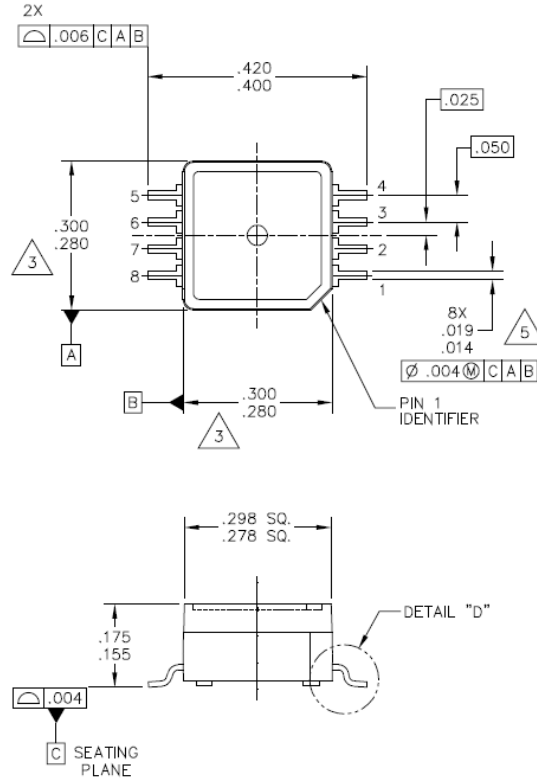
Surface mount board layout is a critical portion of the total design. The footprint for the semiconductor package must be the correct size to ensure proper solder connection interface between the board and the package. With the correct pad geometry, the packages will self-align when subjected to a solder reflow process. It is always recommended to fabricate boards with a solder mask layer to avoid bridging and/or shorting between solder pads, especially on tight tolerances and/or tight layouts.

Figure 9. SSOP footprint



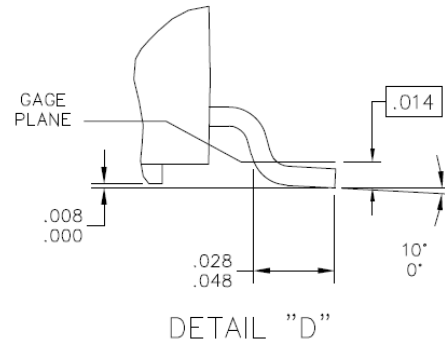
## 6 Package Dimensions

Figure 10. Case 98ARH99066A, super small outline package, surface mount



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TITLE:	8 LEAD SSOP	DOCUMENT NO: 98ARH99066A	REV: H		
		CASE NUMBER: 1317-04	13 APR 2012		
		STANDARD: NON-JEDEC			

Figure 11. Case 98ARH99066A, super small outline package, surface mount



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TITLE: 8 LEAD SSOP	DOCUMENT NO: 98ARH99066A	REV: H	
	CASE NUMBER: 1317-04	13 APR 2012	
	STANDARD: NON-JEDEC		

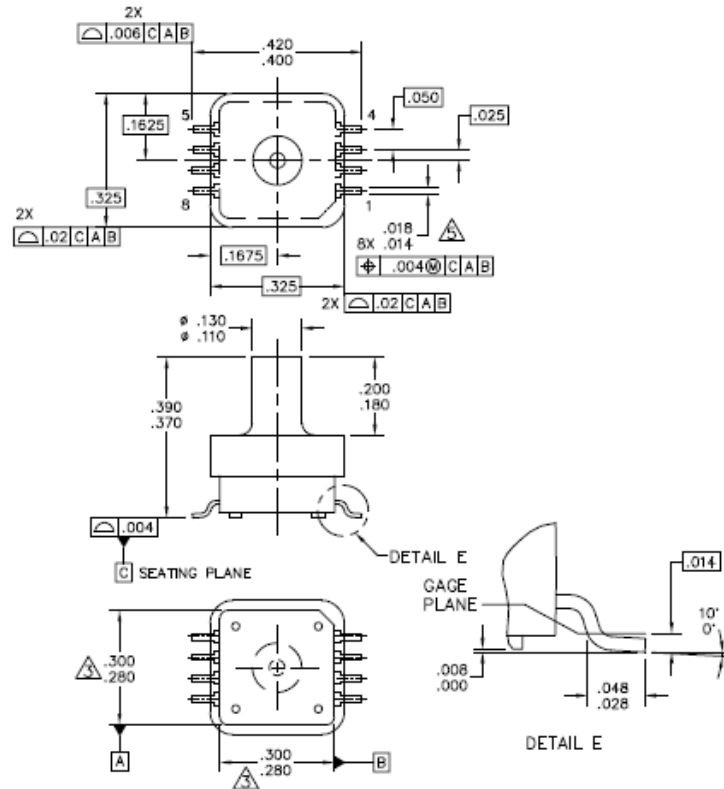
Figure 12. Case 98ARH99066A, super small outline package, surface mount

NOTES:

1. ALL DIMENSIONS IN INCHES.
2. DIMENSIONING AND TOLERANCING PER ASME Y14.5M-1994.
3. DIMENSIONS DO NOT INCLUDE MOLD FLASH OR PROTRUSIONS. MOLD FLASH OR PROTRUSION SHALL NOT EXCEED .006 INCHES PER SIDE.
4. ALL VERTICAL SURFACES TO BE 5° MAXIMUM.
5. DIMENSION DOES NOT INCLUDE DAMBAR PROTRUSION. ALLOWABLE DAMBAR PROTRUSION SHALL BE .008 INCHES MAXIMUM.

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	CASE NUMBER: 1317-04	13 APR 2012	
	STANDARD: NON-JEDEC		

Figure 13. Case 98ARH99089A, super small outline package, surface mount



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	STANDARD: NON-JEDEC	
		09 NOV 2012

Figure 14. Case 98ARH99089A, super small outline package, surface mount

NOTES:

1. ALL DIMENSIONS IN INCHES.
2. DIMENSIONING AND TOLERANCING PER ASME Y14.5M-1994.
3. DIMENSIONS DO NOT INCLUDE MOLD FLASH OR PROTRUSIONS.  
MOLD FLASH OR PROTRUSION SHALL NOT EXCEED .006 INCHES PER SIDE.
4. ALL VERTICAL SURFACES TO BE 5° MAXIMUM.
5. DIMENSION DOES NOT INCLUDE DAMBAR PROTRUSION.  
ALLOWABLE DAMBAR PROTRUSION SHALL BE .008 INCHES MAXIMUM.

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	STANDARD: NON-JEDEC	
		09 NOV 2012

## Revision history

**Table 5. Document revision history**

Date	Revision	Changes
17-Jun-2026	1	Initial release from ST, rebranded NXP document

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