

Board identification

This errata sheet applies to some batches of the STEVAL-SMARTAG1 evaluation board.
See the information below for details.

Table 1. Board summary

Order code	Device info
STEVAL-SMARTAG1	C11 at 470 nF

1 Limitation

1.1 Description

In the [STEVAL-SMARTAG1](#) evaluation board, the ST25DV is powered through the STM32L031 GPIO pin (VDD_EEPROM).

The SDA and SCL lines are pulled up to VDD_EEPROM using 47 K resistors.

A decoupling capacitor (C11) is used to reduce high frequency power noise at the ST25DV VCC input.

In some of the STEVAL-SMARTAG1 earlier batches, this capacitor is at 470 nF which is a value too high when VDD_EEPROM is turned on and off for power saving.

The resulting VDD_EEPROM long rise time (in the order of 100 μ S) causes erroneous I²C communication.

1.2 Workaround

Change C11 value to 100 nF to give adequate high frequency noise suppression while providing fast enough rise time for VDD_EEPROM.

Revision history

Table 2. Document revision history

Date	Revision	Changes
16-Oct-2018	1	Initial release.

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