

Introduction

This errata sheet describes functional and electrical problems of the SPC564A74x, SPC564A80x devices.

The reference documentation is reported in [Chapter 1: Reference document](#)

Device identification for cut 3.0:

- JTAG_ID = 0x2AE0_3041
- MIDR1 register:
 - MAJOR_MASK[3:0]: 4'b0010
 - MINOR_MASK[3:0]: 4'b0000

Errata are contained in Microsoft Excel[®] workbook files attached to this document.

Locate the paper-clip symbol on the left side of the PDF window, and click it. Double-click on the Excel file to open it.

1 Reference document

Table 1. Reference documents

Doc name	Title	Revision
RM0029	SPC564A74xx, SPC564A80xx 32-bit MCU family built on the embedded Power Architecture®	8
DS6103	32-bit MCU family built on the embedded Power Architecture®	9

Revision history

Table 2. Document revision history

Date	Revision	Changes
11-Jul-2014	1	Initial release
28-Jul-2021	2	New Errata DAN-0055953

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