
SPC584Nx, SPC58ENx, SPC58NNx devices errata JTAG_ID = 0x2011_2041

Introduction

This errata sheet describes functional and electrical problems of the SPC584Nx, SPC58ENx, SPC58NNx devices. The reference documentation is reported in [Section 1 Reference documents](#).

Device identification for cut 1.2:

- JTAG_ID = 0x2011_2041
- MIDR1 register:
 - MAJOR_MASK[3:0]: 4'b0000
 - MINOR_MASK[3:0]: 4'b0010

Errata and BAF related errata are contained in Microsoft Excel[®] workbook files attached to this document.

Locate the paper clip symbol on the left side of the PDF window, and click it. Double-click on the Excel file to open it.

1 Reference documents

Table 1. Reference documents

	Title	Revision
RM0421	RM - SPC58xNx 32-bit Power Architecture® microcontroller for automotive ASILD applications	5
DS11734	DS - SPC58xNx 32-bit Power Architecture® microcontroller for automotive ASIL-D applications	6

Revision history

Table 2. Document revision history

Date	Version	Changes
13-Jan-2019	1	Initial release.
23-Jan-2019	2	Typo correction
7-Jun-2019	3	New Errata: DAN-0049480 DAN-0049907 DAN-0048189 Removed errata: DAN-0041154 DAN-0047602 DAN-0048196
21-Jan-2020	4	New Errata: DAN-0049972 (Security) DAN-0050273 DAN-0050583 DAN-0050632 ERR010436
06-Jul-2020	5	New Errata: DAN-0051367 DAN-0051392 ERR009658 ERR010542 Reworded: DAN-0048189 DAN-0050632
15-Feb-2023	6	Updated Table 1. Reference documents New Errata: DAN-0051663 DAN-0052753 DAN-0052755 DAN-0053230 DAN-0053830 DAN-0053968 (security) DAN-0055017 DAN-0055143 DAN-0055935 DAN-0055936 DAN-0056426 DAN-0057876 DAN-0058270 DAN-0058271 DAN-0058690 DAN-0058847

Date	Version	Changes
		DAN-0059304 DAN-0059884 Reworded: PS869 DAN-0050632

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