

Introduction

This errata sheet describes functional and electrical problems of the SPC58xCx devices.

The reference documentation is reported in [Chapter 1: Reference documents](#).

Device identification for cut 2.0:

- JTAG_ID = 0x0114_2041
- MIDR1 register:
 - MAJOR_MASK[3:0]: 4'b0001
 - MINOR_MASK[3:0]: 4'b0000

Errata and BAF related errata are contained in Microsoft Excel[®] workbook files attached to this document.

Locate the paper clip symbol on the left side of the PDF window, and click it. Double-click on the Excel file to open it.

1 Reference documents

Table 1. Reference documents

Doc name	ID	Title	Revision
RM0407	028117	SPC58 C Line - 32 bit Power Architecture automotive MCU Dual z4 cores 180 MHz, 4 MBytes Flash, HSM, ASIL-B	7
DS11620	029264	SPC58 C Line - 32 bit Power Architecture automotive MCU Dual z4 cores 180 MHz, 4 MBytes Flash, HSM, ASIL-B	7

Revision history

Table 2. Document revision history

Date	Revision	Changes
03-Jul-2020	1	Initial release - Differences against ES0400 Rev 5 cut 1.1 New Errata DAN-0051761 DAN-0051763 Fixed errata PS1891 PS2206 PS3022 DAN-0042615 DAN-0043003 DAN-0043294 DAN-0043334 DAN-0047977 DAN-0047979 DAN-0048760 Reworded DAN-0044087

Table 2. Document revision history (continued)

Date	Revision	Changes
22-Mar-2021	2	New Errata
		DAN-0051663 DAN-0052753 DAN-0053230 DAN-0053830 DAN-0053968 DAN-0054221 DAN-0055017 DAN-0055037 DAN-0055170
		Removed Errata
		DAN-0049559
		DAN-0049777
		DAN-0049821
		DAN-0050087
		DAN-0050171
		DAN-0050590
		DAN-0051637
		DAN-0051841
		DAN-0052302
		DAN-0052304
		DAN-0052305
		DAN-0052319
		DAN-0052320
		DAN-0052321
		DAN-0052349
		DAN-0052350
		DAN-0052351
		DAN-0052354
		DAN-0052387
		DAN-0052398
		DAN-0052404
		DAN-0052499
		DAN-0052512
		DAN-0052808
DAN-0052810		
09-Apr-2021	3	Updated Table 1: Reference documents .

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